

RKD705WKKR

Silicon Epitaxial Schottky Barrier Diode
for Detector

R07DS0330EJ0100
Rev.1.00
Apr 06, 2011

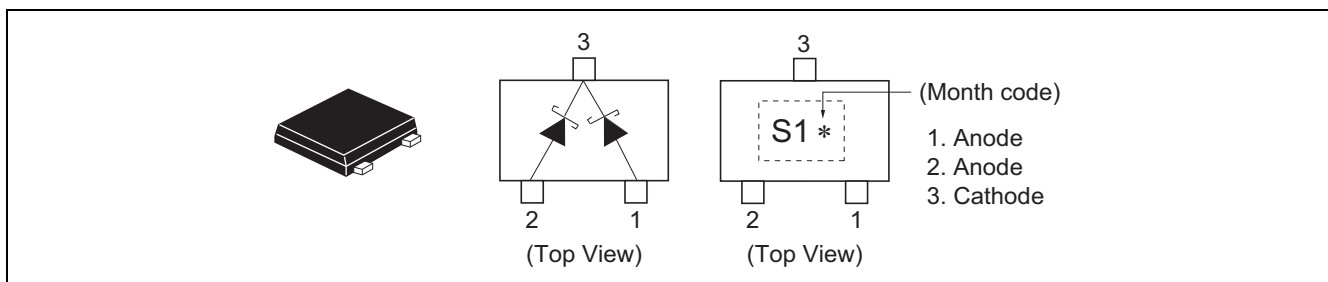
Features

- Low forward voltage, Low capacitance.
- VSON-3 Package is suitable for high density surface mounting.

Ordering Information

Part No	Laser Mark	Package Name	Package Code	Taping Abbreviation (Quantity)
RKD705WKKR H RKD705WKKR P	S1	VSON-3	PUSN0003KA-A	H (3,000pcs / reel) P (3,000pcs / reel)

Pin Arrangement



Month Code

Month of Manufacture	Assemble
	MALAYSIA
January	1
February	2
March	3
April	4
May	5
June	6

Month of Manufacture	Assemble
	MALAYSIA
July	7
August	8
September	9
October	W
November	X
December	Y

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Peak reverse voltage	V_{RM}	40	V
Reverse voltage	V_R	40	V
Average rectified current	I_O^{*1}	30	mA
Non-Repetitive peak forward surge current	I_{FSM}^{*2}	200	mA
Junction temperature	T_J	125	°C
Storage temperature	T_{stg}	-55 to +125	°C

- Notes: 1. Per one device.
 2. $t = 10$ ms sin wave 1 pulse.

Electrical Characteristics *1

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Forward voltage	V_F	—	—	0.33	V	$I_F = 1$ mA
Reverse current	I_R	—	—	1.0	μA	$V_R = 10$ V
Capacitance	C	—	—	1.8	pF	$V_R = 1$ V, $f = 1$ MHz
ESD-Capability *2	—	100	—	—	V	C = 200 pF, $R_L = 0 \Omega$, Both forward and reverse direction 1 pulse.

- Notes: 1. Per one device.
 2. Failure criterion ; $I_R > 2$ μA at $V_R = 10$ V

Main Characteristics

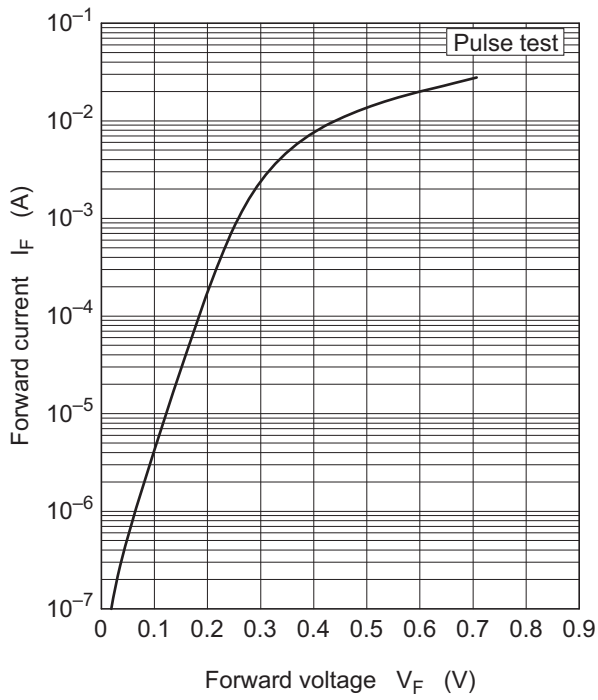


Fig.1 Forward current vs. Forward voltage

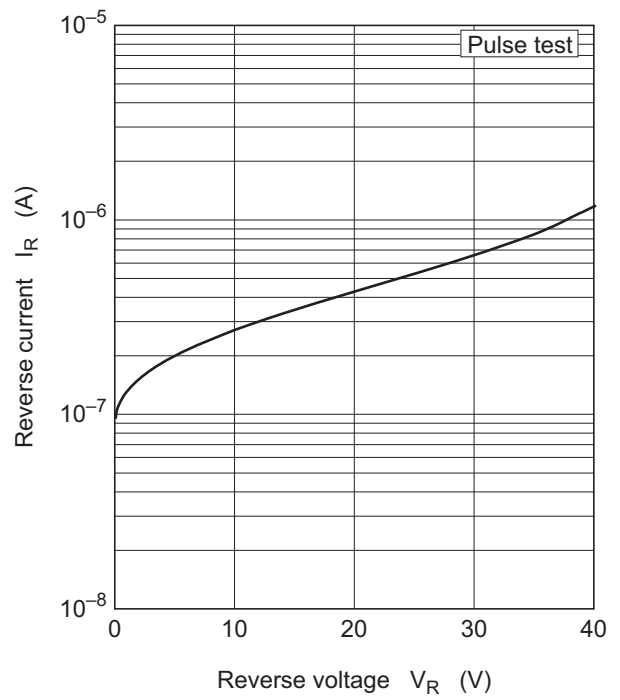


Fig.2 Reverse current vs. Reverse voltage

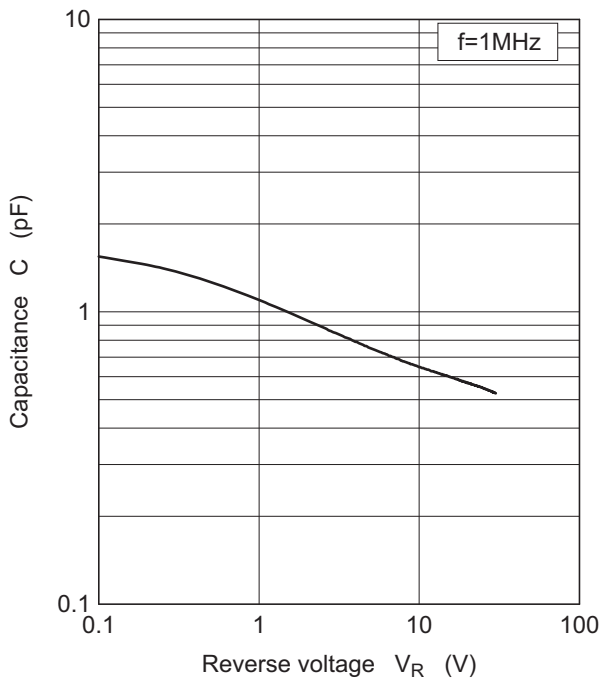
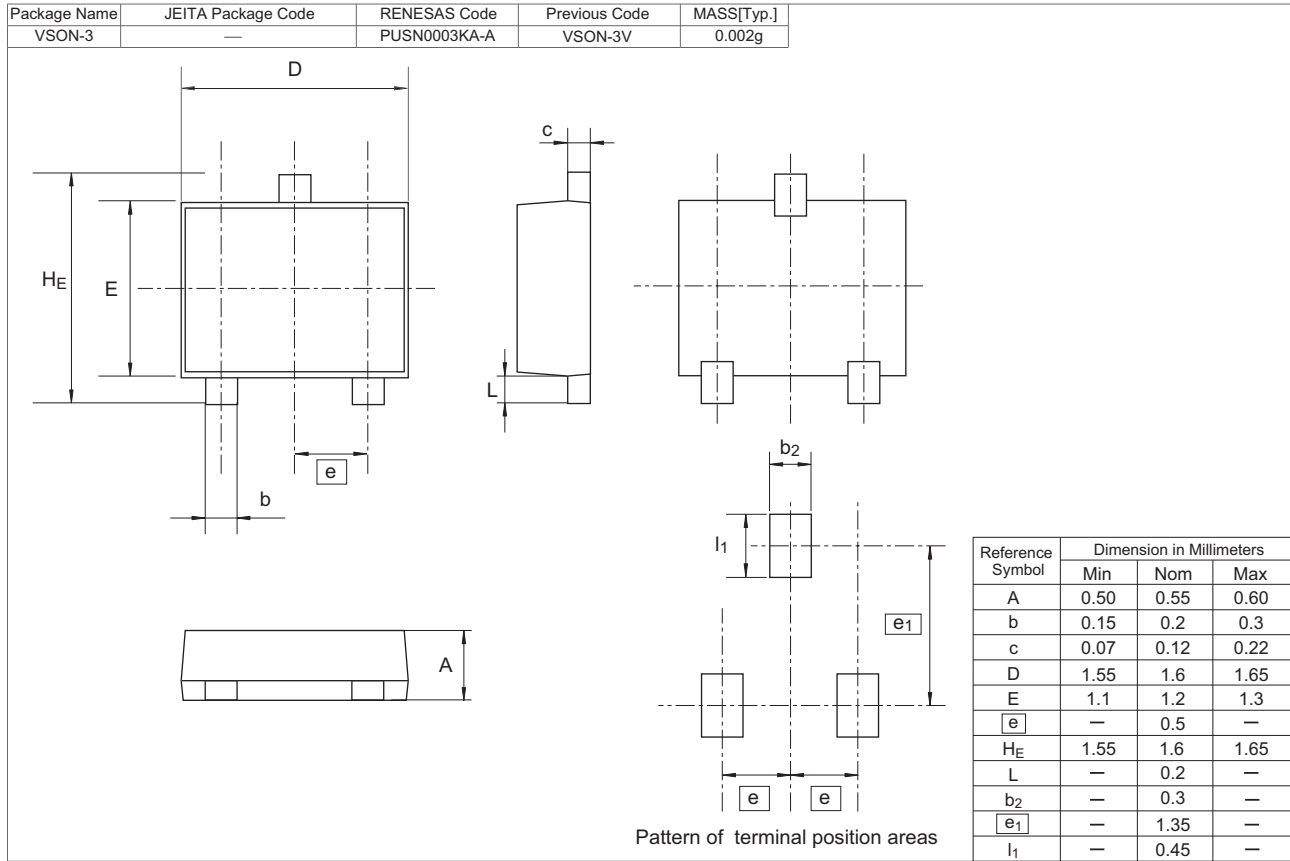


Fig.3 Capacitance vs. Reverse voltage

Package Dimensions



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Renesas Electronics America Inc.
2880 Scott Boulevard Santa Clara, CA 95050-2554, U.S.A.
Tel: +1-408-586-6000, Fax: +1-408-586-6130

Renesas Electronics Canada Limited
1101 Nicholson Road, Newmarket, Ontario L3Y 9C3, Canada
Tel: +1-905-898-5441, Fax: +1-905-898-3220

Renesas Electronics Europe Limited
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.
Tel: +44-1628-585-100, Fax: +44-1628-585-900

Renesas Electronics Europe GmbH
Arcadiastrasse 10, 40472 Düsseldorf, Germany
Tel: +49-211-65030, Fax: +49-211-6503-1327

Renesas Electronics (China) Co., Ltd.
7th Floor, Quantum Plaza, No.27 ZhiChunLu Haidian District, Beijing 100083, P.R.China
Tel: +86-10-8235-1155, Fax: +86-10-8235-7679

Renesas Electronics (Shanghai) Co., Ltd.
Unit 204, 205, AZIA Center, No.1233 Lujiazui Ring Rd., Pudong District, Shanghai 200120, China
Tel: +86-21-5877-1818, Fax: +86-21-6887-7858 / -7898

Renesas Electronics Hong Kong Limited
Unit 1601-1613, 16/F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong
Tel: +852-2886-9318, Fax: +852 2886-9022/9044

Renesas Electronics Taiwan Co., Ltd.
13F, No. 363, Fu Shing North Road, Taipei, Taiwan
Tel: +886-2-8175-9600, Fax: +886 2-8175-9670

Renesas Electronics Singapore Pte. Ltd.
1 HarbourFront Avenue, #06-10, Keppel Bay Tower, Singapore 098632
Tel: +65-6213-0200, Fax: +65-6276-8001

Renesas Electronics Malaysia Sdn.Bhd.
Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No. 18, Jin Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia
Tel: +60-3-7955-9390, Fax: +60-3-7955-9510

Renesas Electronics Korea Co., Ltd.
11F., Samik Lavied' or Bldg., 720-2 Yeoksam-Dong, Kangnam-Ku, Seoul 135-080, Korea
Tel: +82-2-558-3737, Fax: +82-2-558-5141