

### **DESCRIPTION**

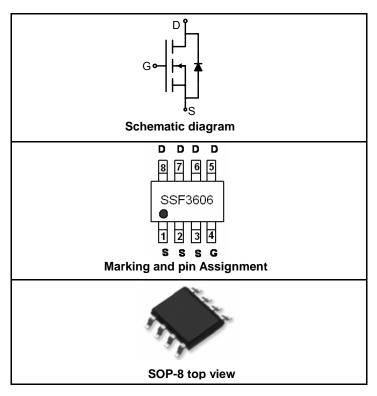
The SSF3606 uses advanced trench technology to provide excellent  $R_{\text{DS(ON)}}$  and low gate charge .This device is suitable for use as a load switch or in PWM applications.

## **GENERAL FEATURES**

- $V_{DS} = 30V, I_D = 15A$   $R_{DS(ON)} < 8.5mΩ @ V_{GS} = 4.5V$  $R_{DS(ON)} < 6mΩ @ V_{GS} = 10V$
- High Power and current handing capability
- Lead free product is acquired
- Surface Mount Package

# **Application**

- ●PWM applications
- Load switch
- Power management



#### PACKAGE MARKING AND ORDERING INFORMATION

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
SSF3606	SSF3606	SOP-8	Ø330mm	12mm	3000 units

ABSOLUTE MAXIMUM RATINGS(TA=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	30	V
Gate-Source Voltage	V <sub>G</sub> s	±20	V
	I <sub>D</sub> (25℃)	15	Α
Drain Current-Continuous@ Current-Pulsed (Note 1)	I <sub>D</sub> (70℃)	12.5	Α
	I <sub>DM</sub>	60	Α
Maximum Power Dissipation	P <sub>D</sub>	2	W
Operating Junction and Storage Temperature Range	$T_{J}, T_{STG}$	-55 To 150	$^{\circ}$ C

### THERMAL CHARACTERISTICS

Thermal Resistance,Junction-to-Ambient (Note 2)	$R_{\theta JA}$	62.5	°C/W	l
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ELECTRICAL CHARACTERISTICS (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	30			V



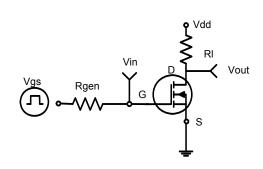
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =30V,V <sub>GS</sub> =0V		1	μΑ	
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V			±100	nA
ON CHARACTERISTICS (Note 3)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =250μA	1.3	1.7	2.5	V
Drain-Source On-State Resistance	В	V <sub>GS</sub> =4.5V, I <sub>D</sub> =11.5A		6.4	8.5	mΩ
Dialii-Source Oii-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =15A V <sub>DS</sub> =5V,I <sub>D</sub> =11A V <sub>DS</sub> =15V,V <sub>GS</sub> =0V, F=1.0MHz		4.8	6	mΩ
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =5V,I <sub>D</sub> =11A		25		S
DYNAMIC CHARACTERISTICS (Note4)						
Input Capacitance	C <sub>lss</sub>			3100		PF
Output Capacitance	Coss			550		PF
Reverse Transfer Capacitance	C <sub>rss</sub>			300		PF
SWITCHING CHARACTERISTICS (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>			19		nS
Turn-on Rise Time	t <sub>r</sub>	$V_{DS}$ =15V, $V_{GS}$ =10V, $R_{GEN}$ =6 $\Omega$		11		nS
Turn-Off Delay Time	t <sub>d(off)</sub>	I <sub>D</sub> =1A 60 25			nS	
Turn-Off Fall Time	t <sub>f</sub>			25		nS
Total Gate Charge	Qg			50		nC
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =15V,I <sub>D</sub> =15A,V <sub>GS</sub> =10V		8		nC
Gate-Drain Charge	Q <sub>gd</sub>	]		15		nC
Body Diode Reverse Recovery Time	T <sub>rr</sub>	1 -154 dl/dt-1004/:		20		nS
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			5		nC
DRAIN-SOURCE DIODE CHARACTERISTIC	S	•		•		
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =2.8A		0.75	1.2	V

# **NOTES:**

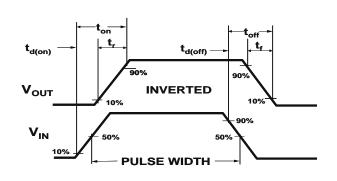
- Repetitive Rating: Pulse width limited by maximum junction temperature.
  Surface Mounted on 1in² FR4 Board, t ≤ 10 sec.
  Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
  Guaranteed by design, not subject to production testing.



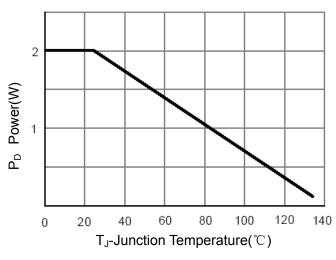
# TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS



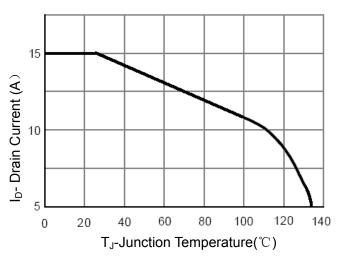
**Figure 1:Switching Test Circuit** 



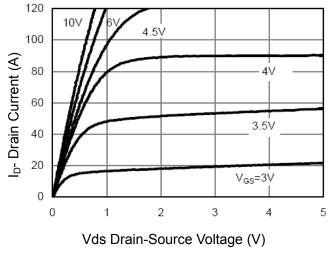
**Figure 2:Switching Waveforms** 



**Figure 3 Power Dissipation** 



**Figure 4 Drain Current** 



**Figure 5 Output CHARACTERISTICS** 

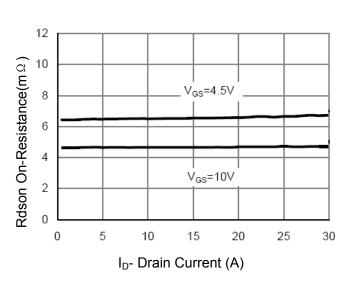
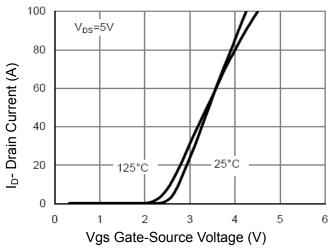


Figure 6 Drain-Source On-Resistance





**Figure 7 Transfer Characteristics** 

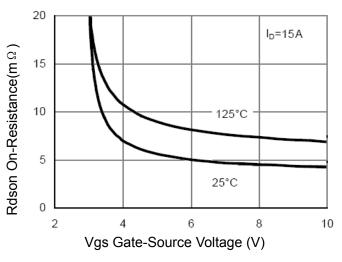


Figure 9 Rdson vs Vgs

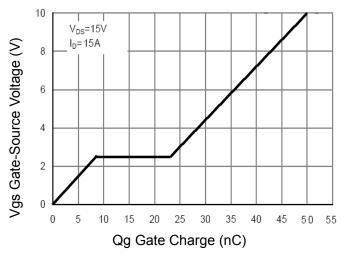


Figure 11 Gate Charge

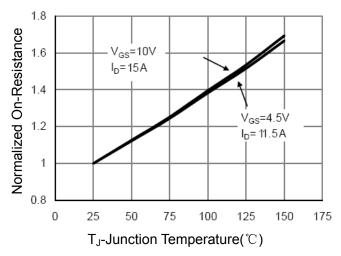


Figure 8 Drain-Source On-Resistance

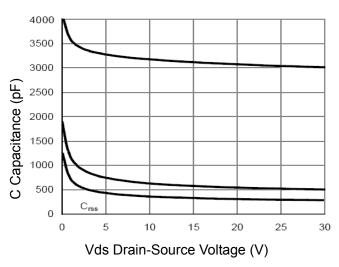


Figure 10 Capacitance vs Vds

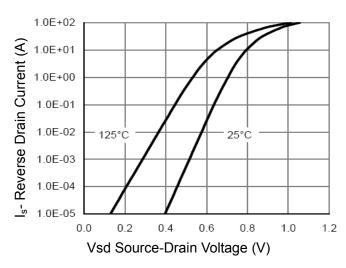


Figure 12 Source- Drain Diode Forward



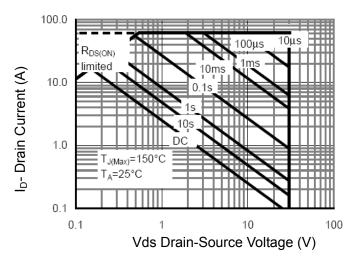
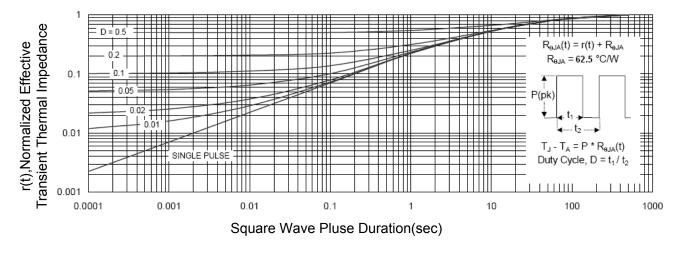


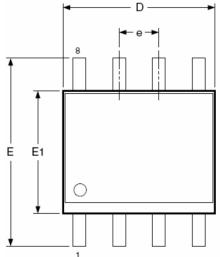
Figure 13 Safe Operation Area

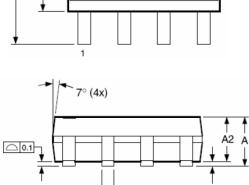


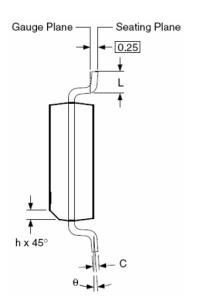
**Figure 14 Normalized Maximum Transient Thermal Impedance** 



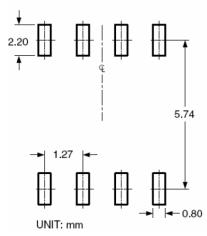
# **SOP-8 PACKAGE INFORMATION**







### RECOMMENDED LAND PATTERN



Symbols	Min.	Nom.	Max.
Α	1.35	1.65	1.75
A1	0.10	_	0.25
A2	1.25	1.50	1.65
b	0.31	_	0.51
С	0.17	_	0.25
D	4.80	4.90	5.00
E4	2 90	2.00	4.00

Dimensions in millimeters

D	4.80	4.90	5.00			
E1	3.80	3.90	4.00			
е	1.27 BSC					
Ε	5.80	6.00	6.20			
h	0.25	_	0.50			
L	0.40	_	1.27			
θ	0°	_	8°			

Dim	ensions	in	inc	hes
ווווט	611210112	111	IIIC	1162

Symbols	Min.	Nom.	Max.
Α	0.053	0.065	0.069
A1	0.004	_	0.010
A2	0.049	0.059	0.065
b	0.012	_	0.020
С	0.007	_	0.010
D	0.189	0.193	0.197
E1	0.150	0.154	0.157
е	0	.050 BS	С
Ε	0.228	0.236	0.244
h	0.010	_	0.020
L	0.016	_	0.050
θ	<b>0</b> °	_	8°

# NOTES:

- Dimensions are inclusive of plating
  Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 6 mils.
- 3. Dimension L is measured in gauge plane.
- 4. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.



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