

SMD Quartz Crystal Resonator

1. QUARTZ CRYSTAL UNIT SPECIFICATION

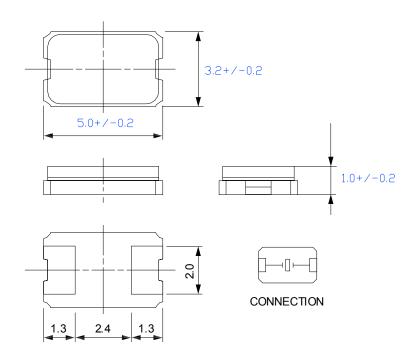
Item	Symbol	Specifications			Remark	
item		Min	Туре	Max	Units	Remark
1. Production type		SMD Quartz Crystal Resonator				
2. Holder		SMD	SMD 5032		Glass Seal	
3. Mode of oscillation		■ Fundamental □ 3 Overtone □ 5 Overtone				
4. Frequency	FL	2	7.00000	00	MHz	
5. Load capacitance	CL	CL 20		pF		
6. Frequency tolerance	Tol	± 30		ppm	at 25℃ ± 3℃	
7. Equivalent resistance	Rs 40			Ω	Max.	
8. Working temperature range	TR	-1	10 ~ 6	60	$^{\circ}$	
9. Freq. Temp. Characteristics	TC		± 30		ppm	working temperature ΔF
10. Drive level	DL		100		μW	
11. Shunt Capacitance	C0	5		pF		
12. Storage temperature range		-4	10 ~ 8	35	$^{\circ}$	
13. Insulation resistance	Insulation resistance 500			ΜΩ	Min.	
14. Measure Circuit		S	&A 250	В		π network
15. Aging			5		ppm/Yr	Max.



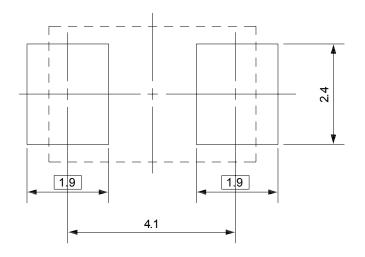
SMD Quartz Crystal Resonator

2. MARKING & DIMENSIONS

2.1 DIMENSIONS (Unit: mm)



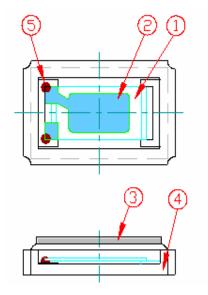
2.2 Recommended Land Pattern (Unit: mm)



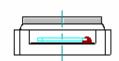


SMD Quartz Crystal Resonator

2.3 INSIDE STRUCTURE



DESCRIPTION				
1	Quartz Blank			
2	Electrode			
3	Lid			
4	Base			
5	Conductive adhesive			



2.4 MARKING

LGE 27.000MA2



SMD Quartz Crystal Resonator

3. CHARACTERISTICS

Units and values indicated with {} in this specification are the former units and the specified values.

Standard atmospheric conditions:

Unless otherwise specified, the standard range of atmospheric conditions for making measurements and tests is as follows:

Ambient temperature: 15℃ to 35℃

Relative humidity: 25% to 85%

Air pressure: 86 to 106 kPa

If there is any doubt about the results, measurements shall be made within the following limits:

Ambient temperature : 25 ℃ ±1 ℃

Relative humidity: 63% to 67%

Air pressure: 86 to 106 kPa

Operating temperature range:

The operating temperature range is the range of ambient temperatures at which the quartz crystal oscillator can be stored without damage. Conditions are as specified elsewhere on these specifications.

Operating temperature range: -10°C to +60°C

Storage temperature range:

The storage temperature range is the range of ambient temperatures at which the quartz crystal oscillator can be stored without damage. Conditions are as specified elsewhere on these specifications.

Storage temperature range: -40°C to +85°C



SMD Quartz Crystal Resonator

3.1 Mechanical characteristics

Provided that measurement shall be carried out after letting it alone in the room temperature for 1h.

Item		Specifications				
1	Drop Test	Dropping 3 times from the height of 75cm onto hard wooden board of thickness more than 30mm.				
2	Mechanical Shock	Devices are shocked to half sine wave (1000 G) three mutually perpendicular axes each 3 times.				
3 Vibration		(1) Vibration Frequency	10~55Hz			
		(2) Cycle	1 to 2 min			
	Vibration	(3) Amplitude	1.0mm			
		(4) Direction	X.Y.Z			
		(5) Time	2hr for each direction			
		MIL - STD - 20E Method 208C				
4 Solderability		Temperature 245℃±5℃				
	Soldorability	Material H63A (Silver 2 ~ 3 %)				
	Solderability	Immersion depth 0.5 mm minimum				
		Immersion time 3 ± 0	3 ± 0.5 seconds			
		Flux Rosii	n resin methyl alcohol solvent (1 : 4)			
5	Resistance to Soldering Heat	MIL - SLD -202, Method 210, Condition I or J 10 sec immersion into 260 ± 5°C solder pot, above 180°C is 90 ~ 120 sec.				

^{*}Frequency variation shall be within +/-5ppm and equivalent resistance less than ±15% max. after the test.

Note: Measuring the frequency should be done after keeping test samples at room temperature for 24 hours.



SMD Quartz Crystal Resonator

3.2 Environmental characteristics

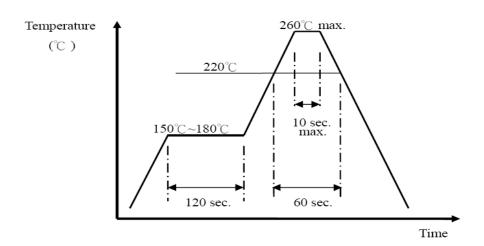
Provided that measurement shall be carried out after letting it alone in the room temperature for 1h.

Item		Specifications			
1	Humidity	It alone at 40 ℃± 2℃ in humidity of 90~95% for 48h.			
2	Storage in Low Temperature	It alone at −40 ℃± 2℃ for 240h.			
3	Storage in High Temperature It alone at 85 ℃ ± 2 ℃ for 240h.				
4	Temperature Cycle The following temperature cycle (10 cycles). Refer to below Fig. Temperature cycle Cycle Shift from low to high, high to low shall be done in 1°C/min.				
	25+/-5°C 2 min	25+/-5°C 2 min 1 Cycle			

 $^{^*}$ Frequency variation shall be within $^+$ /-5ppm and equivalent resistance less than $\pm 15\%$ max. after the test.

Note: Measuring the frequency should be done after keeping test samples at room temperature for 24 hours.

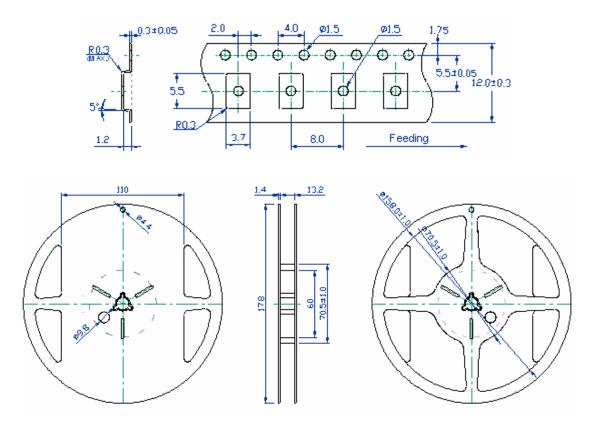
3.3 SUGGESTED IR REFLOW PROFILE



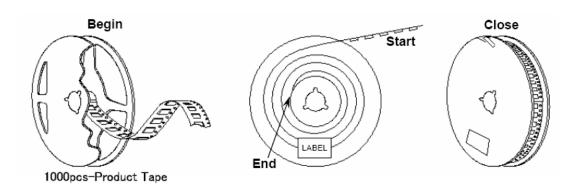


SMD Quartz Crystal Resonator

4. PACKING



- 1. 230mm minimum leafer which consist of carrier and tape followed by a minimum of 160mm of empty carrier tape sealed with cover tape.
- 2. 160mm minimum trailer of empty carrier tape sealed with cover tape.





SMD Quartz Crystal Resonator

5. Note

- 1. Only the lead should be heated when soldering.
 - In case that the package temperature is exceeding 150°C, it may impair the crystal or may cause the crystal quartz to destroy.
- 2. Pulling the lead strongly may cause cracking of the hermetic grass seal bending the lead closely from the case may also cause same problem, so when the lead needs to be bent, please leave move than 05.mm of lead from the case.
- 3. Too much shock or vibration is not allowed. According to conditions such as machine shock during the assembly, the internal quartz crystal might be damaged.
 - Please check your conditions carefully when using it in advance.
- 4. Don't storage or use in the environment that temperature may change rapidly to avoid the condensation. And also we recommend to storage the products in the normal environment. (Temperature, humidity)
- 5. This product can be subjected to ultrasonic cleaning. However, since the oscillator may be affected depending on the condition, be sure to check it.
- 6. Applying excessive drive level to the quartz crystal may cause deterioration for characteristics or damage.
 - Circuit design must be such as to maintain a proper drive level.
- 7. Unless adequate negative resistance is allocated in the oscillation circuit, startup time of oscillation may be increased or no oscillation may occur. In order to avoid this, provide enough negative resistance in the circuitry design.