Preferred Device

Silicon Controlled Rectifiers

Reverse Blocking Thyristors

Designed primarily for half-wave ac control applications, such as motor controls, heating controls and power supplies.

- Glass Passivated Junctions with Center Gate Geometry for Greater Parameter Uniformity and Stability
- Small, Rugged, Thermowatt Construction for Low Thermal Resistance, High Heat Dissipation and Durability
- Blocking Voltage to 800 Volts
- Device Marking: Logo, Device Type, e.g., 2N6394, Date Code

*MAXIMUM RATINGS (T_J = $25^{\circ}C$ unless otherwise noted)

Rating	Symbol	Value	Unit				
Peak Repetitive Off–State Voltage ⁽¹⁾ (T _J = -40 to 125°C, Sine Wave, 50 to 60 Hz, Gate Open)	V _{DRM,} V _{RRM}	50	Volts				
2N6394 2N6395 2N6397 2N6399		50 100 400 800					
On-State RMS Current (180° Conduction Angles; T _C = 90°C)	IT(RMS)	12	A				
Peak Non-Repetitive Surge Current (1/2 Cycle, Sine Wave, 60 Hz, $T_J = 125^{\circ}C$)	ITSM	100	A				
Circuit Fusing (t = 8.3 ms)	l ² t	40	A ² s				
Forward Peak Gate Power (Pulse Width \leq 1.0 μ s, T _C = 90°C)	PGM	20	Watts				
Forward Average Gate Power (t = 8.3 ms, T _C = 90°C)	PG(AV)	0.5	Watts				
Forward Peak Gate Current (Pulse Width \leq 1.0 μ s, T _C = 90°C)	IGM	2.0	A				
Operating Junction Temperature Range	TJ	-40 to +125	°C				
Storage Temperature Range	T _{stg}	-40 to +150	°C				

*Indicates JEDEC Registered Data

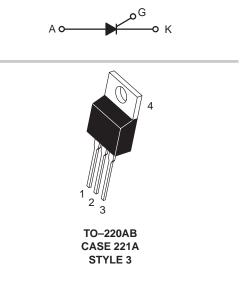
(1) V_{DRM} and V_{RRM} for all types can be applied on a continuous basis. Ratings apply for zero or negative gate voltage; however, positive gate voltage shall not be applied concurrent with negative potential on the anode. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.



ON Semiconductor

http://onsemi.com

SCRs 12 AMPERES RMS 50 thru 800 VOLTS



PIN ASSIGNMENT			
1	Cathode		
2	Anode		
3	Gate		
4	Anode		

ORDERING INFORMATION

Device	Package	Shipping
2N6394	TO220AB	500/Box
2N6395	TO220AB	500/Box
2N6397	TO220AB	500/Box
2N6399	TO220AB	500/Box

Preferred devices are recommended choices for future use and best overall value.

THERMAL CHARACTERISTICS

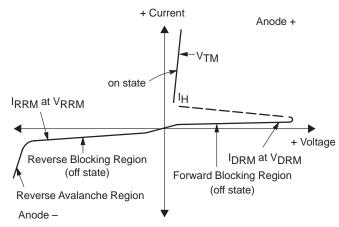
Characteristic Thermal Resistance, Junction to Case Maximum Lead Temperature for Soldering Purposes 1/8" from Case for 10 Seconds			bol	Max		Unit	
			JC	2.0	°C/W °C		
			L	260			
ELECTRICAL CHARACTERISTICS (T _C = 25°C unless otherwise r	noted.)						
Characteristic	Symbol		Min	Тур	Max	Unit	
OFF CHARACTERISTICS							
* Peak Repetitive Forward or Reverse Blocking Current (V _{AK} = Rated V _{DRM} or V _{RRM} , Gate Open) $T_J = 25^{\circ}C$ $T_J = 125^{\circ}C$	IDRM, IRRM				10 2.0	μA mA	
ON CHARACTERISTICS							
* Peak Forward On–State Voltage(1) (I _{TM} = 24 A Peak)	VTM	V _{TM}		1.7	2.2	Volts	
*Gate Trigger Current (Continuous dc) (V _D = 12 Vdc, R _L = 100 Ohms)	IGT		_	5.0	30	mA	
* Gate Trigger Voltage (Continuous dc) (V _D = 12 Vdc, R _L = 100 Ohms)	VGT			0.7	1.5	Volts	
Gate Non–Trigger Voltage ($V_D = 12 \text{ Vdc}, R_L = 100 \text{ Ohms}, T_J = 125^{\circ}\text{C}$)	V _{GD}		0.2	_	_	Volts	
*Holding Current (V _D = 12 Vdc, Initiating Current = 200 mA, Gate Open)	Ч		_	6.0	50	mA	
Turn-On Time (I _{TM} = 12 A, I _{GT} = 40 mAdc, V _D = Rated V _{DRM})	^t gt			1.0	2.0	μs	
Turn-Off Time (V _D = Rated V _{DRM}) (I_{TM} = 12 A, I_R = 12 A) (I_{TM} = 12 A, I_R = 12 A, T_J = 125°C)	tq		_	15 35		μs	
OYNAMIC CHARACTERISTICS							
Critical Rate–of–Rise of Off-State Voltage Exponential (V_D = Rated V_{DRM} , T_J = 125°C)	dv/dt		—	50	—	V/µs	

*Indicates JEDEC Registered Data

(1) Pulse Test: Pulse Width \leq 300 $\mu sec,$ Duty Cycle \leq 2%.

Voltage Current Characteristic of SCR

Parameter
Peak Repetitive Off State Forward Voltage
Peak Forward Blocking Current
Peak Repetitive Off State Reverse Voltage
Peak Reverse Blocking Current
Peak On State Voltage
Holding Current



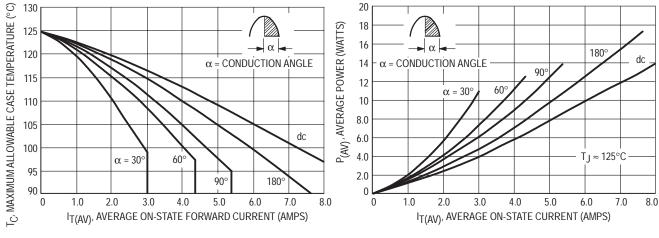


Figure 1. Current Derating

Figure 2. Maximum On–State Power Dissipation

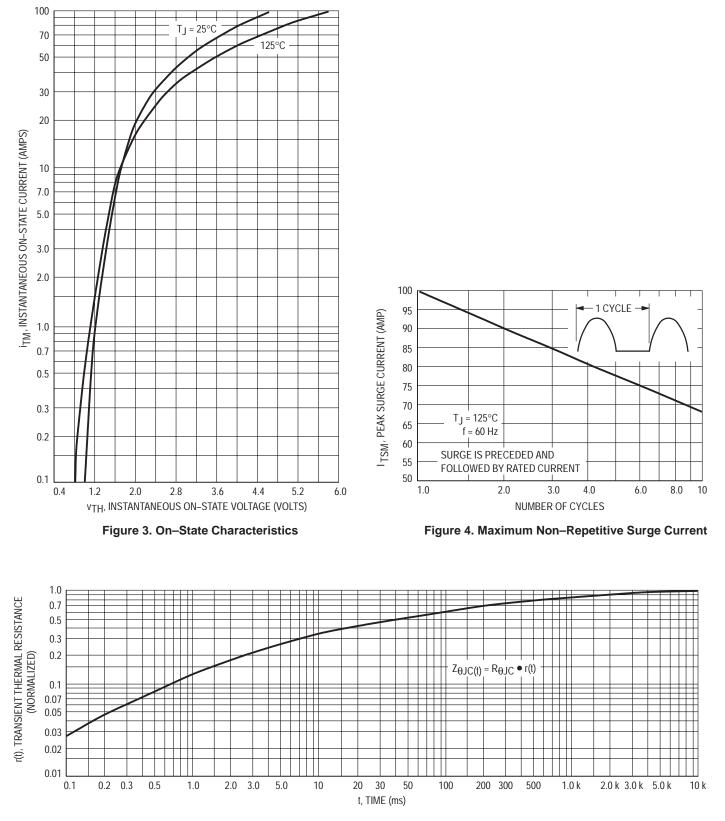
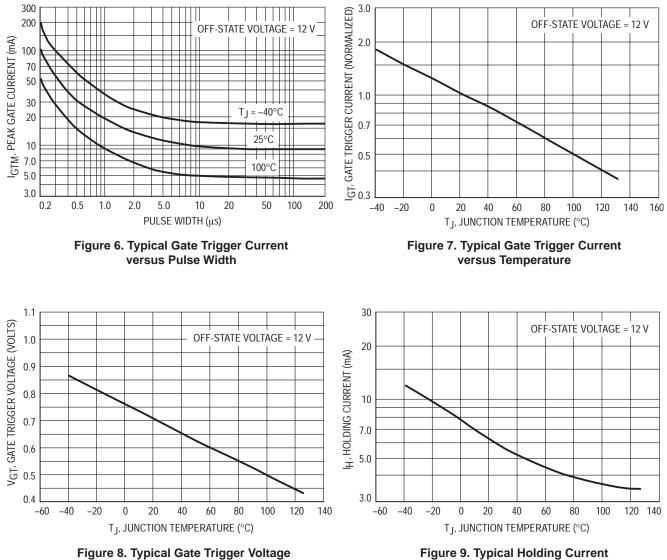


Figure 5. Thermal Response

TYPICAL CHARACTERISTICS

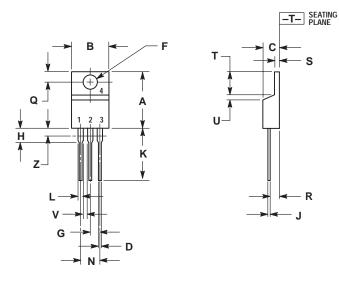


versus Temperature

Figure 9. Typical Holding Currer versus Temperature

PACKAGE DIMENSIONS

TO-220AB CASE 221A-07 ISSUE Z



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		INCHES		MILLIMETERS	
	DIM	MIN	MAX	MIN	MAX
	Α	0.570	0.620	14.48	15.75
	В	0.380	0.405	9.66	10.28
	С	0.160	0.190	4.07	4.82
	D	0.025	0.035	0.64	0.88
	F	0.142	0.147	3.61	3.73
	G	0.095	0.105	2.42	2.66
	Н	0.110	0.155	2.80	3.93
	٦	0.014	0.022	0.36	0.55
	К	0.500	0.562	12.70	14.27
Γ	L	0.045	0.060	1.15	1.52
	Ν	0.190	0.210	4.83	5.33
	Q	0.100	0.120	2.54	3.04
	R	0.080	0.110	2.04	2.79
	S	0.045	0.055	1.15	1.39
	Т	0.235	0.255	5.97	6.47
	U	0.000	0.050	0.00	1.27
	V	0.045		1.15	
	7		0.080		2.04

TYLE 3: PIN 1. CATHODE 2. ANODE 3. GATE 4. ANODE

<u>Notes</u>

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Reliability Data PCN						
Samples Search Order Status	Reverse Blocking Thyristors					
Tech Support	 Designed primarily for half-wave ac control applications, su controls, heating controls and power supplies. 					
	Features:					
	 Glass Passivated Junctions with Center Gate Geome Parameter Uniformity and Stability Small, Rugged, Thermowatt Construction for Low The Resistance, High Heat Dissipation and Durability Blocking Voltage to 800 Volts Device Marking: Logo, Device Type, e.g., 2N6394, Data 	:				
	Orderable Parts					
	Action Part Desc. Desc. Count Outline Status	-				
	N/A 2N6397T Silicon TO-220 3 <u>221A-09</u> Active Controlled					

Rectifier 12A 400V

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