



# BC857BV

# PNP DUAL SMALL SIGNAL SURFACE MOUNT TRANSISTOR

#### **Features Epitaxial Die Construction** ≻A ∢ **Complementary NPN Types Available** (BC847BV) SOT-563 Ultra-Small Surface Mount Package K5V ΥN B C Dim Min Max Тур Lead Free By Design/RoHS Compliant (Note 3) Α 0.15 0.30 0.25 $B_1$ E₁ C<sub>2</sub> В 1.25 1.10 1.20 **Mechanical Data** С 1.55 1.70 1.60 Case: SOT-563 D 0.50 Case Material: Molded Plastic. UL Flammability G 0.90 1.10 1.00 M Classification Rating 94V-0 Н 1.50 1.70 1.60 Moisture sensitivity: Level 1 per J-STD-020C Κ 0.56 0.60 0.60 Terminal Connections: See Diagram н L 0.10 0.30 0.20 Terminals: Finish - Matte Tin annealed over Alloy 42 SEE NOTE 1 Μ leadframe. Solderable per MIL-STD-202, Method 208 0.10 0.18 0.11 Marking (See Page 2): K5V All Dimensions in mm Ordering & Date Code Information: See Page 2 Weight: 0.003 grams (approximate)

### Maximum Ratings @ T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit	
Collector-Base Voltage	V <sub>CBO</sub>	-50	V	
Collector-Emitter Voltage	V <sub>CEO</sub>	-45	V	
Emitter-Base Voltage	V <sub>EBO</sub>	-5.0	V	
Collector Current	IC	-100	mA	
Power Dissipation (Note 2)	Pd	150	mW	
Thermal Resistance, Junction to Ambient (Note 2)	R <sub>JA</sub>	833	°C/W	
Operating and Storage Temperature Range	T <sub>j</sub> , T <sub>STG</sub>	-55 to +150	°C	

Notes: 1. Package is non-polarized. Parts may be on reel in orientation illustrated, 180 rotated, or mixed (both ways).

 Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

3. No purposefully added lead.



## Electrical Characteristics @ T<sub>A</sub> = 25°C unless otherwise specified

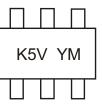
Characteristic		Symbol	Min	Тур	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	(Note 4)	V <sub>(BR)CBO</sub>	-50	—	—	V	$I_{\rm C} = 10$ A, $I_{\rm B} = 0$
Collector-Emitter Breakdown Voltage	(Note 4)	V <sub>(BR)CEO</sub>	-45	—	—	V	I <sub>C</sub> = 10mA, I <sub>B</sub> = 0
Emitter-Base Breakdown Voltage	(Note 4)	V <sub>(BR)EBO</sub>	-5	—	—	V	$I_{\rm E} = 1$ A, $I_{\rm C} = 0$
DC Current Gain	(Note 4)	h <sub>FE</sub>	220	290	475	_	$V_{CE} = -5.0V, I_{C} = -2.0mA$
Collector-Emitter Saturation Voltage	(Note 4)	V <sub>CE(SAT)</sub>		_	-100 -400	mV	$I_{C} = -10mA, I_{B} = -0.5mA$ $I_{C} = -100mA, I_{B} = -5.0mA$
Base-Emitter Saturation Voltage	(Note 4)	V <sub>BE(SAT)</sub>	_	-700 -900	_	mV	$I_{C} = -10mA, I_{B} = -0.5mA$ $I_{C} = -100mA, I_{B} = -5.0mA$
Base-Emitter Voltage	(Note 4)	V <sub>BE(ON)</sub>	-600	_	-750 -820	mV	$V_{CE} = -5.0V, I_C = -2.0mA$ $V_{CE} = -5.0V, I_C = -10mA$
Collector-Cutoff Current	(Note 4)	I <sub>CBO</sub>	_	_	-15 -4.0	nA µA	V <sub>CB</sub> = -30V V <sub>CB</sub> = -30V, T <sub>A</sub> = 150°C
Gain Bandwidth Product		fT	100	_		MHz	V <sub>CE</sub> = -5.0V, I <sub>C</sub> = -10mA, f = 100MHz
Output Capacitance		C <sub>OB</sub>	_	—	4.5	pF	V <sub>CB</sub> = -10V, f = 1.0MHz
Noise Figure		NF	_	_	10	dB	$\begin{array}{l} I_{C} = -0.2mA, \ V_{CE} = -5.0Vdc, \\ R_{S} = 2.0K \ , \ f = 1.0KHz, \\ BW = 200Hz \end{array}$

Ordering Information (Note 5)										
Device	Packaging	Shipping								
BC857BV-7	SOT-563	3000/Tape & Reel								

Notes: 4. Short duration pulse test used to minimize self-heating effect.

5. For Packaging Details: go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

# **Marking Information**

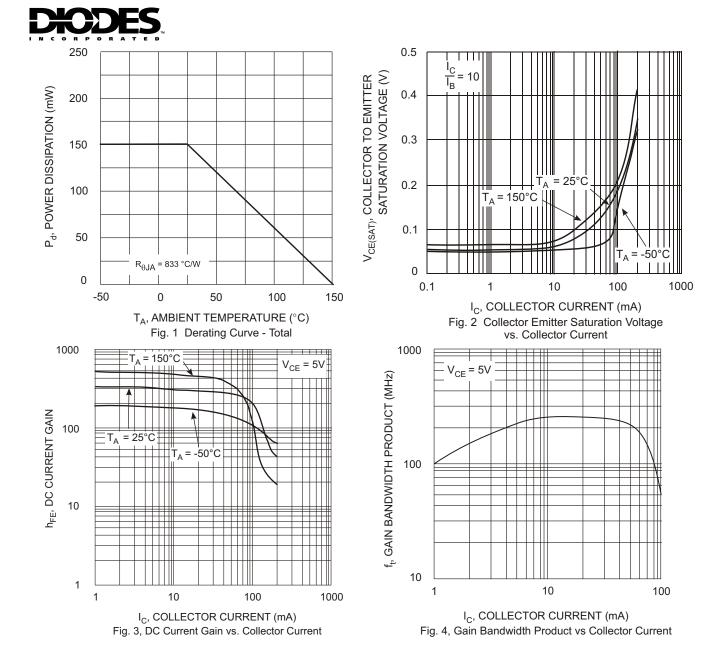


 $\begin{array}{l} \mathsf{K5V} = \mathsf{Product Type Marking Code} \\ \mathsf{YM} = \mathsf{Date Code Marking} \\ \mathsf{Y} = \mathsf{Year} \ (\mathsf{ex: T} = 2006) \\ \mathsf{M} = \mathsf{Month} \ (\mathsf{ex: 9} = \mathsf{September}) \end{array}$ 

Date Code Key

Year	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012
Code	Р	R	S	Т	U	V	W	Х	Y	Z

Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	Ν	D



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