

# Small Signal MOSFET

## 380 mAmps, 60 Volts

### N-Channel SOT-23

#### Features

- ESD Protected
- Low  $R_{DS(on)}$
- Surface Mount Package
- This is a Pb-Free Device
- We declare that the material of product are Halogen Free and compliance with RoHS requirements.

#### Applications

- Low Side Load Switch
- Level Shift Circuits
- DC-DC Converter
- Portable Applications i.e. DSC, PDA, Cell Phone, etc.

#### MAXIMUM RATINGS (T<sub>J</sub> = 25°C unless otherwise stated)

Rating	Symbol	Value	Unit
Drain-to-Source Voltage	V <sub>DSS</sub>	60	V
Gate-to-Source Voltage	V <sub>GS</sub>	±20	V
Drain Current (Note 1) Steady State	I <sub>D</sub>	T <sub>A</sub> = 25°C	320
		T <sub>A</sub> = 85°C	230
t < 5 s	I <sub>D</sub>	T <sub>A</sub> = 25°C	380
		T <sub>A</sub> = 85°C	270
Power Dissipation (Note 1) Steady State	P <sub>D</sub>		300
		t < 5 s	420
Pulsed Drain Current (t <sub>p</sub> = 10 μs)	I <sub>DM</sub>	1.5	A
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C
Source Current (Body Diode)	I <sub>S</sub>	300	mA
Lead Temperature for Soldering Purposes (1/8" from case for 10 s)	T <sub>L</sub>	260	°C
Gate-Source ESD Rating (HBM, Method 3015)	ESD	2000	V

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

#### THERMAL CHARACTERISTICS

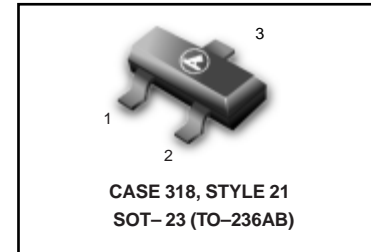
Characteristic	Symbol	Max	Unit
Junction-to-Ambient - Steady State (Note 1)	R <sub>θJA</sub>	417	°C/W
Junction-to-Ambient - t ≤ 5 s (Note 1)	R <sub>θJA</sub>	300	

1. Surface-mounted on FR4 board using 1 in sq pad size (Cu area = 1.127 in sq [1 oz] including traces)

#### ORDERING INFORMATION

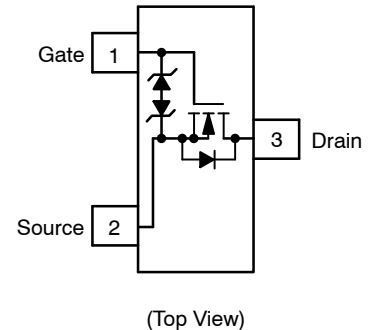
Device	Marking	Shipping
L2N7002KLT1G	RK	3000 Tape & Reel

## L2N7002KLT1G

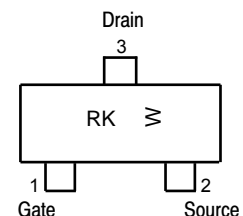


V <sub>(BR)DSS</sub>	R <sub>DS(on)</sub> MAX	I <sub>D</sub> MAX (Note 1)
60 V	1.8 Ω @ 10 V	380 mA
	2.5 Ω @ 5.0 V	

#### Simplified Schematic



#### MARKING DIAGRAM & PIN ASSIGNMENT



RK = Device Code  
W = Month Code

**L2N7002KLT1G**

**ELECTRICAL CHARACTERISTICS** ( $T_J = 25^\circ\text{C}$  unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
<b>OFF CHARACTERISTICS</b>						
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0\text{ V}, I_D = 250\ \mu\text{A}$	60			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	$V_{(BR)DSS}/T_J$			71		mV/°C
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{GS} = 0\text{ V}, V_{DS} = 60\text{ V}$	$T_J = 25^\circ\text{C}$		1	$\mu\text{A}$
			$T_J = 125^\circ\text{C}$		500	
		$V_{GS} = 0\text{ V}, V_{DS} = 50\text{ V}$	$T_J = 25^\circ\text{C}$			100
Gate-to-Source Leakage Current	$I_{GSS}$	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$			$\pm 10$	$\mu\text{A}$

**ON CHARACTERISTICS** (Note 2)

Gate Threshold Voltage	$V_{GS(TH)}$	$V_{GS} = V_{DS}, I_D = 250\ \mu\text{A}$	1.0		2.5	V
Negative Threshold Temperature Coefficient	$V_{GS(TH)}/T_J$			4.0		mV/°C
Drain-to-Source On Resistance	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 500\text{ mA}$			1.8	$\Omega$
		$V_{GS} = 5.0\text{ V}, I_D = 50\text{ mA}$			2.5	
Forward Transconductance	$g_{FS}$	$V_{DS} = 5\text{ V}, I_D = 200\text{ mA}$		80		S

**CHARGES AND CAPACITANCES**

Input Capacitance	$C_{ISS}$	$V_{GS} = 0\text{ V}, f = 1\text{ MHz}, V_{DS} = 25\text{ V}$		32.8		$\mu\text{F}$
Output Capacitance	$C_{OSS}$			5.4		
Reverse Transfer Capacitance	$C_{RSS}$			2.9		
Total Gate Charge	$Q_{G(TOT)}$	$V_{GS} = 4.5\text{ V}, V_{DS} = 10\text{ V}; I_D = 200\text{ mA}$		0.7		nC
Threshold Gate Charge	$Q_{G(TH)}$			0.1		
Gate-to-Source Charge	$Q_{GS}$			0.3		
Gate-to-Drain Charge	$Q_{GD}$			0.1		

**SWITCHING CHARACTERISTICS,  $V_{GS} = V$**  (Note 3)

Turn-On Delay Time	$t_{d(ON)}$	$V_{GS} = 10\text{ V}, V_{DD} = 10\text{ V}, I_D = 500\text{ mA}$		9.9		ns
Rise Time	$t_r$			5.0		
Turn-Off Delay Time	$t_{d(OFF)}$			39.4		
Fall Time	$t_f$			17.9		

**DRAIN-SOURCE DIODE CHARACTERISTICS**

Forward Diode Voltage	$V_{SD}$	$V_{GS} = 0\text{ V}, I_S = 115\text{ mA}$	$T_J = 25^\circ\text{C}$		1.4	V
			$T_J = 85^\circ\text{C}$		0.7	

2. Pulse Test: pulse width  $\leq 300\ \mu\text{s}$ , duty cycle  $\leq 2\%$
3. Switching characteristics are independent of operating junction temperatures

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TYPICAL ELECTRICAL CHARACTERISTICS

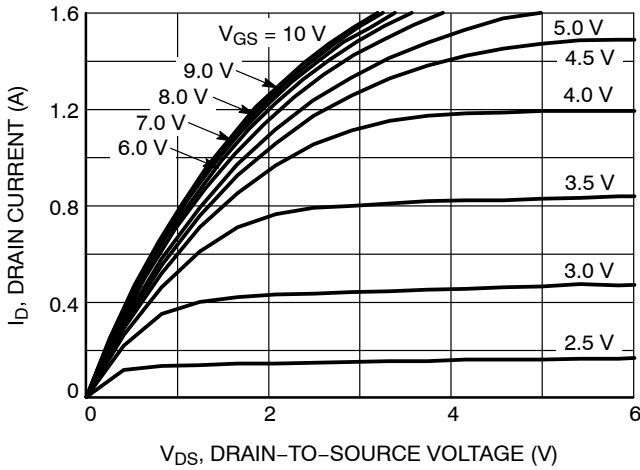


Figure 1. On-Region Characteristics

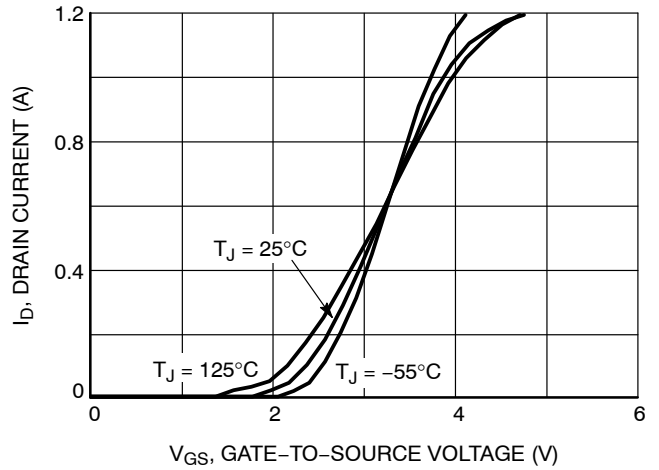


Figure 2. Transfer Characteristics

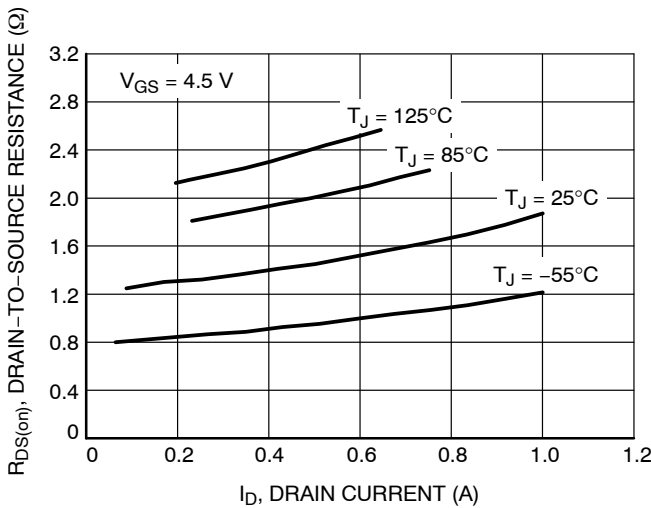


Figure 3. On-Resistance vs. Drain Current and Temperature

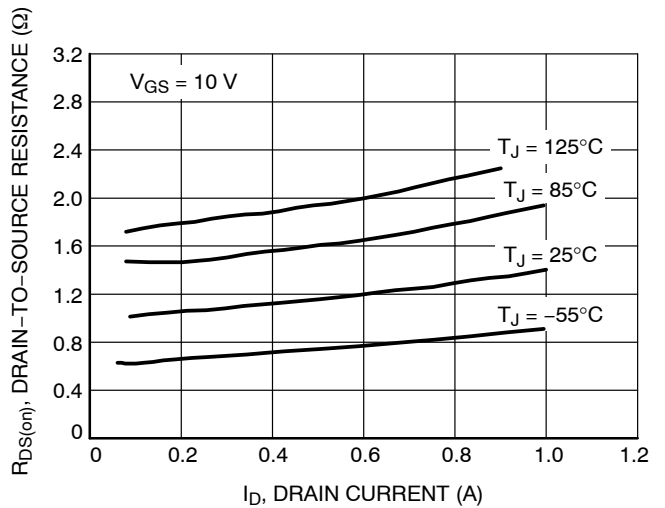


Figure 4. On-Resistance vs. Drain Current and Temperature

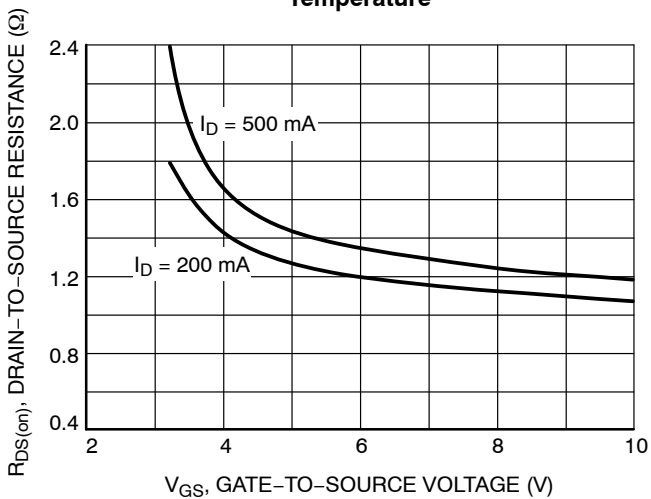


Figure 5. On-Resistance vs. Gate-to-Source Voltage

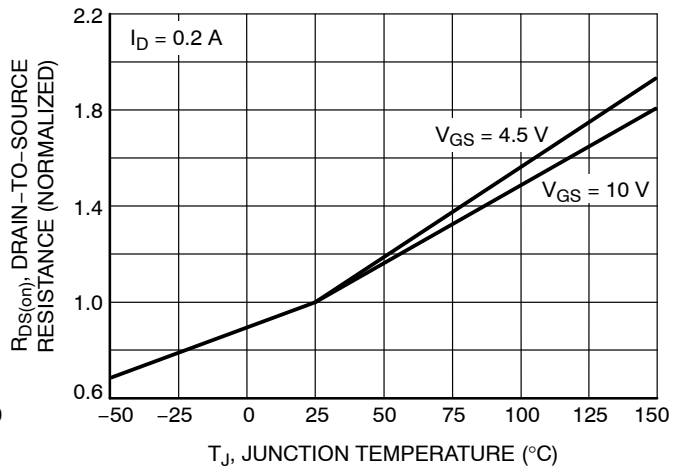
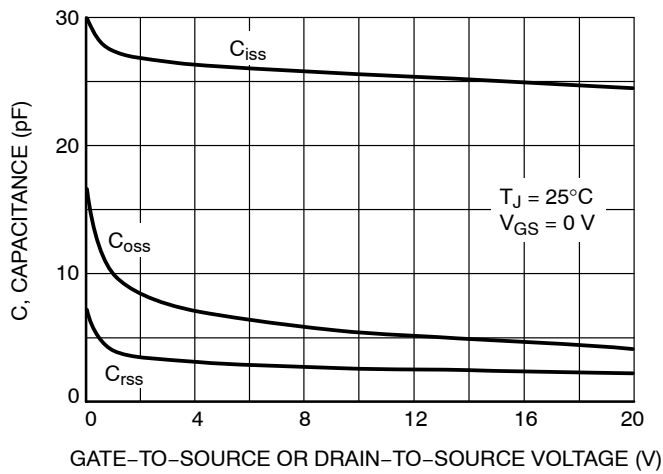
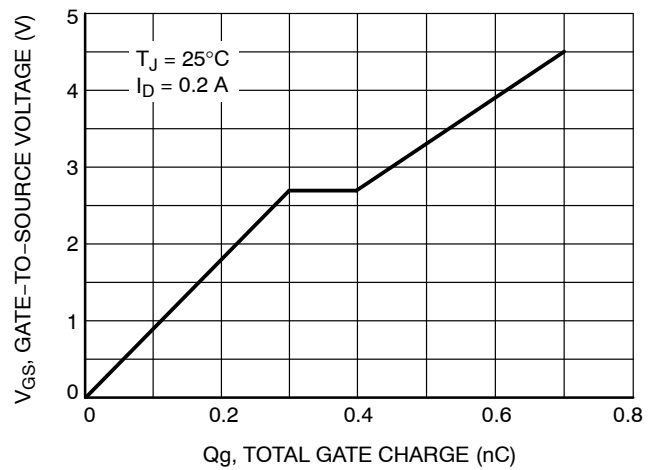
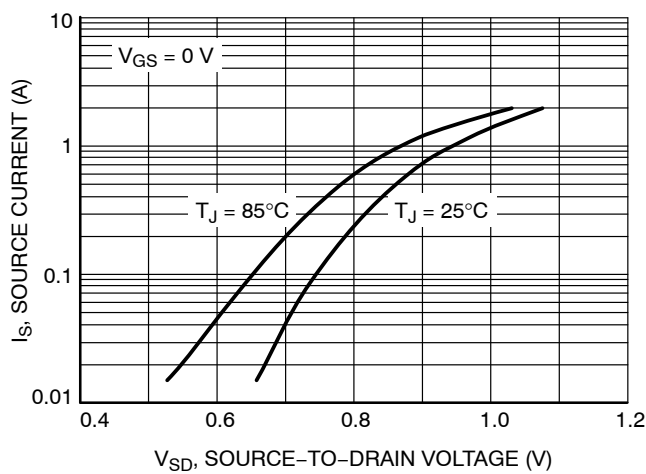
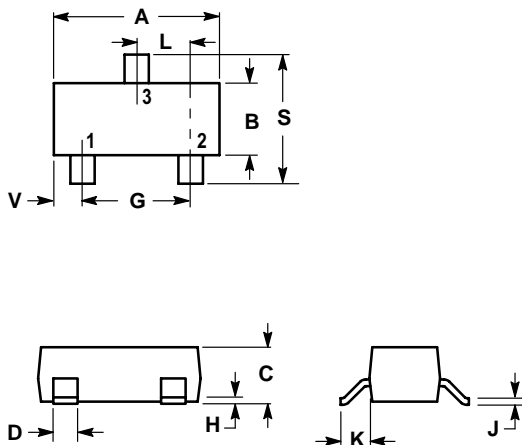


Figure 6. On-Resistance Variation with Temperature

**L2N7002KLT1G**
**TYPICAL ELECTRICAL CHARACTERISTICS**

**Figure 7. Capacitance Variation**

**Figure 8. Gate-to-Source and Drain-to-Source Voltage vs. Total Charge**

**Figure 9. Diode Forward Voltage vs. Current**

**L2N7002KLT1G**
**SOT-23**
**NOTES:**

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M,1982
2. CONTROLLING DIMENSION: INCH.



DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.1102	0.1197	2.80	3.04
B	0.0472	0.0551	1.20	1.40
C	0.0350	0.0440	0.89	1.11
D	0.0150	0.0200	0.37	0.50
G	0.0701	0.0807	1.78	2.04
H	0.0005	0.0040	0.013	0.100
J	0.0034	0.0070	0.085	0.177
K	0.0140	0.0285	0.35	0.69
L	0.0350	0.0401	0.89	1.02
S	0.0830	0.1039	2.10	2.64
V	0.0177	0.0236	0.45	0.60

