N-Channel 30-V (D-S) MOSFET

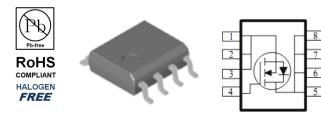
Key Features:

- Low r_{DS(on)} trench technology
- · Low thermal impedance
- Fast switching speed

Typical Applications:

- White LED boost converters
- Automotive Systems
- Industrial DC/DC Conversion Circuits

PRODUCT SUMMARY			
Vds (V)	$r_{DS(on)}(m\Omega)$	I⊳(A)	
30	6 @ V _{GS} = 10V	19	
	8 @ V _{GS} = 4.5V	16	



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}C$ UNLESS OTHERWISE NOTED)					
Parameter		Symbol	Limit	Units	
Drain-Source Voltage			30	V	
Gate-Source Voltage	V _{GS}	±20	v		
Continuous Drain Current ^a	T _A =25°C	I	19	А	
Continuous Drain Current	T _A =70°C	I _D	16		
Pulsed Drain Current ^b	I _{DM}	60			
Continuous Source Current (Diode Conduction) ^a	I _S	4.5	А		
Devuer Dissinguing	T _A =25°C	P _D	3.1	W	
Power Dissipation ^a	T _A =70°C	' D	2.2	vV	
Operating Junction and Storage Temperature Range			-55 to 150	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Maximum	Units	
Maximum Junction-to-Ambient ^a	t <= 10 sec	R_{\thetaJA}	40	°C/W	
	Steady State	ιν _θ ιΑ	80	C/W	

Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

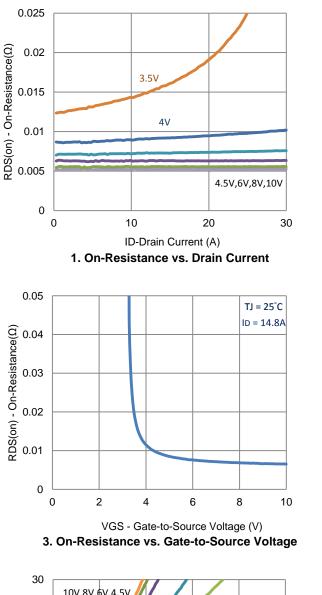
Electrical Characteristics

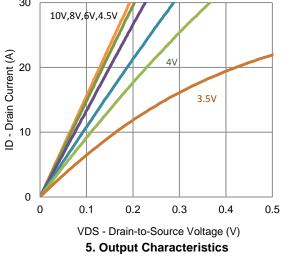
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static							
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \text{ uA}$	1			V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±100	nA	
Zero Gate Voltage Drain Current		$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}$			1 uA		
	DSS	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$	_{iS} = 0 V, T _J = 55°C		25	uA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = 5 V, V_{GS} = 10 V$	30			А	
Drain Sauras On Desistance a	r	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 14.8 \text{ A}$			6	mΩ	
Drain-Source On-Resistance ^a	r _{DS(on)}	$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 11.9 \text{ A}$			8	11122	
Forward Transconductance ^a	g _{fs}	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 14.8 \text{ A}$		22		S	
Diode Forward Voltage ^a	V_{SD}	$I_{S} = 2.3 \text{ A}, V_{GS} = 0 \text{ V}$		0.73		V	
		Dynamic ^b					
Total Gate Charge	Qg	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V},$		21			
Gate-Source Charge	Q _{gs}	$V_{DS} = 13$ V, $V_{GS} = 4.3$ V, $I_{D} = 14.8$ A		6.2		nC	
Gate-Drain Charge	Q_{gd}	10 - 14.0 A		11		1	
Turn-On Delay Time	t _{d(on)}	V _{DS} = 15 V, R _I = 1.1 Ω,		10			
Rise Time	t _r	$V_{DS} = 15 V, R_{L} = 1.1 \Omega_{2},$ $I_{D} = 14.8 A,$		10			
Turn-Off Delay Time	t _{d(off)}	$V_{\text{GEN}} = 10 \text{ V}, \text{ R}_{\text{GEN}} = 6 \Omega$		59		ns	
Fall Time	t _f	$V_{\text{GEN}} = 10$ V, $R_{\text{GEN}} = 0.22$		16			
Input Capacitance	C _{iss}			2056			
Output Capacitance	C _{oss}	V_{DS} = 15 V, V_{GS} = 0 V, f = 1 MHz		326		pF	
Reverse Transfer Capacitance	C _{rss}			292			

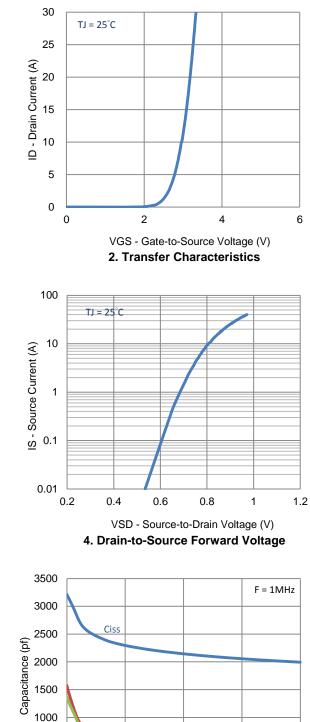
Notes

- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

FREESCALE reserves the right to make changes without further notice to any products herein. FREESCALE makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does freescale assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in freescale data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. freescale does not convey any license under its patent rights nor the rights of others. freescale products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the freescale product could create a situation where personal injury or death may occur. Should Buyer purchase or use freescale products for any such unintended or unauthorized application, Buyer shall indemnify and hold freescale and its officers, employees, subsidiaries, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that freescale was negligent regarding the design or manufacture of the part. freescale is an Equal Opportunity/Affirmative Action Employer.









Coss

Crss

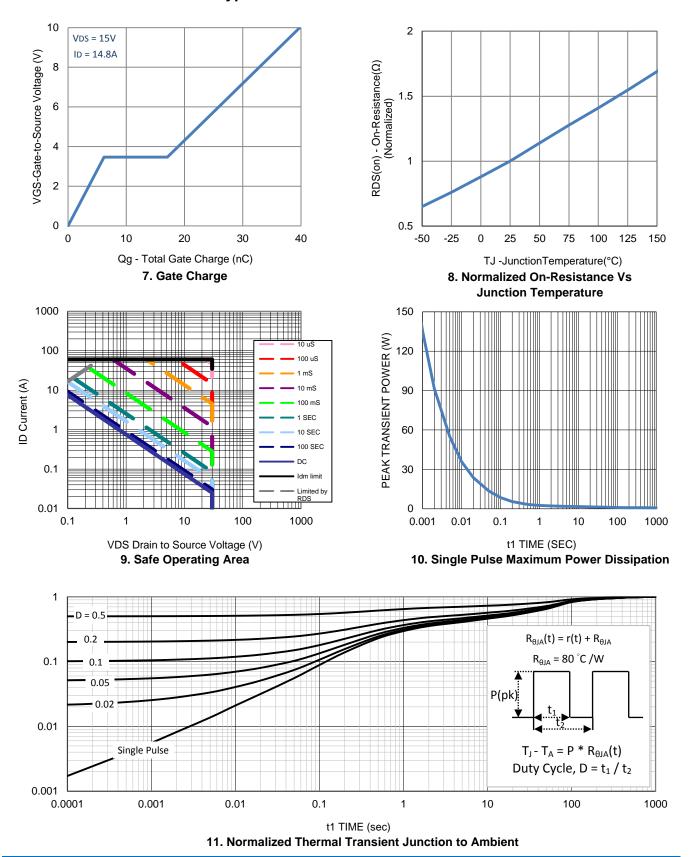
500

0

0

10 15 20

www.freescale.net.cn

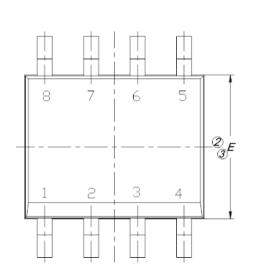


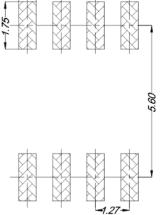
Typical Electrical Characteristics

Package Information

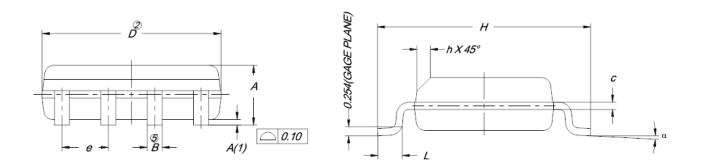
0.65

Land Pattern (Only for Reference)





	MILLIMETERS				
DIM.	MIN.	NOM.	MAX.		
А	1.35	1.55	1.75		
A(1)	0.10	0.18	0.25		
В	0.38	0.45	0.51		
С	0.19	0.22	0.25		
D	4.80	4.90	5.00		
E	3.80	3.90	4.00		
е	1.27 BSC				
н	5.80	6.00	6.20		
L	0.50	0.72	0.93		
α	0°	4°	8°		
h	0.25	0.38	0.50		



Note:

- 1. All Dimension Are In mm.
- 2. Package Body Sizes Exclude Mold Flash, Protrusion Or Gate Burrs. Mold Flash, Protrusion Or Gate Burrs Shall Not Exceed 0.10 mm Per Side.
- 3. Package Body Sizes Determined At The Outermost Extremes Of The Plastic Body Exclusive Of Mold Flash, Tie Bar Burrs, Gate Burrs And Interlead Flash, But Including Any Mismatch Between The Top And Bottom Of The Plastic Body.
- 4. The Package Top May Be Smaller Than The Package Bottom.
- Dimension "B" Does Not Include Dambar Protrusion. Allowable Dambar Protrusion Shall Be 0.08 mm Total In Excess Of "B" Dimension At Maximum Material Condition. The Dambar Cannot Be Located On The Lower Radius Of The Foot.