TOSHIBA Field Effect Transistor Silicon N Channel MOS Type ( $\pi$ -MOSVII)

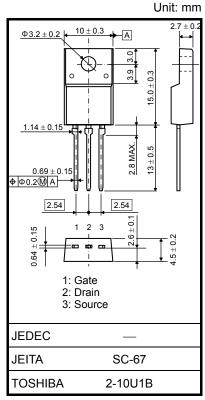
# TK3A65DA

#### Switching Regulator Applications

- Low drain-source ON-resistance: RDS (ON) = 2.3  $\Omega$ (typ.)
- High forward transfer admittance:  $|Y_{fs}| = 2.2 \text{ S}$  (typ.)
- Low leakage current:  $I_{DSS} = 10 \ \mu A \ (max) \ (V_{DS} = 650 \ V)$
- Enhancement mode:  $V_{th} = 2.4$  to 4.4 V ( $V_{DS} = 10$  V,  $I_D = 1$  mA)

Character	ristics	Symbol	Rating	Unit				
Drain-source voltage		V <sub>DSS</sub>	650	V				
Gate-source voltage		V <sub>GSS</sub>	±30	V				
Drain current	DC (Note 1)	۱ <sub>D</sub>	2.5					
	Pulse (t = 1 ms) (Note 1)	I <sub>DP</sub>	10	A				
Drain power dissipati	on (Tc = 25°C)	PD	35	W				
Single pulse avalanch	ne energy (Note 2)	E <sub>AS</sub>	244	mJ				
Avalanche current		I <sub>AR</sub>	2.5	А				
Repetitive avalanche	energy (Note 3)	E <sub>AR</sub>	3.5	mJ				
Channel temperature		T <sub>ch</sub>	150	°C				
Storage temperature	range	T <sub>stg</sub>	-55 to 150	°C				

#### Absolute Maximum Ratings (Ta = 25°C)



Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

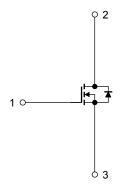
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	3.57	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	62.5	°C/W

Note 1:Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD} = 90 \text{ V}, \text{ T}_{ch} = 25^{\circ}\text{C}(\text{initial}), \text{ L} = 69 \text{ mH}, \text{ R}_{G} = 25 \Omega, \text{ I}_{AR} = 2.5 \text{ A}$ 

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.



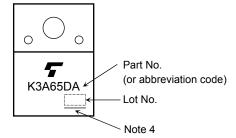
**Electrical Characteristics (Ta = 25°C)** 

Char	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I <sub>GSS</sub>	$V_{GS}=\pm 30~V,~V_{DS}=0~V$	_		±1	μA
Drain cut-off curr	ent	I <sub>DSS</sub>	$V_{DS} = 650 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	_		10	μA
Drain-source breakdown voltage		V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	650		_	V
Gate threshold v	oltage	V <sub>th</sub>	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$	2.4		4.4	V
Drain-source ON	-resistance	R <sub>DS (ON)</sub>	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 1.3 \text{ A}$	_	2.3	2.51	Ω
Forward transfer	admittance	Y <sub>fs</sub>	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1.3 \text{ A}$	0.6	2.2	_	S
Input capacitance		C <sub>iss</sub>		_	490		
Reverse transfer capacitance		C <sub>rss</sub>	$V_{DS}$ = 25 V, $V_{GS}$ = 0 V, f = 1 MHz	_	3	_	pF
Output capacitance		C <sub>oss</sub>			55		
Switching time	Rise time	tr	$V_{GS}^{10 \text{ V}} V_{GS}^{10 \text{ V}} I_D = 1.3\text{A} V_{OUT}^{10 \text{ V}} V_{GS}^{10 \text{ V}} V_{OUT}^{10 \text{ V}} $		18	_	
	Turn-on time	t <sub>on</sub>			40		
	Fall time	t <sub>f</sub>			8	_	ns
	Turn-off time	t <sub>off</sub>		_	55	—	
Total gate charge		Qg		_	11		
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx 400 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 2.5 \text{ A}$		6		nC
Gate-drain charge		Q <sub>gd</sub>		_	5		

#### Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	—	_	_	2.5	А
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	—	_	_	10	А
Forward voltage (diode)	V <sub>DSF</sub>	$I_{DR} = 2.5 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	$I_{DR} = 2.5 \text{ A}, V_{GS} = 0 \text{ V},$	_	1000	_	ns
Reverse recovery charge	Qrr	dl <sub>DR</sub> /dt = 100 A/μs		5	_	μC

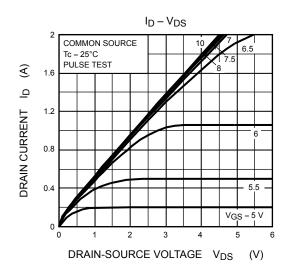
#### Marking

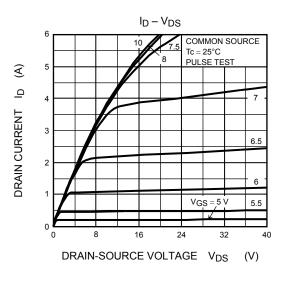


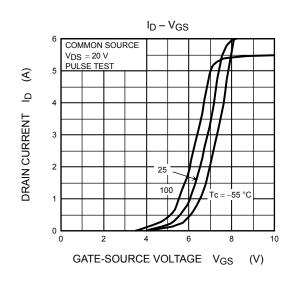
Note 4 : A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

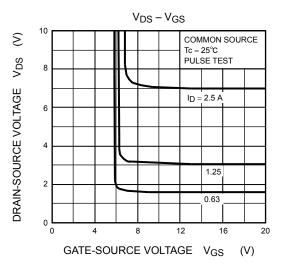
Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

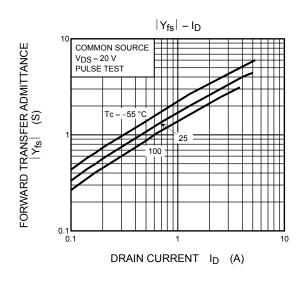
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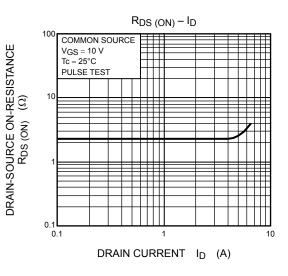




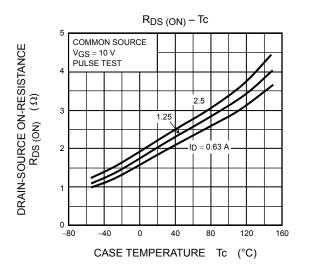


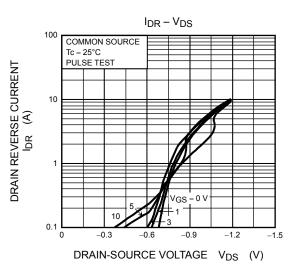


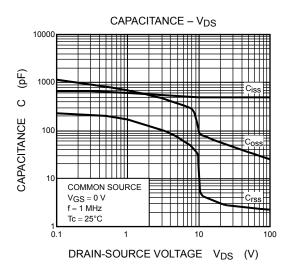


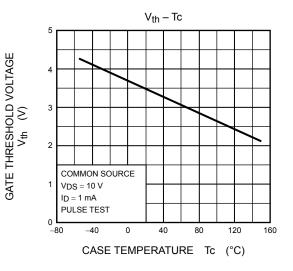


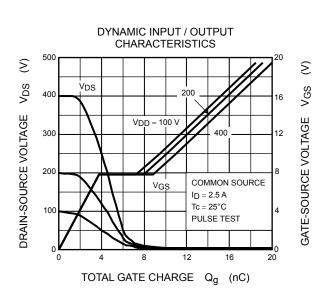
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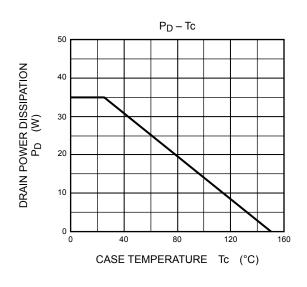


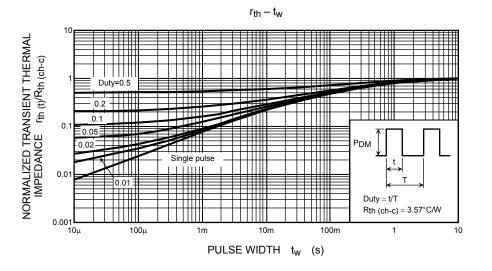




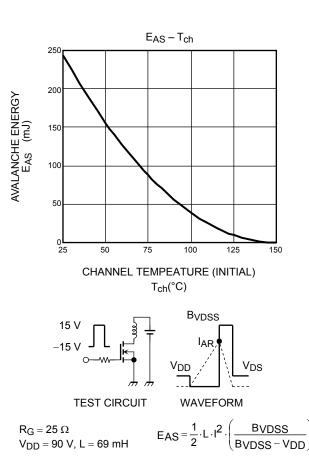








SAFE OPERATING AREA 100 ID max (pulsed) \* 100 us \* 10 € H ID max (continuous) \_ <del>| | | | | |||</del> DC operatio Tc = 25°C DRAIN CURRENT 0. \*: SINGLE NONREPETITIVE PULSE Tc = 25°C 0.01 CURVES MUST BE DERATED LINEARLY WITH INCREASE IN TEMPERATURE. VDSS max 0.001 0.1 1 10 100 1000 DRAIN-SOURCE VOLTAGE VDS (V)



2009-12-08

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