N-Channel 30-V (D-S) MOSFET

Key Features:

- Low r_{DS(on)} trench technology
- · Low thermal impedance
- Fast switching speed

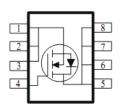
Typical Applications:

- · White LED boost converters
- Automotive Systems
- Industrial DC/DC Conversion Circuits

| PRODUCT SUMMARY | | | |
|---------------------|-----------------------------|--------------------|--|
| V _{DS} (V) | $r_{DS(on)}(m\Omega)$ | I _D (A) | |
| 30 | 11 @ V _{GS} = 4.5V | 13.8 | |
| | $12 @ V_{GS} = 2.5V$ | 13.2 | |







| ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}$ C UNLESS OTHERWISE NOTED) | | | | | |
|---|----------------------|----------------|------------|-------|--|
| Parameter | | Symbol | Limit | Units | |
| Drain-Source Voltage | | | 30 | V | |
| Gate-Source Voltage | V_{GS} | ±20 | V | | |
| Continuous Drain Current a | T _A =25°C | l _D | 13.8 | | |
| Continuous Drain Current | T _A =70°C | 'D | 11.6 | Α | |
| Pulsed Drain Current ^b | I _{DM} | 50 | | | |
| Continuous Source Current (Diode Conduction) a | I _S | 4.6 | Α | | |
| Power Dissipation ^a | T _A =25°C | P_{D} | 3.1 | W | |
| Fower Dissipation | T _A =70°C | ' D | 2.2 | v v | |
| Operating Junction and Storage Temperature Range | | | -55 to 150 | °C | |

| THERMAL RESISTANCE RATINGS | | | | | | |
|--|--------------|-----------------|---------|-------|--|--|
| Parameter | | Symbol | Maximum | Units | | |
| Maximum Junction-to-Ambient ^a | t <= 10 sec | $R_{\theta JA}$ | 40 | °C/W | | |
| Maximum Junction-to-Ambient | Steady State | IXOJA | 80 | C/VV | | |

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Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

Electrical Characteristics

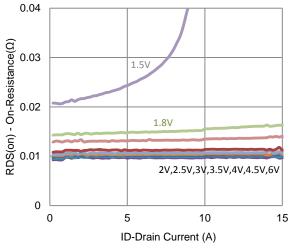
| Parameter | Symbol | Test Conditions | Min | Тур | Max | Unit | |
|---|---|---|-----|------|------|-------|--|
| Static | | | | | | | |
| Gate-Source Threshold Voltage | $V_{GS(th)}$ | $V_{GS(th)}$ $V_{DS} = V_{GS}$, $I_D = 250 \text{ uA}$ | | | | V | |
| Gate-Body Leakage | I_{GSS} $V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$ | | | | ±100 | nA | |
| Zero Gate Voltage Drain Current | I _{DSS} | $V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}$ | | | 1 | uA | |
| Zero Gate Voltage Brain Gurrent | DSS | $V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$ | | | 25 | uA | |
| On-State Drain Current ^a | $I_{D(on)}$ | $V_{DS} = 5 \text{ V}, V_{GS} = 4.5 \text{ V}$ | 20 | | | Α | |
| Drain-Source On-Resistance ^a | r | $V_{GS} = 4.5 \text{ V}, I_D = 11 \text{ A}$ | | | 11 | mΩ | |
| Drain-Source On-Resistance | r _{DS(on)} | $V_{GS} = 2.5 \text{ V}, I_D = 10.5 \text{ A}$ | | | 12 | 11122 | |
| Forward Transconductance ^a | g _{fs} | $V_{DS} = 15 \text{ V}, I_{D} = 11 \text{ A}$ | | 22 | | S | |
| Diode Forward Voltage ^a | V_{SD} | $I_S = 2.3 \text{ A}, V_{GS} = 0 \text{ V}$ | | 0.66 | | V | |
| | | Dynamic ^b | | | | | |
| Total Gate Charge | Q_g | $V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V},$ | | 24 | | | |
| Gate-Source Charge | Q_gs | $I_{DS} = 13 \text{ V}, \text{ V}_{GS} = 4.3 \text{ V},$ $I_{D} = 11 \text{ A}$ | | 4.9 | | nC | |
| Gate-Drain Charge | Q_gd | 1 _D = 1177 | | 6.1 | | | |
| Turn-On Delay Time | t _{d(on)} | $V_{DS} = 15 \text{ V}, R_1 = 1.2 \Omega,$ | | 18 | | | |
| Rise Time | t _r | $V_{DS} = 13 \text{ V}, \text{ N}_{L} = 1.2 \Omega$, $I_{D} = 11 \text{ A},$ | | 36 | | ne | |
| Turn-Off Delay Time | $t_{d(off)}$ | $V_{GEN} = 10 \text{ V}, R_{GEN} = 6 \Omega$ | | 103 | | ns | |
| Fall Time | t _f | VGEN = 10 V, NGEN 0 12 | | 53 | | | |
| Input Capacitance | C_{iss} | | | 2047 | | _ | |
| Output Capacitance | C _{oss} | $V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$ | | 217 | | pF | |
| Reverse Transfer Capacitance | C_{rss} | 7 | | 191 | | | |

Notes

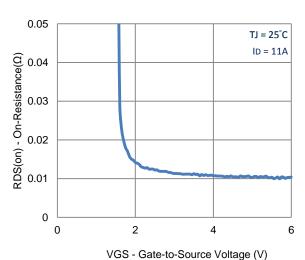
- Pulse test: PW <= 300us duty cycle <= 2%.
- Guaranteed by design, not subject to production testing. b.

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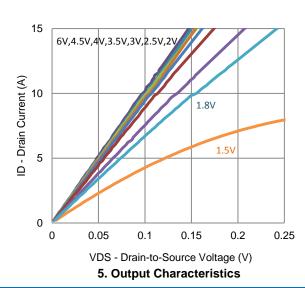
Typical Electrical Characteristics



1. On-Resistance vs. Drain Current



3. On-Resistance vs. Gate-to-Source Voltage

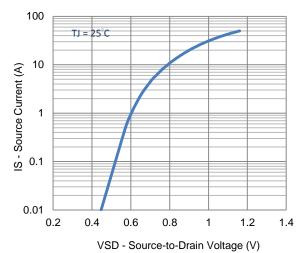


20
TJ = 25°C

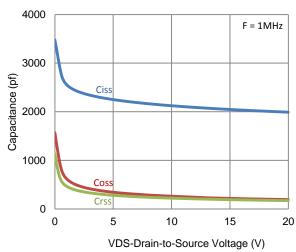
16
(V) tu 12
12
0
0
1 1 2 3

VGS - Gate-to-Source Voltage (V)

2. Transfer Characteristics

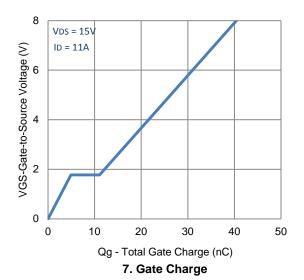


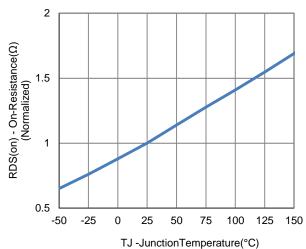
4. Drain-to-Source Forward Voltage

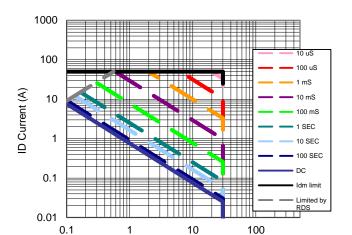


6. Capacitance

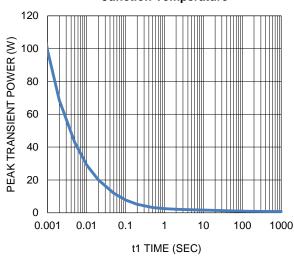
Typical Electrical Characteristics







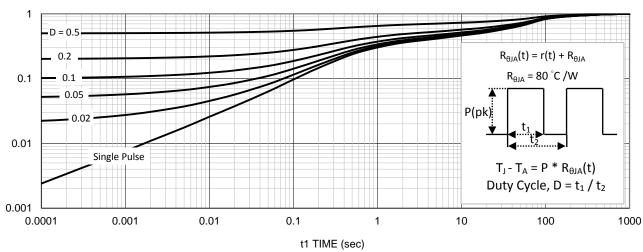
8. Normalized On-Resistance Vs Junction Temperature



VDS Drain to Source Voltage (V)

9. Safe Operating Area

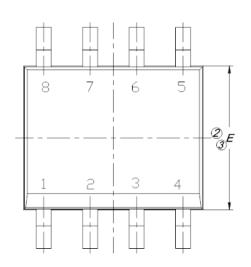
10. Single Pulse Maximum Power Dissipation

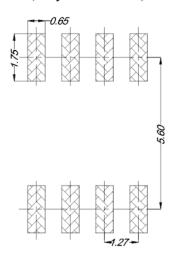


11. Normalized Thermal Transient Junction to Ambient

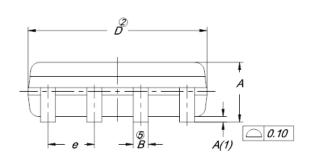
Package Information

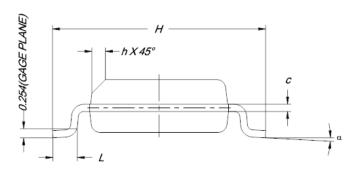
Land Pattern (Only for Reference)





| 5114 | MILLIMETERS | | | | | |
|------|-------------|------|------|--|--|--|
| DIM. | MIN. | NOM. | MAX. | | | |
| Α | 1.35 | 1.55 | 1.75 | | | |
| A(1) | 0.10 | 0.18 | 0.25 | | | |
| В | 0.38 | 0.45 | 0.51 | | | |
| С | 0.19 | 0.22 | 0.25 | | | |
| D | 4.80 | 4.90 | 5.00 | | | |
| Е | 3.80 | 3.90 | 4.00 | | | |
| е | 1.27 BSC | | | | | |
| Н | 5.80 | 6.00 | 6.20 | | | |
| L | 0.50 | 0.72 | 0.93 | | | |
| α | 0° | 4° | 8° | | | |
| h | 0.25 | 0.38 | 0.50 | | | |





Note:

- 1. All Dimension Are In mm.
- Package Body Sizes Exclude Mold Flash, Protrusion Or Gate Burrs. Mold Flash, Protrusion Or Gate Burrs Shall Not Exceed 0.10 mm Per Side.
- 3. Package Body Sizes Determined At The Outermost Extremes Of The Plastic Body Exclusive Of Mold Flash, Tie Bar Burrs, Gate Burrs And Interlead Flash, But Including Any Mismatch Between The Top And Bottom Of The Plastic Body.
- 4. The Package Top May Be Smaller Than The Package Bottom.
- Dimension "B" Does Not Include Dambar Protrusion. Allowable Dambar Protrusion Shall Be 0.08 mm Total In Excess Of "B" Dimension At Maximum Material Condition. The Dambar Cannot Be Located On The Lower Radius Of The Foot.