Unit: mm

TOSHIBA Field Effect Transistor Silicon P-Channel MOS Type (π-MOSV)

2SJ567

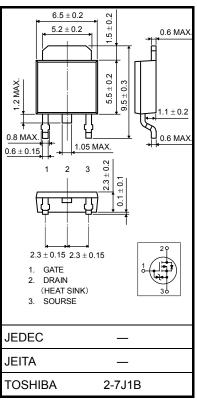
Switching Applications

Chopper Regulator, DC/DC Converter and Motor Drive Applications

- Low drain-source ON-resistance: $R_{DS (ON)} = 1.6 \Omega (typ.)$
- High forward transfer admittance: |Yfs| = 2.0 S (typ.)
- Low leakage current: $I_{DSS} = -100 \mu A \text{ (max) (V}_{DS} = -200 \text{ V)}$
- Enhancement model: $V_{th} = -1.5$ to -3.5 V ($V_{DS} = -10$ V, $I_{D} = -1$ mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	-200	V	
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V_{DGR}	-200	V	
Gate-source voltage		V _{GSS}	±20	V	
Drain current	DC (Note 1) I _D	-2.5	Α	
	Pulse (Note 1) I _{DP}	-10	ı	
Drain power dissipation (Tc = 25°C)		PD	20	W	
Single-pulse avalanche energy (Note 2)) E _{AS}	97.5	mJ	
Avalanche current		I _{AR}	-2.5	Α	
Repetitive avalanche energy (Note 3)) E _{AR}	2.0	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55 to 150	°C	



Weight: 0.36 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristic	Symbol	Max	Unit	
Thermal resistance, channel to case	R _{th (ch-c)}	6.25	°C/W	
Thermal resistance, channel to ambient	R _{th (ch-a)}	125	°C/W	

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: $V_{DD} = -50$ V, Tch = 25°C (initial), L = -25.2 mH, $I_{AR} = -2.5$ A, $R_G = 25~\Omega$

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.

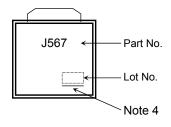
Electrical Characteristics (Ta = 25°C)

Char	acteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I _{GSS}	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μА
Drain cutoff curre	ent	I _{DSS}	V _{DS} = -200 V, V _{GS} = 0 V	_	_	-100	μА
Drain-source bre	akdown voltage	V (BR) DSS	$I_D = -10 \text{ mA}, V_{GS} = 0 \text{ V}$	-200	_	_	V
Gate threshold vo	oltage	V _{th}	$V_{DS} = -10 \text{ V}, I_D = -1 \text{ mA}$	-1.5	_	-3.5	V
Drain-source ON	-resistance	R _{DS} (ON)	$V_{GS} = -10 \text{ V}, I_D = -1.5 \text{ A}$	_	1.6	2.0	Ω
Forward transfer	admittance	Y _{fs}	$V_{DS} = -10 \text{ V}, I_D = -1.5 \text{ A}$	1.0	2.0	_	S
Input capacitance	Э	C _{iss}		_	410	_	
Reverse transfer capacitance		C _{rss}	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	40	_	pF
Output capacitance		C _{oss}]		145	_	
Switching time	Rise time	t _r	$V_{GS} = -1.5 \text{ A} V_{OUT}$ $V_{GS} = -10 \text{ V}$ $V_{DD} \approx -100 \text{ V}$ $V_{DD} \approx -100 \text{ V}$ $V_{DD} \approx -100 \text{ V}$	_	20	_	
	Turn-on time	t _{on}		_	45	_	
	Fall time	t _f		_	15	_	ns
	Turn-off time	t _{off}		_	85	_	
Total gate charge (Gate source plus gate-drain)		Qg	$V_{DD} \approx -160 \text{ V}, V_{GS} = -10 \text{ V},$	_	10	_	nC
Gate-source charge		Q _{gs}	$I_D = -2.5 \text{ A}$	_	6	_	
Gate-drain ("Miller") charge		Q _{gd}		_	4		

Source-Drain Ratings and Characteristics (Ta = 25°C)

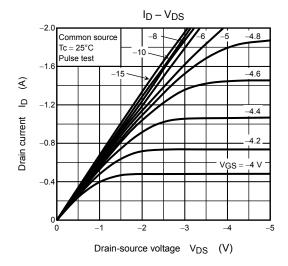
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I_{DR}	_	_	_	-2.5	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	-10	Α
Forward voltage (diode)	V_{DSF}	$I_{DR} = -2.5 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	2.0	V
Reverse recovery time	t _{rr}	$I_{DR} = -2.5 \text{ A}, V_{GS} = 0 \text{ V},$	_	135	_	ns
Reverse recovery charge	Q _{rr}	dl _{DR} /dt = 100 A/μs	_	0.81	_	μС

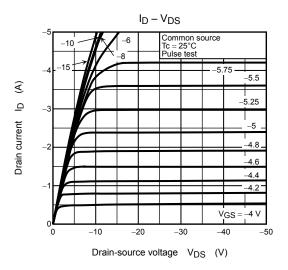
Marking

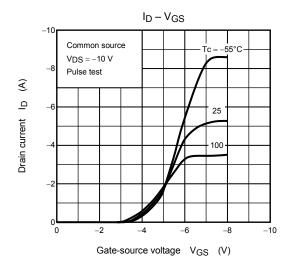


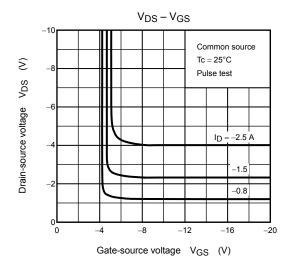
Note 4: A line under a Lot No. identifies the indication of product Labels $\hbox{\tt [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]}$

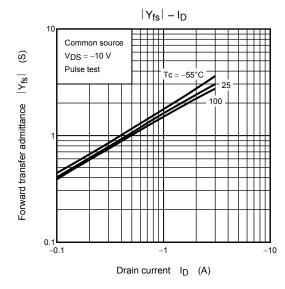
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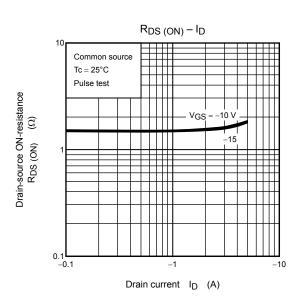


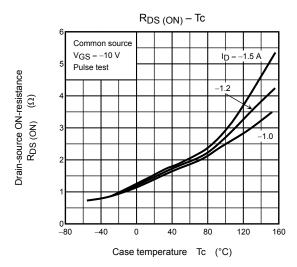


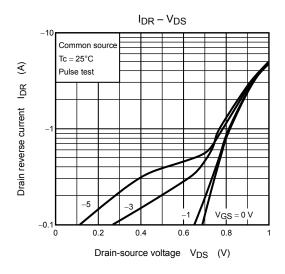


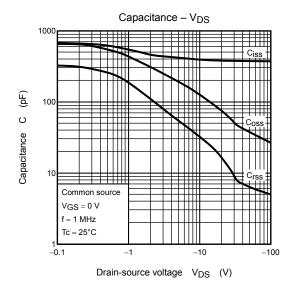


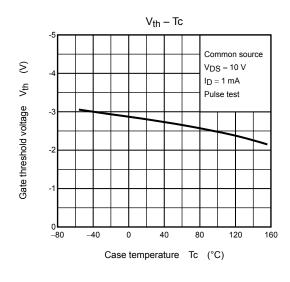


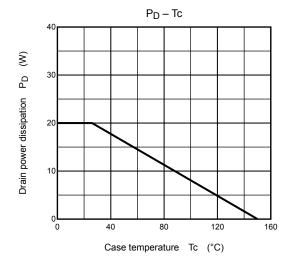


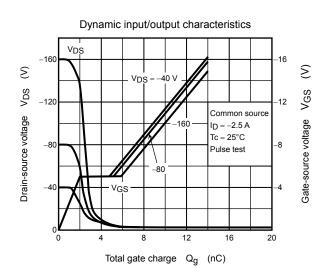


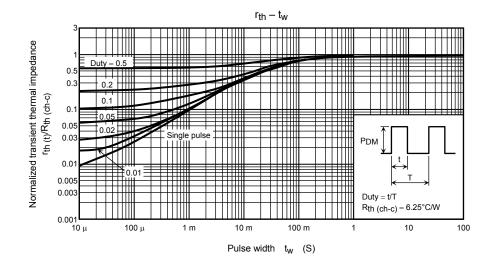


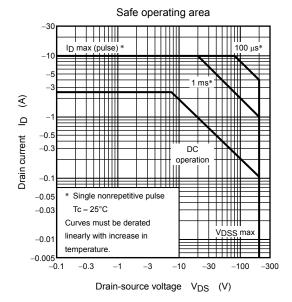


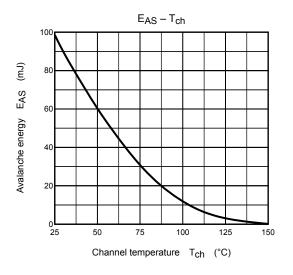


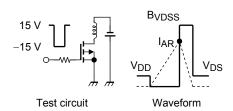












$$\begin{aligned} R_G &= 25~\Omega \\ V_{DD} &= -50~V,~L = 25.2~mH \end{aligned} \qquad E_{AS} &= \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{BVDSS}{BVDSS - V_{DD}} \right) \end{aligned}$$

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