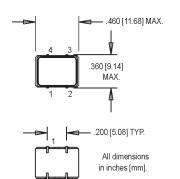
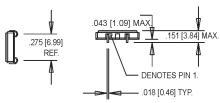
K1526C & K1536C Series 9x11 mm, 5.0 or 3.3 Volt, CMOS/TTL, VCXO

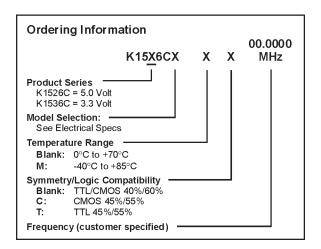


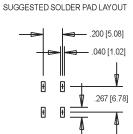


- Champion Product Former
- Phase-Locked Loops (PLL's), Clock Recovery, Reference Signal Tracking, Synthesizers, Frequency Modulation/ Demodulation









	.200 [5.08]	
-	.040 [1.02]	
# [267 [6.78]	
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.105 [2.67]		

Pin Connections

PIN	FUNCTION			
1	Voltage Control			
2	Ground & Gnd Plane			
3	Output			
4	+Vdd			

	PARAMETER	Symbol					Units
	Model		K15	26CA	K1526CD		
			K1536CA		K1536CD	K1526CE	
	Frequency Range	F	2 to 55	55.1 to 80	2 to 55	2 to 40	MHz
	Frequency Stability:	ΔF/F					
	Overall			f Calibration,	Temperature		
			Load, and	-			
	0°C to +70°C		±25	±40	±25	±32	ppm
	-40°C to +85°C		±50	±60	±50	±50	ppm
	Pullability						
	Minimum		±100	±80	±80	±200	ppm
	Maximum		±150	±160	±130		ppm
	PARAMETER	Symbol	Min.	Тур.	Max.	Units	Condition/Notes
١	Operating Temperature	TA		ing informatio			
us	Storage Temperature	Ts	-40		+125	∘C	
∺	Aging						
اق ا	1 st Year		-3/-5		+3/+5	ppm	<52 MHz / ≥52 MHz
Electrical Specifications	Thereafter (per year)		-1/-2		+1/+2	ppm	<52 MHz / ≥52 MHz
١ğ	Control Voltage	Vc	0.5	2.5	4.5	٧	K1526C
<u></u>			0.3	1.65	3.0	V	K1536C
1 😤			0		5.0	V	K1526CE
ec	Linearity				10	%	Positive Monotonic Slope
□	Modulation Bandwidth	fm	20			kHz	+3 dB
	Input Impedance	Zin	50K			Ohms	@ 10 kHz
	Input Voltage	Vdd	4.5	5.0	5.5	٧	K1526C
			3.0	3.3	3.6	٧	K1536C
	Input Current	ldd			30	mA	
	Output Type						CMOS/TTL
	Load				15	pF	HCMOS
	Symmetry (Duty Cycle)			ing information	on)		
	Logic "1" Level	Voh	Vdd -0.5			V	
	Logic "2" Level	Vol			0.5	V	
	Output Current	T./T(-		20	mA	000/ 1- 000/ 1/14 01 15 5
	Rise/Fall Time	Tr/Tf	-	-	5	ns	20% to 80% Vdd, CL = 15 pF
	Start up Time	* 1	-	1	10	ms	late waste of 40 kills - 00 kills
	Phase Jitter @ 26 MHz	ΦJ	400 !!	4	40.111	ps RMS	Integrated 12 kHz – 20 MHz
	Phase Noise (Typical)	10 Hz	100 Hz	1 kHz	10 kHz	100 kHz	Offset from carrier
Щ	@ 26 MHz	-65	-95	-115	-130	-140	dBc/Hz

MtronPTI reserves the right to make changes to the product(s) and service(s) described herein without notice. No liability is assumed as a result of their use or application.