Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π–MOSV)

2SK2699

Chopper Regulator, DC-DC Converter and Motor Drive Applications

 $\begin{array}{ll} \bullet & Low\ drain-source\ ON\ resistance & : RDS\ (ON) = 0.5\ \Omega\ (typ.) \\ \bullet & High\ forward\ transfer\ admittance & : |Y_{fs}| = 11\ S\ (typ.) \\ \bullet & Low\ leakage\ current & : IDSS = 100\ \mu A\ (max)\ (VDS = 600\ V) \\ \bullet & Enhancement\ mode & : V_{th} = 2.0 {\sim} 4.0\ V\ (VDS = 10\ V,\ ID = 1\ mA) \end{array}$

Maximum Ratings (Ta = 25°C)

Characteri	stics	Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	600	V	
Drain-gate voltage (R	_{3S} = 20 kΩ)	V_{DGR}	600	V	
Gate-source voltage		V _{GSS}	±30	V	
Drain current	DC (Note 1)	I _D	12	Α	
	Pulse (Note 1)	I _{DP}	48	Α	
Drain power dissipatio	n (Tc = 25°C)	P _D	150	W	
Single pulse avalanche	e energy (Note 2)	E _{AS}	605	mJ	
Avalanche current		I _{AR}	12	Α	
Repetitive avalanche e	energy (Note 3)	E _{AR}	15	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature ra	ange	T _{stg}	-55~150	°C	

1. GATE 2. DRAIN (HEAT SINK) 3. SOURCE JEDEC —

SC-65

2-16C1B

Weight: 4.6 g (typ.)

JEITA

TOSHIBA

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	0.833	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	50	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 7.35 mH, R_G = 25 Ω , I_{AR} = 12 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.



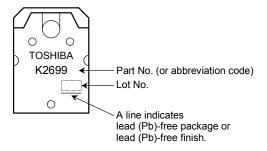
Electrical Characteristics (Ta = 25°C)

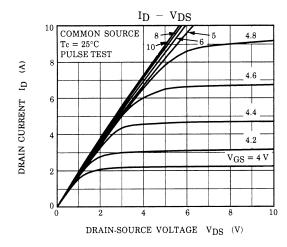
Charac	eteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	rrent	I _{GSS}	V _{GS} = ±25 V, V _{DS} = 0 V	_	_	±10	μΑ
Gate-source bre	eakdown voltage	V (BR) GSS	I _G = ±10 μA, V _{DS} = 0 V	±30	_	_	V
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 600 V, V _{GS} = 0 V	_	_	100	μΑ
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	600	_	_	V
Gate threshold v	oltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	_	4.0	V
Drain-source O	N resistance	R _{DS (ON)}	V _{GS} = 10 V, I _D = 6 A	_	0.5	0.65	Ω
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 6 A	6.0	11.0	_	S
Input capacitano	:e	C _{iss}			2600	_	pF
Reverse transfer capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	270	_	
Output capacitance		Coss] [820	_	
Switching time	Rise time	t _r	V_{GS} $0V$ $R_{L}=50\Omega$ V_{DD} $=300V$	_	45	_	ns ns
	Turn-on time	t _{on}		_	75	_	
	Fall time	t _f		_	65	_	
	Turn-off time	t _{off}	Duty $\leq 1\%$, $t_{\rm W} = 10 \mu \rm s$	_	270	_	
Total gate charge (gate-source plus gate-drain)		Qg		_	58	_	
Gate-source charge		Q _{gs}	$V_{DD} \approx 480 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 12 \text{ A}$		37		nC
Gate-drain ("miller") Charge		Q _{gd}			21	_	

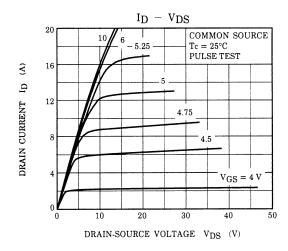
Source-Drain Diode Ratings and Characteristics (Ta = 25°C)

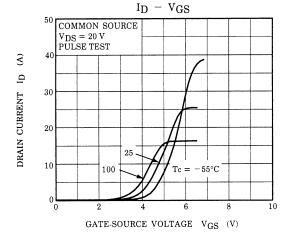
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}		_	_	12	Α
Pulse drain reverse current (Note 1)	I _{DRP}	-	_	_	48	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 12 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 12 A, V _{GS} = 0 V dI _{DR} / dt = 100 A / μs	1	460		ns
Reverse recovery charge	Q _{rr}	dI _{DR} / dt = 100 A / μs	_	4.8	_	μC

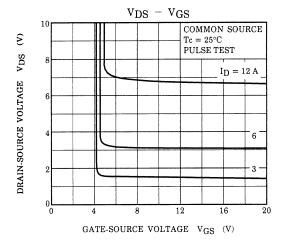
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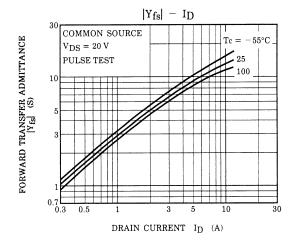


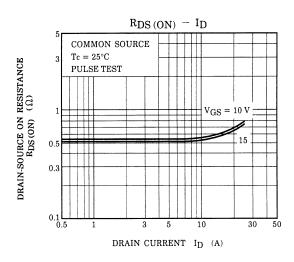


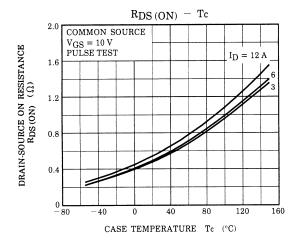


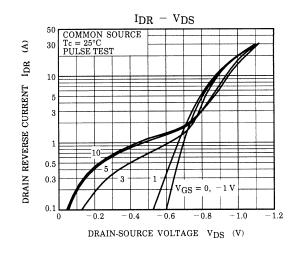


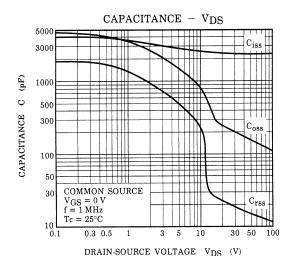


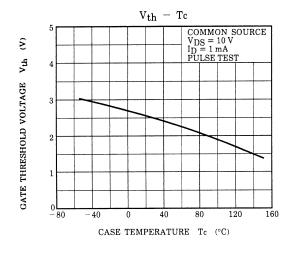


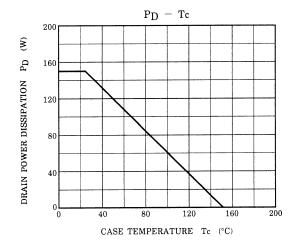


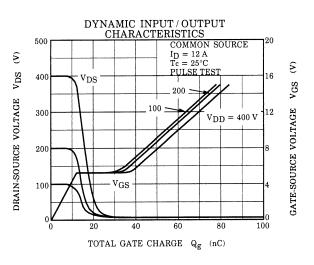


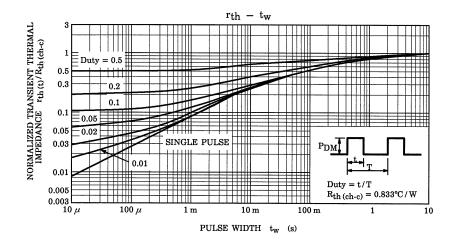


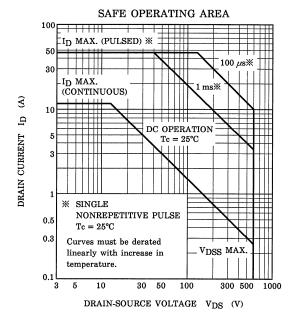


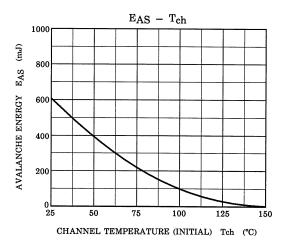


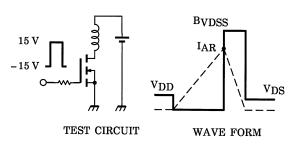












$$\begin{aligned} &R_G = 25~\Omega \\ &V_{DD} = 90~V,~L = 7.35~mH \end{aligned} \qquad E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{B_{VDSS}}{B_{VDSS} - V_{DD}} \right)$$

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