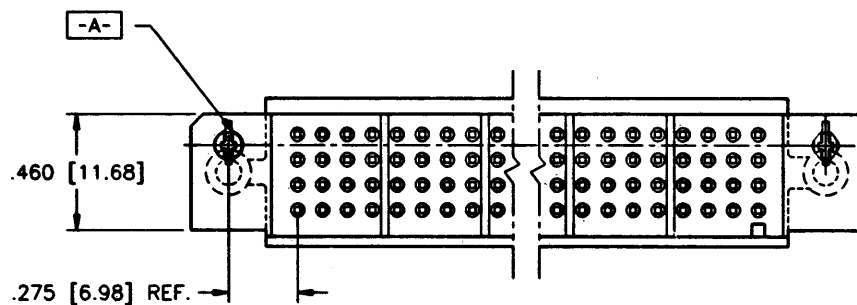
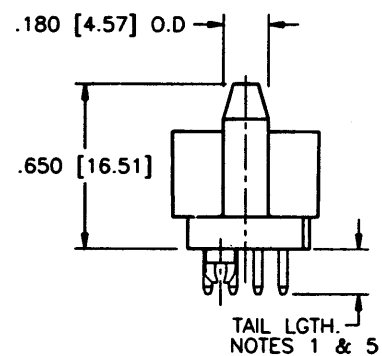


600001-XX001

[illegible]

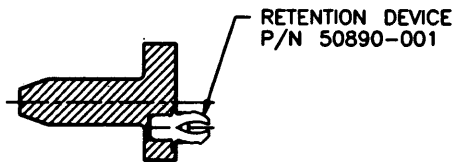
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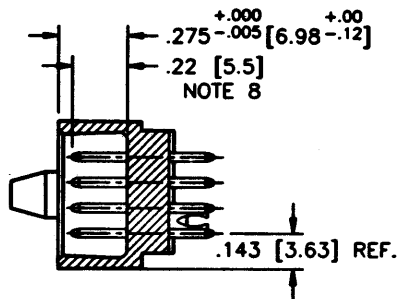
QTY. 1
TITLE HPC III VERT. HDR. ASSY.
0 G.P., S.T.

SCALE	SIZE	DWG. NO.	SHEET
3:1	C	60001	1 OF 2

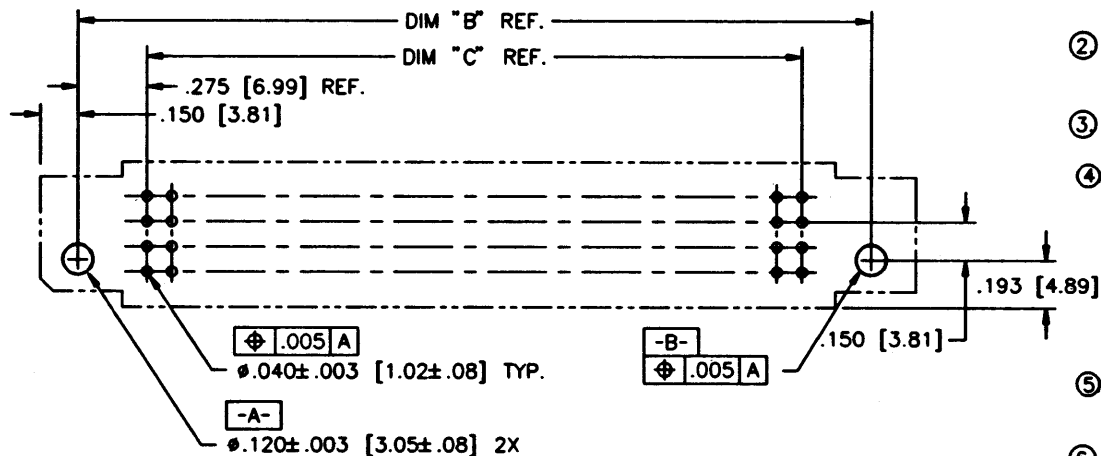
PRODUCT NUMBER	DIM.	LENGTH FORMULAS
60001-XX001	DIM "A"	$.100/2.54 \times \text{NO. OF COLUMNS} + .75/19.1$
	DIM "B"	$.100/2.54 \times \text{NO. OF COLUMNS} + .450/11.43$
	DIM "C"	$.100/2.54 \times \text{NO. OF COLUMNS} - .100/2.54$



SECTION U-U



SECTION X-X



RECOMMENDED PCB LAYOUT
(ALL HOLES LOCATED ON .100/2.54 GRID
UNLESS OTHERWISE SPECIFIED)

NOTES:

1. PRODUCT NUMBERING CODE:
- 60001 - X XXX X
- TAIL LENGTH(NOTE 5)
NO. OF POSITIONS(NOTE 2)
PLATING(NOTE 3)
BASE NUMBER
- ② THIS PRODUCT IS CONFIGURED IN SIZES OF 10 thru 80 COLUMNS IN INCREMENTS OF ONE.
i.e.: 10,11,12,13,.....78,79,80.
- ③ HOUSING MATERIAL: GLASS FILLED PPS.
PIN MATERIAL: PHOS BRONZE
- ④ PLATING CONTACT AREA OPTIONS:
1 = 30u" / .76ummm GOLD
5 = 30u" / .76ummm GXT
A = 50u" Ni UNDER ALL WITH 30u" Au ON CONTACT AREA SnPb ON PCB INTERFACE AND Au FLASH ON ALL OTHER SURFACES.
B = 50u" Ni UNDER ALL WITH 30u" GXT ON CONTACT AREA SnPb ON PCB INTERFACE AND Au FLASH ON ALL OTHER AREAS.
- ⑤ TAIL LENGTH OPTIONS: ($\pm .015 / .38$)
A = .177 / .45
J = .120 / 3.05
- ⑥ "BERG" NAME, P/N AND LOT CODE TO APPEAR ON THIS SURFACE PER BUS-12-105.
7. FOR DOCUMENTATION MENU, SEE BERG BUS-01-057
- ⑧ REFERENCE 50896 FOR F.M.L.B. AVAILABLE OPTIONS AND WORKSHEET.
9. MANUFACTURE AS PER BUS-24-004.

[illegible]

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BERG
ELECTRONICS

FILE
HPC III VERT. HDR. ASSY.
O G.P., S.T.

SCALE	SIZE	DESC. NO.	SHEET
3:1	C	60001	2 of 2