

# SIEMENS

## CNY17F SERIES PHOTOTRANSISTOR NO BASE CONNECTION OPTOCOUPLER

### FEATURES

- High Current Transfer Ratio  
CNY17F-1, 40-80%  
CNY17F-2, 63-125%  
CNY17F-3, 100-200%  
CNY17F-4, 160-320%
- Breakdown Voltage, 5300 VAC<sub>RMS</sub>
- High Collector-Emitter Voltage
- $V_{CEO}=70$  V
- No Base Terminal Connection for Improved Common Mode Interface Immunity
- Field-Effect Stable by TRIOS\*
- Long Term Stability
- Industry Standard Dual-In-Line Package
- Underwriters Lab File #E52744
- VDE #0884, Available with Option 1

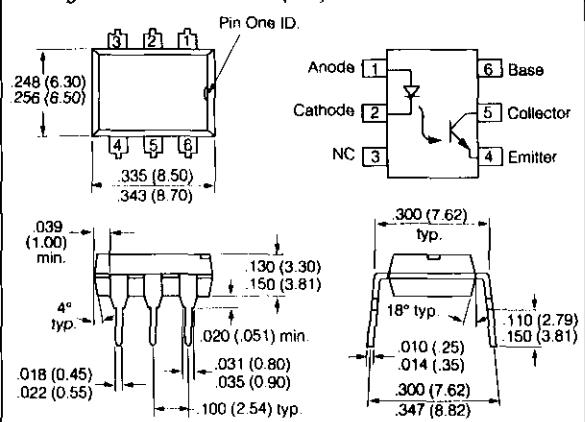
### DESCRIPTION

The CNY17F is an optocoupler consisting of a Gallium Arsenide infrared emitting diode optically coupled to a silicon planar phototransistor detector in a plastic plug-in DIP-6 package.

The coupling device is suitable for signal transmission between two electrically separated circuits. The potential difference between the circuits to be coupled is not allowed to exceed the maximum permissible reference voltages.

In contrast to the CNY17 Series, the base terminal of the F type is not connected, resulting in a substantially improved common-mode interference immunity.

Package Dimensions in Inches (mm)



### Maximum Ratings ( $T_A=25^\circ\text{C}$ )

#### Emitter

Reverse Voltage	.....	6 V
DC Forward Current	.....	60 mA
Surge Forward Current ( $t \leq 10 \mu\text{s}$ )	.....	2.5 A
Total Power Dissipation	.....	100 mW

#### Detector

Collector-Emitter Breakdown Voltage	.....	70 V
Collector Current	.....	50 mA
Collector Current ( $t \leq 1 \text{ ms}$ )	.....	100 mA
Total Power Dissipation	.....	150 mW

#### Package

Isolation Test Voltage (between emitter and detector referred to standard climate 23/50 DIN 50014)	.....	5300 VAC <sub>RMS</sub>
Creepage	.....	>7 mm
Clearance	.....	>7 mm
Isolation Thickness between Emitter and Detector	.....	20.4 mm
Comparative Tracking Index per DIN IEC 112/ VDE 0303, part 1	.....	175
Isolation Resistance ( $V_{IO}=500$ V)	.....	$\geq 10^{11} \Omega$
Storage Temperature Range	.....	-55 to +150°C
Ambient Temperature Range	.....	-55 to +100°C
Junction Temperature	.....	100°C
Soldering Temperature (max. 10 s, dip soldering: distance to seating plane $\geq 1.5$ mm)	.....	260°C

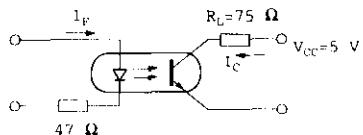
### Characteristics ( $T_A=25^\circ\text{C}$ )

	Symbol	Unit	Condition
<b>Emitter</b>			
Forward Voltage	$V_F$	V	$I_F=60$ mA
Breakdown Voltage	$V_{BR}$	V	$I_R=10 \mu\text{A}$
Reverse Current	$I_R$	$\mu\text{A}$	$V_R=6$ V
Capacitance	$C_O$	pF	$V_R=0$ V, $f=1$ MHz
Thermal Resistance	$R_{ThJA}$	K/W	
<b>Detector</b>			
Capacitance	$C_{CE}$	pF	$V_{CE}=5$ V, $f=1$ MHz
Thermal Resistance	$R_{ThJA}$	K/W	
<b>Package</b>			
Collector-Emitter Saturation Voltage	$V_{CEsat}$	V	$I_F=10$ mA,
Coupling Capacitance	$C_C$	pF	$I_C=2.5$ mA

**Current Transfer Ratio ( $I_C/I_F$  at  $V_{CE}=5$  V,  $25^\circ\text{C}$ )  
and Collector-Emitter Leakage Current by dash number**

	-1	-2	-3	-4	Unit
$I_C/I_F$ at $V_{CE}=5$ V ( $I_F=10$ mA)	40-80	63-125	100-200	160-320	%
$I_C/I_F$ at $V_{CE}=5$ V ( $I_F=1$ mA)	30 (>13)	45 (>22)	70 (>34)	90 (>56)	%
Collector-Emitter Leakage Current ( $V_{CE}=10$ V) ( $I_{CEO}$ )	2 ( $\leq 50$ )	2 ( $\leq 50$ )	5 ( $\leq 100$ )	5 ( $\leq 100$ )	nA

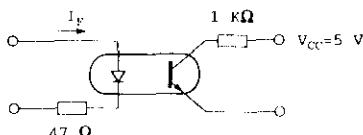
**Linear Operation (without saturation)**



$I_F=10$  mA,  $V_{CC}=5$  V,  $T_A=25^\circ\text{C}$

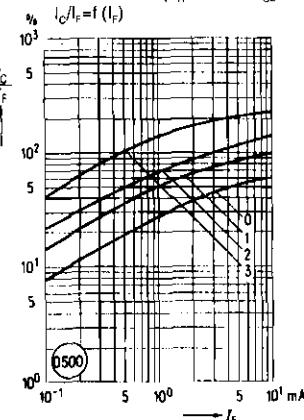
Load Resistance	$R_L$	75	Ω
Turn-On Time	$t_{ON}$	3.0	μs
Rise Time	$t_R$	2.0	μs
Turn-Off Time	$t_{OFF}$	2.3	μs
Fall Time	$t_f$	2.0	μs
Cut-Off Frequency	$f_{CO}$	250	kHz

**Switching Operation (with saturation)**

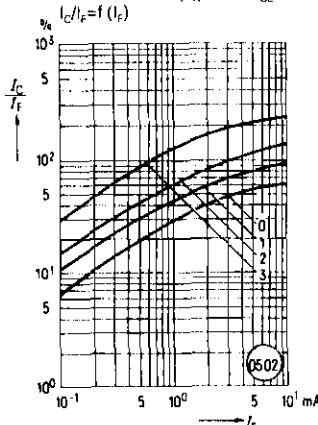


	-1 ( $I_F=20$ mA)	-2 and -3 ( $I_F=10$ mA)	-4 ( $I_F=5$ mA)		
Turn-On Time	$t_{ON}$	3.0	4.2	6.0	μs
Rise Time	$t_R$	2.0	3.0	4.6	μs
Turn-Off Time	$t_{OFF}$	18	23	25	μs
Fall Time	$t_f$	11	14	15	μs

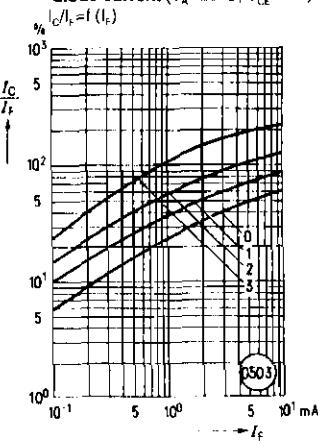
**Current transfer ratio versus  
diode current ( $T_A=-25^\circ\text{C}$ ,  $V_{CE}=5$  V)**

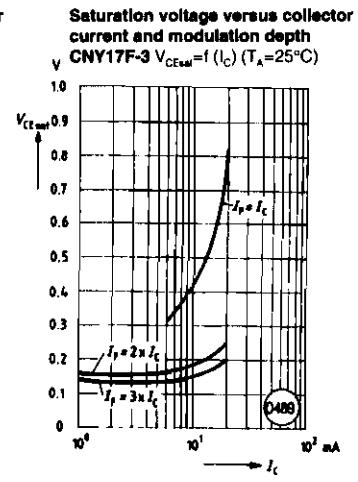
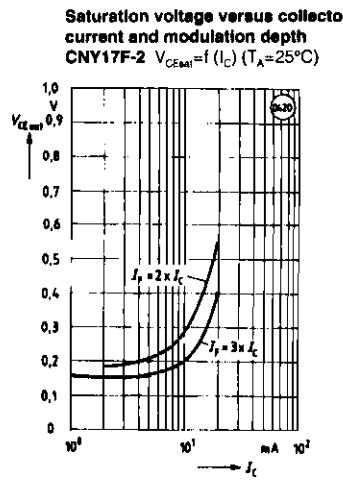
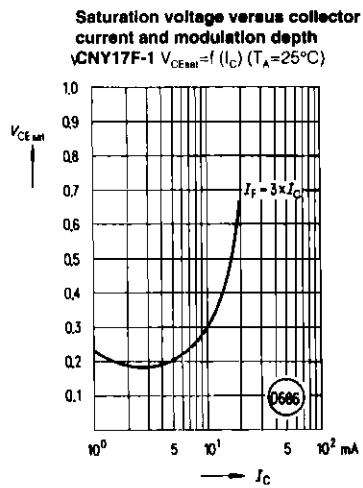
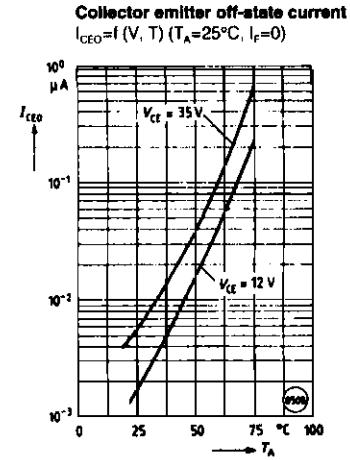
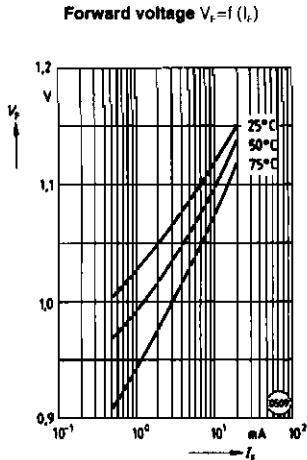
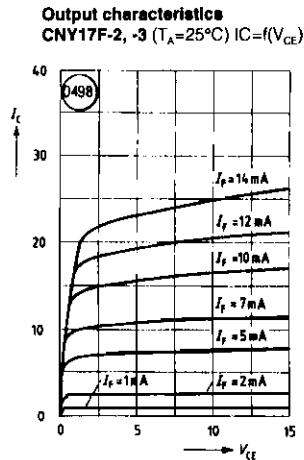
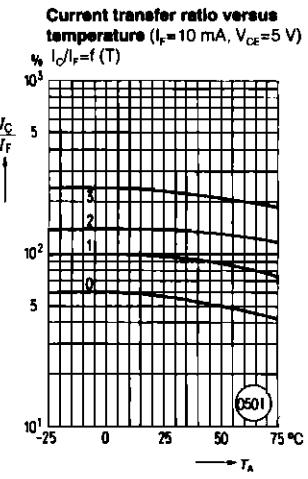
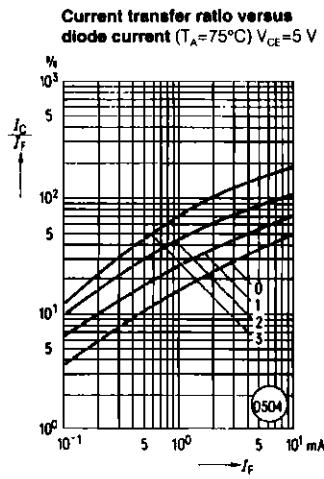
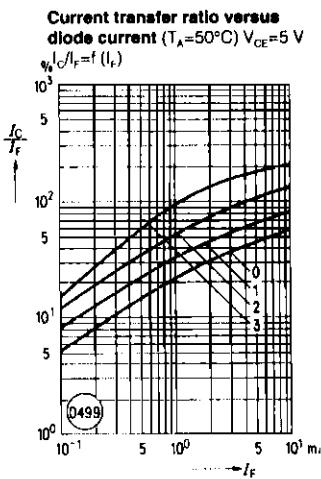


**Current transfer ratio versus  
diode current ( $T_A=0^\circ\text{C}$ ,  $V_{CE}=5$  V)**

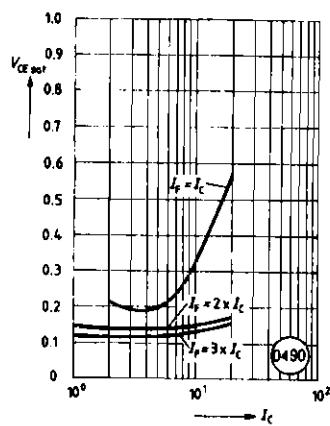


**Current transfer ratio versus  
diode current ( $T_A=25^\circ\text{C}$ ,  $V_{CE}=5$  V)**

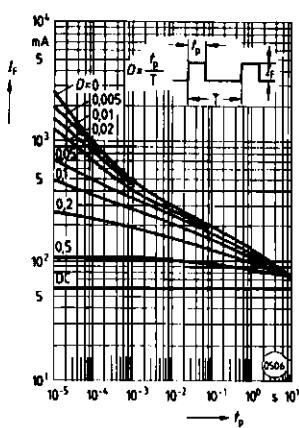




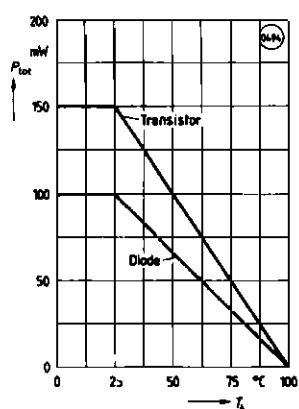
**Saturation voltage versus collector current and modulation depth**  
CNY17F-4  $V_{CEsat} = f(I_C)$  ( $T_A = 25^\circ\text{C}$ )



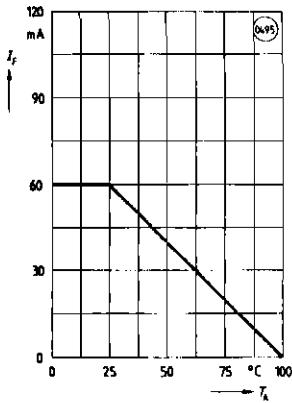
**Permissible pulse load**  
 $D$ -parameter,  $T_A = 25^\circ\text{C}$ ,  $I_F = f(t_p)$



**Permissible power dissipation transistor and diode**  $P_{tot} = f(T_A)$



**Permissible forward current diode**  
 $I_F = f(T_A)$



**Transistor capacitance**  
 $C = f(V_B)$  ( $T_A = 25^\circ\text{C}$ ,  $f = 1\text{ MHz}$ )

