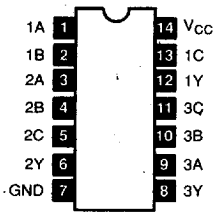
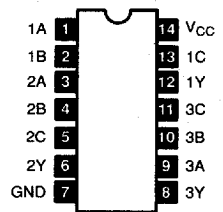
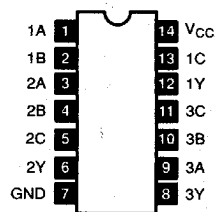
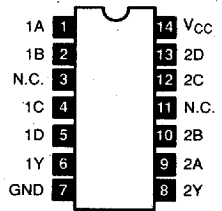
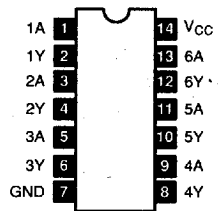
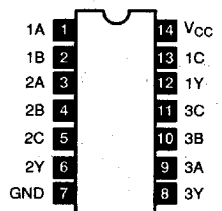
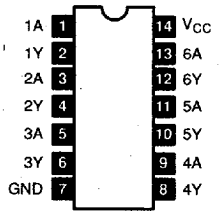
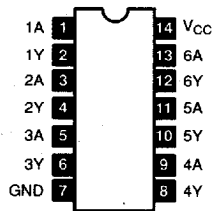
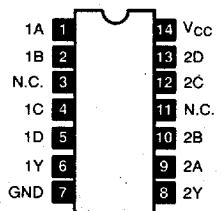
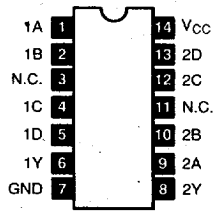
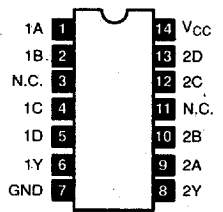
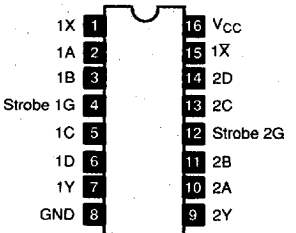


# INTEGRATED CIRCUITS - TTL

## (TRANSISTOR TRANSISTOR LOGIC)

<p><b>NTE7410, NTE74C10, NTE74H10, NTE74LS10, NTE74S10</b> Triple 3-Input Positive NAND Gate</p> 	<p><b>NTE7411, NTE74H11, NTE74HC11, NTE74LS11, NTE74S11</b> Triple 3-Input Positive AND Gate</p> 	<p><b>NTE7412, NTE74LS12</b> Triple 3-Input Positive NAND Gate W/Open Collector Outputs</p> 
<p><b>NTE7413, NTE74LS13</b> Dual 4-Input NAND Schmitt Trigger</p> 	<p><b>NTE7414, NTE74C14, NTE74HC14, NTE74HCT14, NTE74LS14</b> Hex Schmitt Trigger Inverter</p> 	<p><b>NTE74LS15, NTE74S15</b> Triple 3-Input AND Gate W/Open Collector Outputs</p> 
<p><b>NTE7416</b> Hex Inverter Buffer/Driver W/Open Collector High Voltage Outputs</p> 	<p><b>NTE7417</b> Hex Buffer/Driver W/Open Collector High Voltage Outputs</p> 	<p><b>NTE7420, NTE74C20, NTE74H20, NTE74LS20, NTE74S20</b> Dual 4-Input NAND Gate</p> 
<p><b>NTE7421, NTE74H21, NTE74LS21</b> Dual 4-Input AND Gate</p> 	<p><b>NTE7422, NTE74H22, NTE74LS22, NTE74S22</b> Dual 4-Input NAND Gate W/Open Collector Outputs</p> 	<p><b>NTE7423</b> Expandable Dual 4-Input NOR Gate W/Strobe</p> 

See Diagrams, beginning on Page 1-293

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