

SIEMENS

SFH 608

PHOTOTRANSISTOR, 5.3 KV, TRIOS® LOW CURRENT OPTOCOUPLED

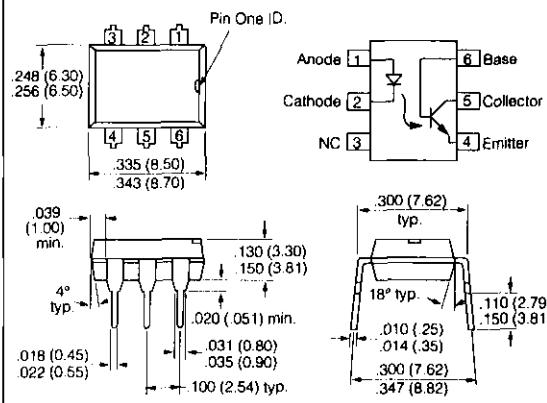
FEATURES

- Very High CTR at $I_F=1$ mA, $V_{CE}=0.5$ V
 - SFH608-2, 63-125%
 - SFH608-3, 100-200%
 - SFH608-4, 160-320%
 - SFH608-5, 250-500%
- Specified Minimum CTR at $I_F=0.5$ mA, $V_{CE}=1.5$ V: $\geq 32\%$ (typ. 120%)
- Good CTR Linearity with Forward Current
- Low CTR Degradation
- High Collector-Emitter Voltage $V_{CEO}=55$ V
- Isolation Test Voltage: 5300 VAC_{RMS}
- Low Current Input
- Low Coupling Capacitance
- High Common Mode Transient Immunity
- Phototransistor Optocoupler In 6 Pin DIP Package
- Field Effect Stable: TRIOS®
- VDE 0884 Available with Option 1
- Underwriters Lab File #E52744
- Applications
 - Telecommunications
 - Industrial Controls
 - Office Machines
 - Microprocessor System Interfaces

DESCRIPTION

The SFH 608 is an optocoupler designed for high current transfer ratio at low input currents with the output transistor saturated. This makes the device ideal for low current switching applications. The SFH608 is packaged in a six pin plastic DIP.

Package Dimensions in Inches (mm)



Maximum Ratings ($T_A=25^\circ\text{C}$)

Emitter

Reverse Voltage	6 V
DC Forward Current	50 mA
Surge Forward Current ($t_{\text{ps}} \leq 10 \mu\text{s}$)	2.5 A
Total Power Dissipation	70 mW

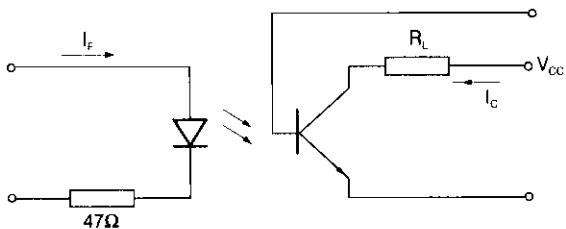
Detector

Collector-Emitter Voltage	55 V
Collector-Base Voltage	55 V
Emitter-Base Voltage	7 V
Collector Current	50 mA
Surge Collector Current ($t_{\text{ps}} \leq 1 \text{ ms}$)	100 mA
Total Power Dissipation	150 mW
Isolation Test Voltage (between emitter and detector/referred to climate)	
DIN 40046 part 2 Nov. 74 ($t=1 \text{ sec.}$)	5300 VAC _{RMS}
Creepage	≥ 7 mm
Clearance	≥ 7 mm
Comparative Tracking Index (per DIN IEC 112/VDE 0303, part 1)	175
Isolation Resistance	
$V_{IO}=500$ V, $T_A=25^\circ\text{C}$	$\geq 10^{12} \Omega$
$V_{IO}=500$ V, $T_A=100^\circ\text{C}$	$\geq 10^{11} \Omega$
Storage Temperature Range	-55°C to +150°C
Operating Temperature Range	-55°C to +100°C
Junction Temperature	100°C
Soldering Temperature (max. 10 sec., dip soldering: distance to seating plane ≥ 1.5 mm)	260°C

*TRIOS—TRansparent IOn Shield

Characteristics (T_A=25°C, unless otherwise specified)

	Symbol	Typ.	Unit	Condition
Emitter				
Forward Voltage	V _F	1.1 (\leq 1.5)	V	I _F =5 mA
Reverse Voltage	V _R	(\geq 6)	V	I _R =10 μ A
Reverse Current	I _R	0.01 (\leq 10)	μ A	V _R =6 V
Capacitance	C _O	25	pF	V _R =0 V, f=1 MHz
Thermal Resistance	R _{thJA}	1070	K/W	
Detector				
Collector-Emitter Voltage	V _{CEO}	\geq 55	V	I _{CE} =10 μ A
Emitter-Base Voltage	V _{EBO}	\geq 7	V	I _{EB} =10 μ A
Capacitance	C _{CE}	10	pF	V _{CE} =5 V, f=1 MHz
Capacitance	C _{CB}	16	pF	V _{CB} =5 V, f=1 MHz
Capacitance	C _{EB}	10	pF	V _{EB} =5 V, f=1 MHz
Thermal Resistance	R _{thJA}	500	K/W	
Package				
Coupling Capacitance	C _C	0.60	pF	
Coupling Transfer Ratio				
SFH 608-2	I _C /I _F	63–125 75 (\geq 32)	%	I _F =1 mA, V _{CE} =0.5 V I _F =0.5 mA, V _{CE} =1.5 V
SFH 608-3	I _C /I _F	100–200 120 (\geq 50)	%	I _F =1 mA, V _{CE} =0.5 V I _F =0.5 mA, V _{CE} =1.5 V
SFH 608-4	I _C /I _F	160–320 200 (\geq 80)	%	I _F =1 mA, V _{CE} =0.5 V I _F =0.5 mA, V _{CE} =1.5 V
SFH 608-5	I _C /I _F	250–500 300 (\geq 125)	%	I _F =1 mA, V _{CE} =0.5 V I _F =0.5 mA, V _{CE} =1.5 V
Collector-Emitter Saturation Voltage				
SFH 608-2	V _{CESat}	0.25 (\leq 0.4)	V	I _C =0.32 mA, I _F =1 mA
SFH 608-3	V _{CESat}	0.25 (\leq 0.4)	V	I _C =0.5 mA, I _F =1 mA
SFH 608-4	V _{CESat}	0.25 (\leq 0.4)	V	I _C =0.8 mA, I _F =1 mA
SFH 608-5	V _{CESat}	0.25 (\leq 0.4)	V	I _C =1.25 mA, I _F =1 mA
Collector-Emitter Leakage Current	I _{CEO}	10 (\leq 200)	nA	V _{CE} =10 V



I_C=2 mA (to adjust by I_F), R_L=100 Ω , T_A=25°C, V_{CC}=5 V

Description	Symbol	Values	Unit
Turn-On Time	t _{ON}	8	μ s
Rise Time	t _R	5	μ s
Turn-Off Time	t _{OFF}	7.5	μ s
Fall Time	t _F	7	μ s

Figure 1. Switching times T_A=25°C, I_F=1 mA, V_{CC}=5 V, t_{ON}, t_R, t_{OFF}, t_F=f(R_L)

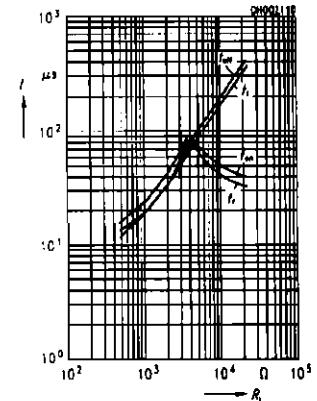


Figure 2. Current transfer ratio (typ.) V_{CE}=0.5 V, CTR=f(T_A, I_F)

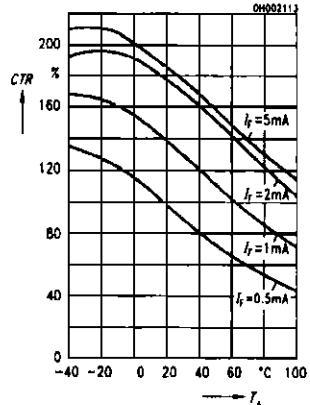


Figure 3. Current transfer ratio (typ.) V_{CE}=1.5 V, CTR=f(T_A, I_F)

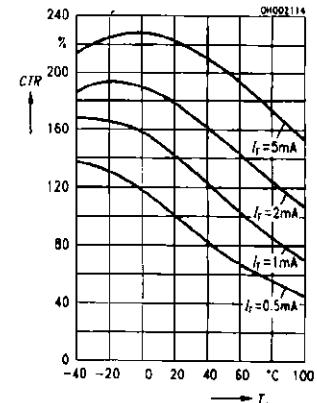


Figure 4. Diode forward voltage (typ.)
 $T_A=25^\circ\text{C}$, $V_F=f(I_F)$

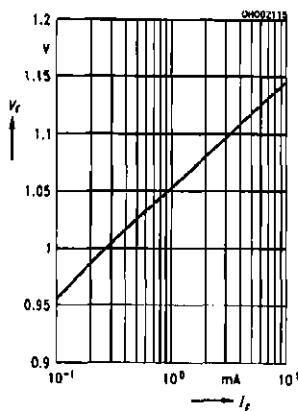


Figure 7. Output characteristics (typ.)
 $T_A=25^\circ\text{C}$, $I_{CE}=f(V_{CE}, I_F)$

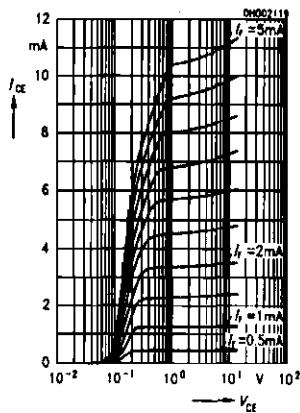


Figure 10. Transistor capacitance (typ.)
 $T_A=25^\circ\text{C}$, $f=1\text{ MHz}$, $C_{CE}=f(V_{CE})$,
 $C_{CB}=f(V_{CB})$, $C_{EB}=f(V_{EB})$

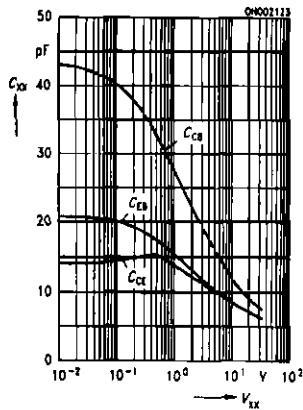


Figure 5. Diode forward voltage (typ.)
 $I_F=1\text{ mA}$, $V_F=f(T_A)$

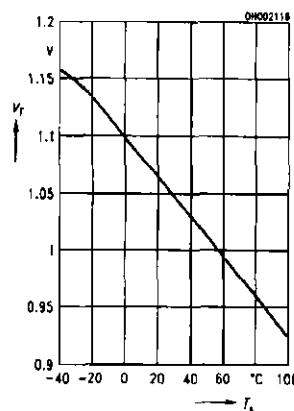


Figure 8. Permissible forward current diode
 $I_F=f(T_A)$

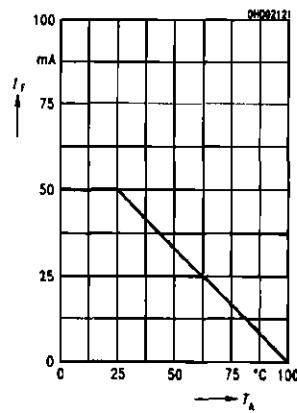


Figure 11. Collector-emitter leakage current
 $I_F=0$, $V_{CE}=10\text{ V}$, $I_{CEO}=f(T_A)$

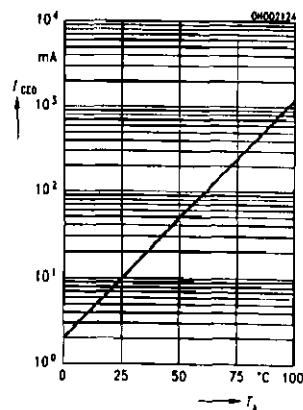


Figure 6. Output characteristics (typ.)
 $T_A=25^\circ\text{C}$, $I_{CE}=f(V_{CE}, I_B)$

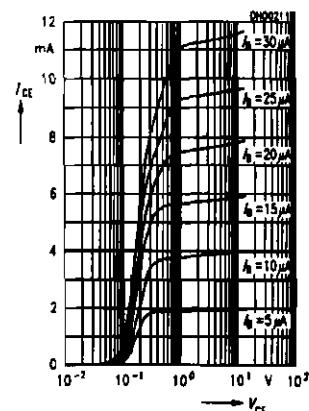


Figure 9. Permissible power dissipation
 $P_{OT}=f(T_A)$

