

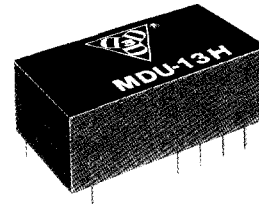
# Multiple Digital Delay Units

**SERIES: MDU-13H**  
**(16 pins DIP) 10KH ECL Interfaced**



## Features:

- Input & Output Buffered.
- 3 Delay lines/package.
- Fits in Standard 16 Pins DIP.



## Specifications:

- Total Delay Tolerance: See Table.
- Rise-time: 2 ns typical, for delays less than 40 ns.
- Supply voltage:  $-5 \text{ Vdc} \pm 5\%$ .
- Operating Temperature:  $-30^\circ\text{C}$  to  $85^\circ\text{C}$ .
- Power Dissipation:  $\sim 200 \text{ mw}$  typ. (no load).
- Temperature coefficient: 100 PPM/ $^\circ\text{C}$ .
- Fan-out: 70 ECL loads.
- DC parameters: See ECL-10KH Logic Table on Page 6.

## Test Conditions:

- Input pulse-width: 150% of total delay.
- Input pulse rise-time:  $\leq 6 \text{ ns}$ .
- Input pulse voltage:  $\sim .7\text{V}$
- Rise-time measured from 20% to 80% of leading edge.
- Delay time measured at 50% of leading edge.
- All measurements taken ( $V_{EE} = -5.2\text{V}$  and  $T_A = 25^\circ\text{C}$ ).

Part No.	Delay Per Line (ns)
MDU-13H-3	$3 \pm 1$
MDU-13H-4	$4 \pm 1$
MDU-13H-5	$5 \pm 1$
MDU-13H-10	$10 \pm 1$
MDU-13H-15	$15 \pm 1$
MDU-13H-20	$20 \pm 1$
MDU-13H-25	$25 \pm 2$
MDU-13H-30	$30 \pm 2$
MDU-13H-35	$35 \pm 2$
MDU-13H-40	$40 \pm 2$
MDU-13H-45	$45 \pm 2.2$
MDU-13H-50	$50 \pm 2.5$

Other delay times on request.

