

# MOS LSIs

## ■ CCD Delay Line Series (1)

Category	Type No.	System	Stages	Clock Frequency	Signal Area (-3dB)	Input Signal Amplitude	Clock Driver	Supply Voltage	Power Consumption	Composition		Package	No.	
					COM Depth					Clock Multiplier	Auto-bias clamp			
N M O S	Quasi-single power supply 5V, (6V)	MN3801/S	NTSC	906	4fsc	6.0MHz	400mV <sub>p-p</sub>	built-in	5V 200mW 6V 0.01mW	1H	—	—	DIP014-P-0300A SOP018-P-0300	L4 L34
		MN3802A/S	PAL	1,131	4fsc	6.0MHz	200mV <sub>p-p</sub>	built-in	5V 300mW 6V 0.01mW	1H	—	—	DIP014-P-0300A SOP018-P-0300	L4 L34
		MN3803/S	NTSC	452	4fsc	6.0MHz	400mV <sub>p-p</sub>	built-in	5V 150mW 6V 0.01mW	0.5H	—	—	DIP014-P-0300A SOP018-P-0300	L4 L34
	Supply voltage 5V	MN8040	NTSC	906	4fsc	5.5MHz	200mV <sub>p-p</sub>	built-in	5V 200mW	1H	—	—	DIP014-P-0300A	L4
		MN8029LS	NTSC	226	fsc	1.4MHz	200mV <sub>p-p</sub>	built-in	5V 30mW	1H	—	—	SOP008-P-0225	L29
		MN8037LS	PAL	282	fsc	1.6MHz	200mV <sub>p-p</sub>	built-in	5V 55mW	1H	—	—	SOP008-P-0225	L29
	MN3106/S	LSI for clock multiplier (fsc → 4fsc) For MN3801/3802A/3803/8040/8041/8028A							5V 45mW				DIP008-P-0300 SOP008-P-0225	L1 L29
	Supply voltage 9V	MN8029/S	NTSC	226	fsc	1.6MHz	400mV <sub>p-p</sub>	built-in	9V 90mW	1H	—	—	DIP008-P-0300 SOP008-P-0225	L1 L29
		MN8037/S	PAL	282	fsc	1.9MHz	400mV <sub>p-p</sub>	built-in	9V 125mW	1H	—	—	DIP008-P-0300 SOP008-P-0225	L1 L29
		MN8038/S	SECAM	280	fsc	1.9MHz	400mV <sub>p-p</sub>	built-in	9V 125mW	1H	—	—	DIP008-P-0300 SOP008-P-0225	L1 L29
		MN8036/S	NTSC	112	fsc → 1/2fsc	0.7MHz	400mV <sub>p-p</sub>	built-in	9V 45mW	1H	—	—	DIP008-P-0300 SOP008-P-0225	L1 L29
		MN8033/S	PAL	140	fsc → 1/2fsc	0.9MHz	400mV <sub>p-p</sub>	built-in	9V 60mW	1H	—	—	DIP008-P-0300 SOP008-P-0225	L1 L29
	Supply voltage 12V	MN8028A	NTSC	455 × 2	4fsc	5.5MHz	500mV <sub>p-p</sub>	external MN3104	12V 25mW	0.5H 0.5H	—	—	DIP016-P-0300A	L7
		MN3104	CCD delay line MN8028A clock driver							12V 680mW				HDIP014-P-0300F
	C M O S	Quasi-single power supply 5V, (9V)	MN3810K/S	NTSC	454	fsc → 2fsc	2.4MHz	250mV <sub>p-p</sub>	built-in	5V 100mW 9V 0.01mW	1H	○ (× 2)	○	SDIP018-P-0300 SOP018-P-0300
MN3811K/S			PAL	566.5	fsc → 2fsc	2.8MHz	250mV <sub>p-p</sub>	built-in	5V 125mW 9V 0.01mW	1H	○ (× 2)	○	SDIP018-P-0300 SOP018-P-0300	L18 L34
Low EMI Low clock noise Supply voltage 5V, 9V		MN3814/S	NTSC	906	4fsc	5.5MHz	500mV <sub>p-p</sub>	built-in	5V 130mW 9V 90mW	1H	—	○	DIP008-P-0300 SOP018-P-0300	L1 L34
		MN3815/S	PAL	1,132	4fsc	6.5MHz	500mV <sub>p-p</sub>	built-in	5V 200mW 9V 90mW	1H	—	○	DIP008-P-0300 SOP018-P-0300	L1 L34
		MN3830S	NTSC	454	2fsc	3.0MHz	500mV <sub>p-p</sub>	built-in	5V 45mW 9V 45mW	1H	○ (× 2)	○	SOP016-P-0225	L32
Low EMI Low clock noise Supply voltage 5V single		MN3850/S	NTSC	454	2fsc	3.0MHz	500mV <sub>p-p</sub>	built-in	4.9V 70mW	1H	—	○	DIP008-P-0300 SOP008-P-0225	L1 L29
		MN3851MS	PAL	566.5	2fsc	3.5MHz	500mV <sub>p-p</sub>	built-in	4.9V 90mW	1H	—	○	SOP008-P-0225	L29
		MN3857S	NTSC	680 × 2	fsc → 3fsc	4.2MHz	500mV <sub>p-p</sub>	built-in	4.9V 150mW	1H 1H	○ (× 3)	○	SOP016-P-0300	B68
		MN3858S	NTSC	454	2fsc	3.0MHz	500mV <sub>p-p</sub>	built-in	4.9V 70mW	1H	—	○	SOP008-P-0225	L29
		MN3859S	PAL	566.5	2fsc	3.5MHz	500mV <sub>p-p</sub>	built-in	4.9V 90mW	1H	—	○	SOP008-P-0225	L29
		MN3866S	NTSC	681 × 2 or 606 × 2 (Switch by SW)	fsc → 3fsc or 9.54MHz (Switch by SW)	4.2MHz or 3.7MHz	500mV <sub>p-p</sub>	built-in	4.9V 165mW	1H 1H	○ (× 3)	○	SOP020-P-0300	L35
MN38662S		NTSC	681 × 2 or 606 × 2 (Switch by SW)	fsc → 3fsc or 9.535MHz (Switch by SW)	4.2MHz or 3.7MHz	500mV <sub>p-p</sub>	built-in	4.8V 160mW	1H 1H	○ (× 3)	○	SOP020-P-0300	L35	

(Package Symbol) DIP = Dual-In-Line Plastic Package, SDIP = Shrink Dual-In-Line Plastic Package, SOP = Small Outline Package, (PANAFLET PACKAGE)