

HCMOS-Logic Digital Delay Units

SERIES: **DDU-8C**

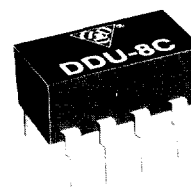
**5 Outputs
8 pin DIP**

**data
delay
devices, inc.**



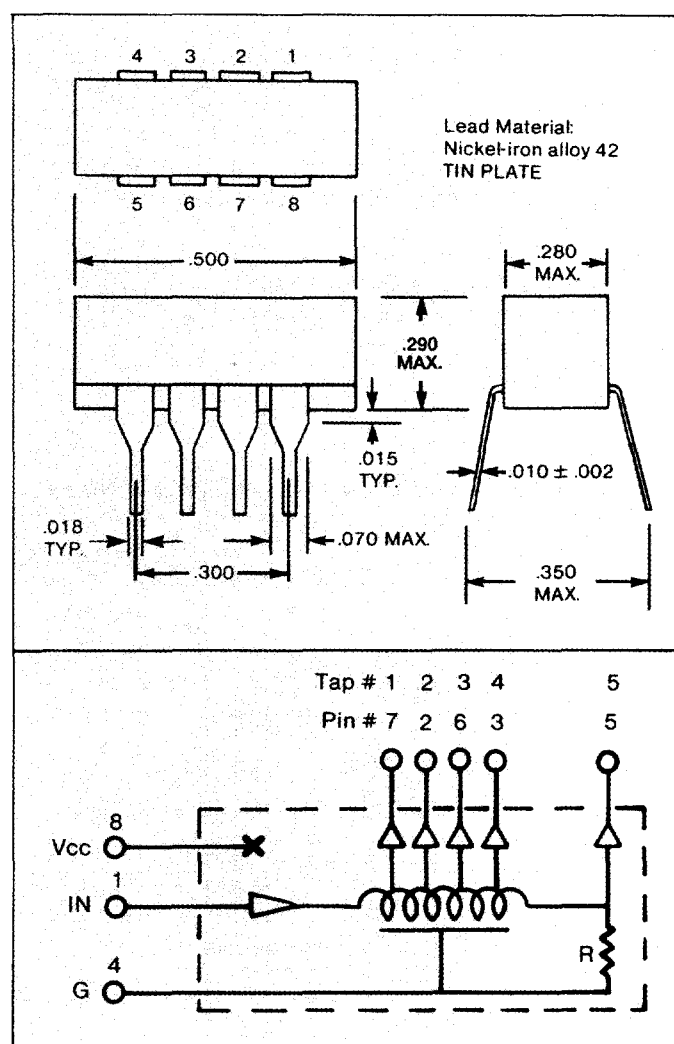
Features:

- Low Cost.
- Completely interfaced for CMOS.
- Low profile.
- Fits standard 8 pins DIP socket.



Specifications:

- No. Taps: 5 equally spaced taps.
- Total delay tolerance: $\pm 5\%$ or 2 ns
whichever is greater.
- Rise time: 8 ns typ.
- Temperature coefficient: 300 PPM/ $^{\circ}\text{C}$.
- Temperature range: 0°C to $+70^{\circ}\text{C}$.
- Supply Voltage: 5 Vdc $\pm 5\%$.
- Supply current:
 $I_{\text{CCH}} = 10 \text{ ma}$
 $I_{\text{CCL}} = 40 \mu\text{a}$
- Fan-out: 10 LSTTL loads min.
- DC Parameters: See HCMOS-Logic Table on Page 6.



Part No.	Total Delay (ns)	Tap Delay (ns)
DDU-8C-5050	50	10 ± 3
DDU-8C-5060	60	12 ± 3
DDU-8C-5075	75	15 ± 3
DDU-8C-5100	100	20 ± 3
DDU-8C-5125	125	25 ± 3
DDU-8C-5150	150	30 ± 3
DDU-8C-5175	175	35 ± 4
DDU-8C-5200	200	40 ± 4
DDU-8C-5250	250	50 ± 5