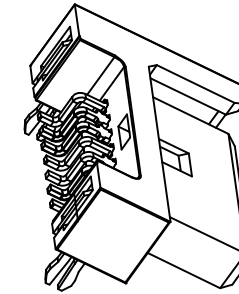
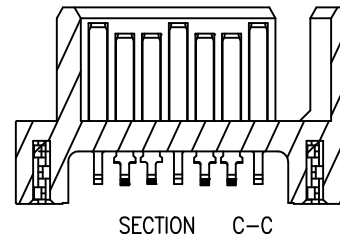
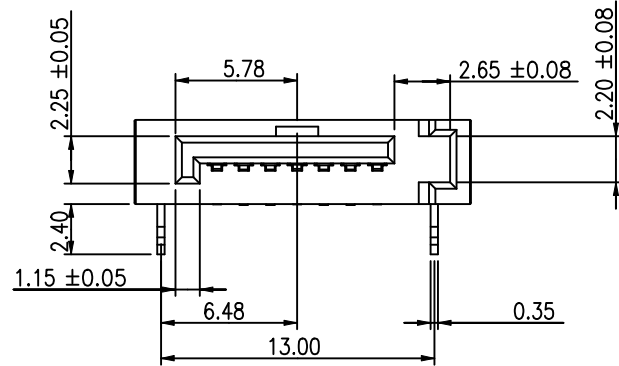


A1RFRD10

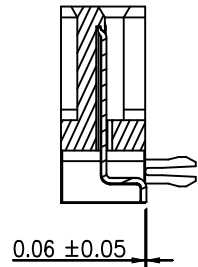
REVISION RECORD				
REV	ECO	DESCRIPTION	DRFT	CHKD



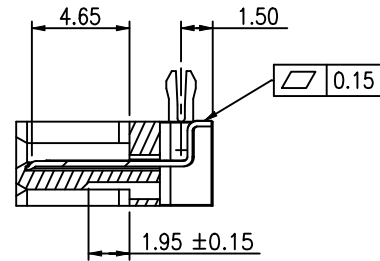
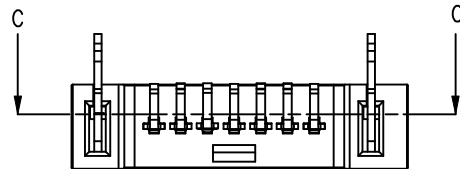
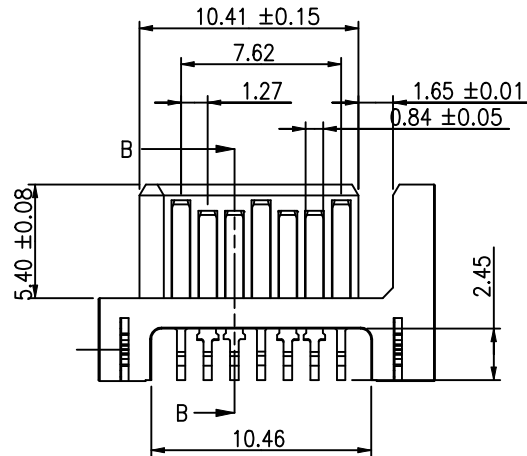
NOTES :

- MATERIAL :
HSG : LCP UL 94-V0
TERMINAL : PHOSPHOR BRONZE
LOCK FOOT : PHOS-BRONZE
- PLATING :
TERMINAL : GOLD PLATING OPTION
SOLDER TAIL : PLATED TIN OVER NICKEL
LOCK FOOT : PLATED TIN OVER NICKEL
PART NUMBER : E8177-71U91X-L

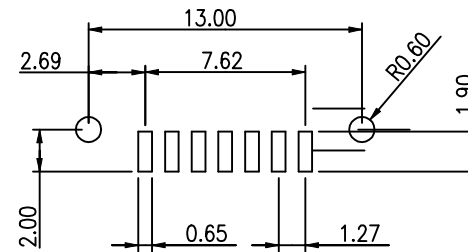
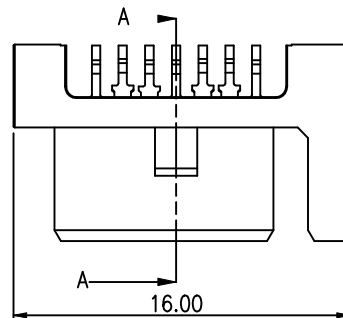
PLATING: X
1- 3u" 2- 6u"
3-15u" 4-30u"
5-50u"



SECTION B-B



SECTION A-A

RECOMMENDED PCB LAYOUT
(t=1.2mm)

DETACHED LISTS			DFTO SAMMY	DATE 08/17/05	FULL RISE ELECTRONIC CO., LTD			
	TOLERANCES EXCEPT AS NOTED		CHKD ANJUE	DATE 2009/08/17				
			MFO	DATE				
			APPLV LUSHENG	DATE 2009/08/17				
			MATERIAL :		TITLE HOR. FACE_DOWN SMD SATA PLUG SIGNAL			
	ANGLES ± 0.5		QT'Y :					
			FINISH :					
	THIRD ANGLE PROJECTION		SCALE : 4:1		DRAWING NO. GE813A24-LA /PART NO. SEE NOTE		SIZE A3	REV 0
	DO NOT SCALE DRAWING				SHEET 1 OF 1			

8

7

6

5

4

3

2

1