

## Dual-Port 10/100/1000BASE-T Ethernet Controller with Integrated TCP Processing Engine, RDMA, and iSCSI with PCI Express®

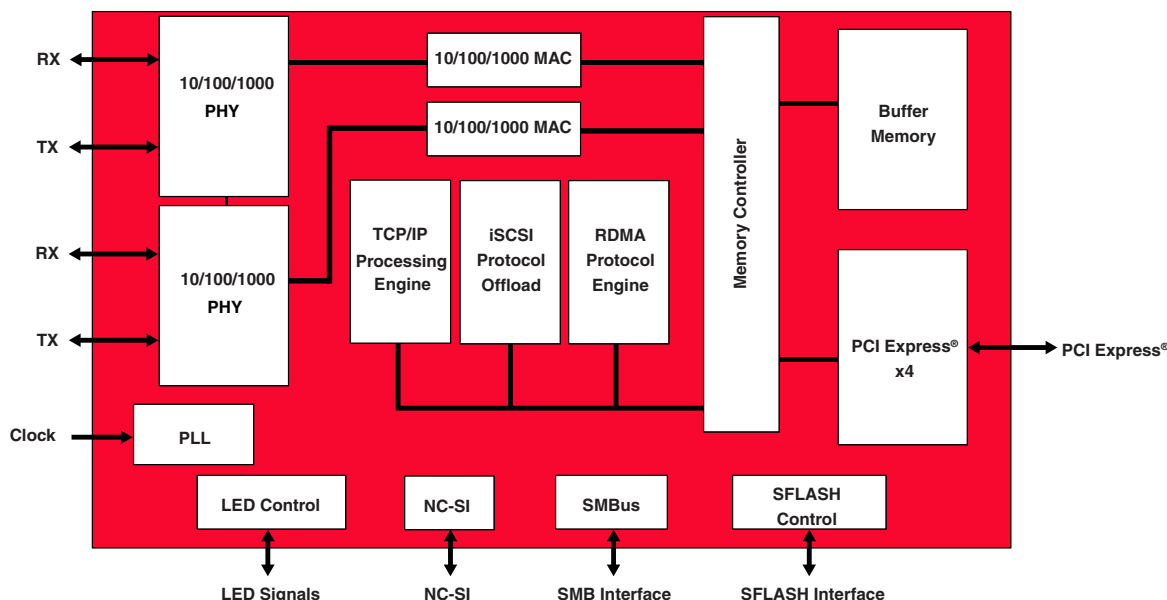
### FEATURES

- **Dual-port, single-chip solution for LAN on Motherboard (LOM) and Network Interface Card (NIC) applications**
  - Integrated 10BASE-T/100BASE-TX/1000BASE-T transceivers
  - Host interfaces
    - PCI Express® x4 Host interface
    - PCI Express V2.0-Ready
- **TCP processing engine**
  - Full Fast-path TCP processing
  - Support for IPv4 and IPv6
- **iSCSI controller**
  - iSCSI initiator
- **RDMA controller (RNIC)**
  - RDMA over TCP (iWARP)—RDMAC 1.0 compliant
  - Hardware-based data placement in application buffers without CPU intervention (User and Kernel modes)
- **Other performance features**
  - Receive Side Scaling (RSS)
  - TCP, IP checksum offload
  - TCP segmentation offload
  - Adaptive interrupts
  - Message Signal Interrupt (MSI and MSI-X) support
- **Robust manageability**
  - Network Controller Sideband Interface (NC-SI)
  - PXE 2.0 remote boot
  - Network Controller - Sideband Interface (NC-SI) support
  - Wake-On LAN
  - IPMI pass-through feature
  - Statistics gathering (SNMP MIB II, Ethernet-like MIB, Ethernet MIB (802.3x, clause 30))
  - Comprehensive diagnostic and configuration software suite
  - ACPI 1.1a compliant power management
- **Advanced network features**
  - Virtual LANs—802.1q VLAN tagging
  - Jumbo frames (9 KB)
  - 802.3x flow control
- **Low-power CMOS design**
- **On-chip power circuit controller**
- **676-ball 27 mm x 27 mm FBGA package**
- **3.3V I/Os**
- **JTAG**

### SUMMARY OF BENEFITS

- **Industry's first dual-port 10/100/1000 TOE solution—power and space optimized for server blade and low-profile NIC applications.**
- **Extremely low CPU utilization for TCP/IP applications**
  - Host CPU is free to run application code.
- **Accelerated IP-based storage**
  - Lower CPU utilization for file-level storage protocols such as CIFS and NFS.
  - iSCSI functionality with low CPU utilization
- **RDMA support for data placement in application buffers reduces CPU utilization and lowers data transit latencies.**
- **Future-proof**
  - Flexible implementation for TCP, iWARP, and iSCSI can accommodate specification changes and interoperability issues.
- **Performance-focused – optimized for throughput and CPU utilization**
  - Adaptive interrupts reduce system overhead for network processing.
  - RSS reduces CPU utilization on multi-CPU systems.
  - MSI and MSI-X allow interrupt distribution in a multi-CPU host system.
  - PCI Express host interface allows a low-latency access to CPU and Memory resources.
- **Robust and highly manageable**
  - NC-SI enables high bandwidth out-of-band system management functionality over shared infrastructure.
  - NC-SI interface standardized through the DMTF for easier product integration.
  - PXE 2.0, ACPI 1.1, Wake-On LAN
  - IPMI pass-through capability allows on-board management controllers access to the network in OS-present and OS-absent states.
- **Server class reliability, availability, and performance features**
  - Link aggregation and load balancing
    - Switch-dependent
  - 802.3ad (LACP), generic trunking (GEC/FEC)
    - Switch and NIC independent
- **Low power consumption**
  - Advanced power management
- **Minimal real estate—ideal for LOM**
  - On-chip power circuit controller

## OVERVIEW



**BCM5709C Block Diagram**

The BCM5709C provides a dual-port, fully integrated Layer 4 and Layer 5 solution - TCP/IP, RDMA, and iSCSI along with two complete 10/100/1000BASE-T Gigabit Ethernet Physical Layer Devices, IEEE 802.3 compliant Media Access Controls (MAC) and Physical Layer Transceiver solution for high performance network applications. By itself, the BCM5709C provides a complete dual-port, single-chip Gigabit Ethernet NIC with TCP/IP Processing Engine, RDMA NIC (RNIC), iSCSI HBA or LOM solution.

The BCM5709C is different from other network controllers because it can process the TCP/IP and relevant L5 protocols on data directly from the application buffers on the host, thereby relieving the host CPU from these time-consuming operations. On the receive path, the BCM5709C processes the frame up to the highest layer supported present in it, for example, the BCM5709C processes the frame for RDMA when the frame is an RDMA frame.

With the appropriate configuration, the BCM5709C can simultaneously support the following three functions:

- RDMA Network Interface Controller (RNIC)
- iSCSI Host Bus Adapter
- TOE Chimney-enabled network accelerator

### Target Applications of the BCM5709C

- Gigabit Ethernet NICs and LAN-on Motherboard (LOM)
- iSCSI Host Bus Adapters (HBA)
- RDMA Network Interface Card (RNIC)

#### Network Interface Card (NIC) Designs

|                        |                    |
|------------------------|--------------------|
| Dual 10/100/1000BASE-T | PCI Express x4 NIC |
|------------------------|--------------------|

#### LAN on Motherboard (LOM) Designs

|                        |                    |
|------------------------|--------------------|
| Dual 10/100/1000BASE-T | PCI Express x4 NIC |
|------------------------|--------------------|

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