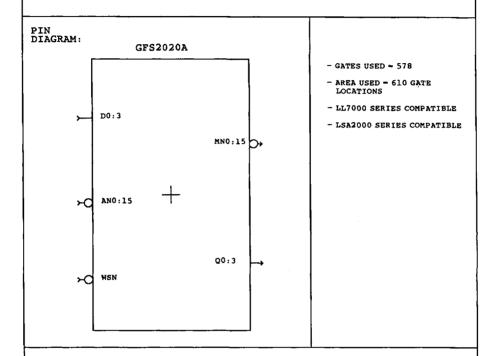
GFS2020A

#### 16 X 4 CAM WITHOUT MASK

#### GENERAL DESCRIPTION:

THE GFS2020A MEGAFUNCTION IS A 16-WORD BY 4-BIT CONTENT ADDRESSABLE MEMORY (CAM). THIS MEGAFUNCTION IS FUNCTIONALLY IDENTICAL TO THE FAIRCHILD 100142 EXCEPT THAT IT HAS SIXTEEN WORDS INSTEAD OF FOUR AND IT HAS NO MASK PUNCTION. FOR A DETAILED FUNCTIONAL DESCRIPTION, SEE THE FAIRCHILD DATA BOOK.



INPUT LOADING:

(LOADING IN TRANSISTOR PAIRS)

D0:3 - 4

AN0:15 - 2

WSN -

OUTPUT DRIVE:

(DRIVE IN ( #P, #N) )

MN0:15 = (1,0.25)

Q0:3 = (1,1)

### NDL SYNTAX:

 $\verb| z(MN0,MN1,MN2,MN3,MN4,MN5,MN6,MN7,MN8,MN9,MN10,MN11,MN12,MN13,MN14,MN15,M0,Q1,Q2,Q3) | \\$ 

-GFS2020A (D0,D1,D2,D3,AN0,AN1,AN2,AN3,AN4,AN5,AN6,AN7,AN8,AN9,AN10,AN11,AN12,AN13,AN14,AN15,WSN)\$

@ 1986, 1987 LSI Logic Corporation

1 OF 3

### FUNCTIONAL INFORMATION:

EACH WORD LOCATION OF THE GFS2020A MEGAPUNCTION HAS ITS OWN ACTIVE-LOW ADDRESS SELECT LINE. IN THE READ MODE, DATA FROM THE ADDRESSED LOCATION APPEARS AT THE DATA OUTPUT (Q0:3). IN THE WRITE MODE, WHEN THE WRITE STROBE (WSN) IS LOW, DATA IS STORED IN THE SELECTED LOCATION. MATCH COMPARISONS ARE ALWAYS MADE BETWEEN INPUT DATA AND ALL THE WORDS IN THE CAM. NO CONTROL SIGNAL IS REQUIRED FOR THE MATCH PUNCTION. IF A MATCH COMPARE RESULTS IN A MATCH, THE MATCH OUTPUT (MN0:15) GOES LOW.

© 1986, 1987 LSI Logic Corporation

2 OF 3

# AC CHARACTERISTICS:

# PROPAGATION DELAY\*:

OUTPUT	Q0:3 NS	MNO:15 NS
AN0:15	7.7	12.2
D0:3	13.3	8.3
WSN	15.6	12.8

<sup>\* 7</sup>K NOMINAL

### SET-UP AND HOLD TIMES\*:

FROM INPUT	SET-UP TIME NS	HOLD TIME NS
AN0:15	0	5.4
D0:3	1.3	1.8

<sup>\*</sup> MINIMUM WSN WIDTH: 5.0 NS