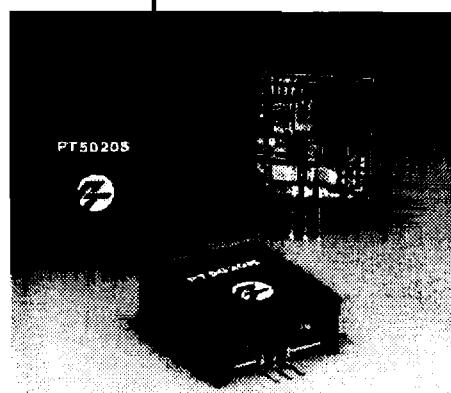


PT5020 Series

POSITIVE INPUT/NEGATIVE OUTPUT CONVERTER

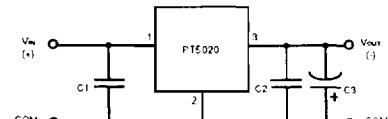
ADVANCED INFORMATION - REVISED 11/95



PT5020S



Standard Application



C₁ = Optional ceramic (1-5μF)
 C₂ = Optional ceramic (1-5μF)
 C₃ = Electrolytic (100μF)

- Input Voltage Range +4.75 to +7 Volts
- Self-Contained Inductor
- Internal Over-Temperature Protection
- Complete solution with only one external capacitor required.

The Power Trends' PT5020 ISRs convert a positive input voltage (typ +5V) to a negative output voltage for a wide range of analog and communication circuit applications.

The plus to minus ISRs use a "Buck-Boost" topology and are packaged in the 3 pin SIP configuration. Available output voltages are -3.3V, -5V, -5.2V, -9V, -12V and -15V

Pin-Out Information

Pin No.	Function
1	Input
2	Ground
3	Output



Ordering Information

- PT5021 □ = -3.3 Volts
 PT5022 □ = -5 Volts
 PT5023 □ = -9 Volts
 PT5024 □ = -12 Volts
 PT5025 □ = -15 Volts
 PT5026 □ = -5.2 Volts

(For dimensions, see page 62.)

PT Series Suffix (PT12345X)

Case/Pin Configuration	Heat Tab Configuration	Uncased	None	Top
Vertical Through-Hole	U	N	S	
Horizontal Through-Hole	—	A	H	
Horizontal Surface Mount	—	C	J	

(See Thermal Application Notes on page 44 for heat tab application data.)

Specifications

Note: Buck Boost topology ISRs are not short circuit protected.

PT5020 SERIES						
Characteristics (T _A =25°C unless noted)	Symbols	Conditions	Min	Typ	Max	
Output Current	I _o	Over V _{in} range V _o =-3.3V V _o =-5V V _o =-5.2V V _o =-9V V _o =-12V V _o =-15V	0.25** 0.25** 0.25** 0.10** 0.10** 0.10**	— — — — — —	1.0 1.0 1.0 0.60 0.50 0.30	Amps
Current Limit	I _d	V _{in} = 5V	—	1.5 I _o max	—	Amps
Inrush Current	I _g	V _{in} = +5V @ max I _o	—	3.0	—	Amps
	t _g	On start up	—	1.0	—	mSec
Input Voltage Range	V _{in}	I _o = 0.1 to I _o max	4.75	—	7*	VDC
Static Voltage Tolerance	V _o	Over V _{in} Range I _o = I _o max T _A = -20°C to shutdown	—	±1.5	±3	% V _o
Line Regulation	R _{reg} _{line}	Over V _{in} range	—	±0.5	±1	% V _o
Load Regulation	R _{reg} _{load}	I _{min} < I _o < I _{max}	—	±0.5	±1	% V _o
Ripple/Noise	V _r	V _{in} =5V, I _o =I _{max}	—	±2	±5	% V _o
Transient Response	t _{tr}	25% load change V _o over/undershoot	—	500 3.0	5.0	μSec % V _o
Efficiency	η	V _{in} =5V, I _o =0.5 I _{max}	—	75	—	%
Switching Frequency	f _o	Over V _{in} and I _o ranges	V _o =-3.3V V _o =-5V V _o =-5.2V V _o =-9V V _o =-12V V _o =-15V	0.8 0.8 0.8 500 500 0.8	1 1 1 650 650 1	MHz MHz MHz KHz KHz MHz
Operating Temperature	T _A	Free Air Convection, (40-60 LFM) Over V _{in} and I _o range	-20	—	+65	°C
Thermal Resistance	θ _{JA}	Free Air Convection (40-60 LFM)	—	TBD	—	°C/W
Storage Temperature	T _S	—	-40	—	+125	°C
Mechanical Shock	—	Per Mil-STD-883D, Method 2002.3 Condition A, 1 msec, Half Sine, mounted to a fixture	—	—	500	G's
Mechanical Vibration	—	Per Mil-STD-883D, Method 2007.2 Condition A, 20-2000 Hz	—	—	5	G's
Weight (without heat tab)	—	—	—	7.5	—	grams
Relative Humidity	—	Non-condensing	0	—	95	%

* For applications with input voltages greater than 7 VDC, use a 78SR Series in the buck boost configuration - see Application Note on page 51.

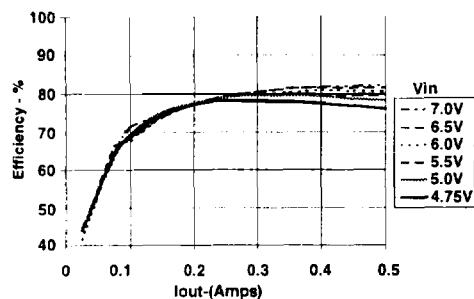
** ISR will operate down to no load with reduced specifications.

CHARACTERISTIC DATA

PT5022 (-5VDC)

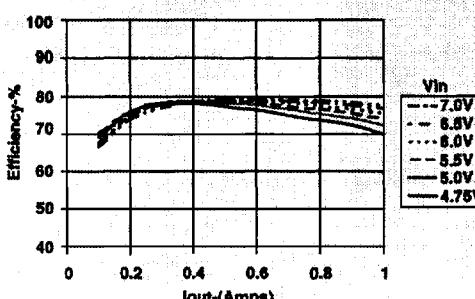
(See Note 1)

Efficiency vs Output Current

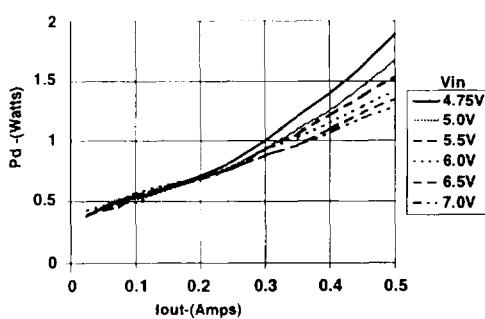
**PT5024 (-12VDC)**

(See Note 1)

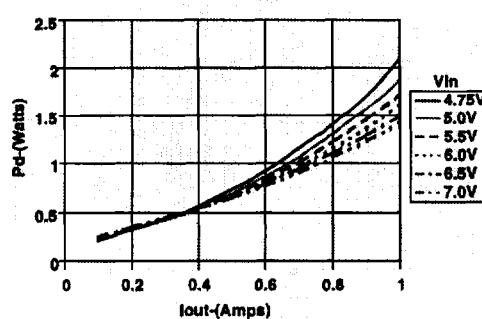
Efficiency vs Output Current



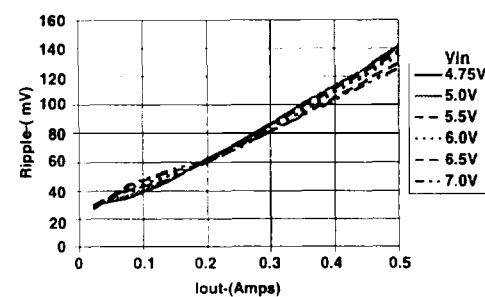
Power Dissipation vs Output Current



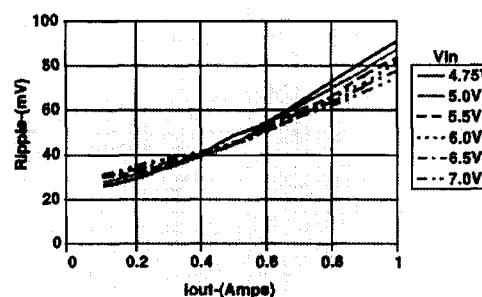
Power Dissipation vs Output Current



Ripple Voltage vs Output Current



Ripple Voltage vs Output Current



Note 1: All data listed in the above graphs has been developed from actual products tested at 25°C. This data is considered typical data for the ISR.