



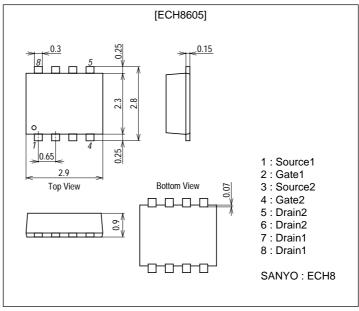
# **Ultrahigh-Speed Switching Applications**

### **Features**

- · Low ON-resistance.
- · Ultrahigh-speed switching.
- · 4V drive.

# **Package Dimensions**

unit : mm 2206A



# **Specifications**

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		-30	V
Gate-to-Source Voltage	VGSS		±20	V
Drain Current (DC)	ID		-4	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	-40	Α
Allowable Power Dissipation	PD	Mounted on a ceramic board (900mm²X0.8mm)1unit	1.3	W
Total Dissipation	PT	Mounted on a ceramic board (900mm²X0.8mm)	1.5	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

### Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=-1mA, VGS=0	-30			V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0			-1	μΑ
Gate-to-Source Leakage Current	IGSS	VGS=±16V, VDS=0			±10	μΑ
Cutoff Voltage	VGS(off)	V <sub>DS</sub> =-10V, I <sub>D</sub> =-1mA	-1.0		-2.4	V

Marking : JD Continued on next page.

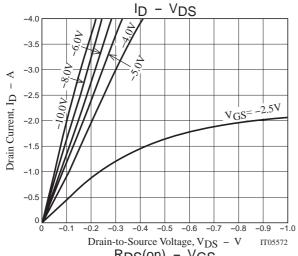
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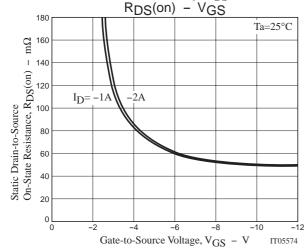
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Unit
Forward Transfer Admittance	yfs	V <sub>DS</sub> =-10V, I <sub>D</sub> =-2A	3.3	5		S
Static Drain-to-Source On-State Resistance	R <sub>DS</sub> (on)1	I <sub>D</sub> =-2A, V <sub>G</sub> S=-10V		50	67	mΩ
	RDS(on)2	ID=-1A, VGS=-4V		87	120	$m\Omega$
Input Capacitance	Ciss	V <sub>DS</sub> =-10V, f=1MHz		550		pF
Output Capacitance	Coss	V <sub>DS</sub> =-10V, f=1MHz		120		pF
Reverse Transfer Capacitance	Crss	V <sub>DS</sub> =-10V, f=1MHz		90		pF
Turn-ON Delay Time	t <sub>d</sub> (on)	See specified Test Circuit.		13		ns
Rise Time	t <sub>r</sub>	See specified Test Circuit.		110		ns
Turn-OFF Delay Time	t <sub>d</sub> (off)	See specified Test Circuit.		65		ns
Fall Time	tf	See specified Test Circuit.		75		ns
Total Gate Charge	Qg	V <sub>DS</sub> =-10V, V <sub>GS</sub> =-10V, I <sub>D</sub> =-4A		14		nC
Gate-to-Source Charge	Qgs	V <sub>DS</sub> =-10V, V <sub>GS</sub> =-10V, I <sub>D</sub> =-4A		2.2		nC
Gate-to-Drain "Miller" Charge	Qgd	V <sub>DS</sub> =-10V, V <sub>GS</sub> =-10V, I <sub>D</sub> =-4A		2.5		nC
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =-4A, V <sub>G</sub> S=0		-0.88	-1.2	V

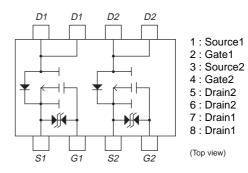
### **Switching Time Test Circuit**

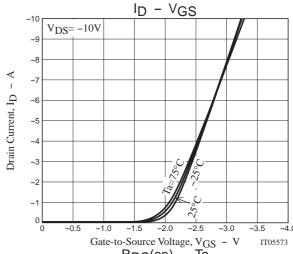
# VIN VDD= -15V OV 1 -10V VIN ID= -2A RL=7.5Ω PW=10μs D.C.≤1% PG SOΩ ECH8605

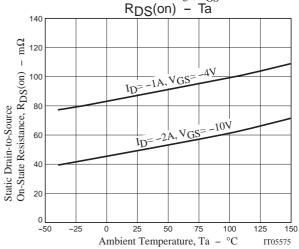




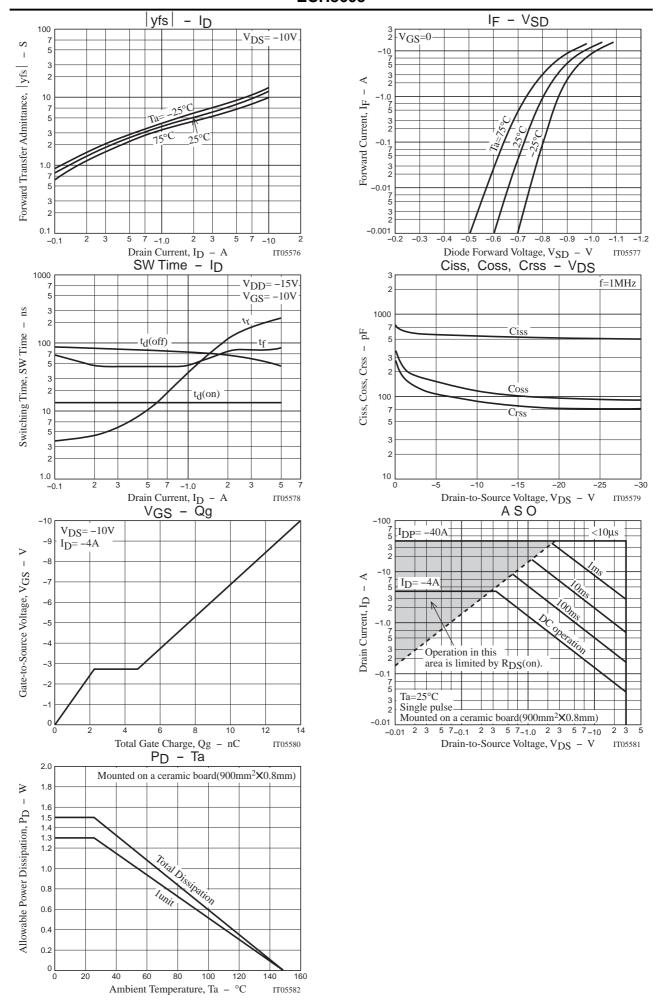
## **Electrical Connection**







### **ECH8605**



### **ECH8605**

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