

IGBT

FGL60N100D

General Description

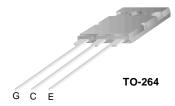
Insulated Gate Bipolar Transistors (IGBTs) with trench gate structure have superior performance in conduction and switching to planar gate structure, and also have wide noise immunity. These devices are well suitable for IH applications

Features

- High Speed Switching
- Low Saturation Voltage : V_{CE(sat)} = 2.5V @ I_C = 60A
- High Input Impedance
- Built-in Fast Recovery Diode

Application

Home Appliance, Induction Heater, IH JAR, Micro Wave Oven





Absolute Maximum Ratings $T_C = 25^{\circ}C$ unless otherwise noted

Symbol	Description		FGL60N100D	Units	
V _{CES}	Collector-Emitter Voltage		1000	V	
V _{GES}	Gate-Emitter Voltage		± 25	V	
	Collector Current	@ T _C = 25°C	60	А	
I _C	Collector Current	@ T _C = 100°C	42	А	
I _{CM (1)}	Pulsed Collector Current		120	Α	
l _F	Diode Continuous Forward Current	@ T _C = 100°C	15	Α	
P _D	Maximum Power Dissipation	@ T _C = 25°C	176	W	
	Maximum Power Dissipation	@ T _C = 100°C	70	W	
T _J	Operating Junction Temperature		-55 to +150	°C	
T _{stg}	Storage Temperature Range		-55 to +150	°C	
T _L	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 second	ds	300	°C	

Notes :

(1) Repetitive rating : Pulse width limited by max. junction temperature

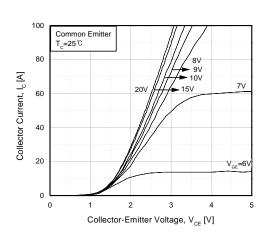
Thermal Characteristics

Symbol	Parameter	Тур.	Max.	Units
R _{θJC} (IGBT)	Thermal Resistance, Junction-to-Case		0.71	°C/W
$R_{\theta JC}(DIODE)$	Thermal Resistance, Junction-to-Case		2.08	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient		25	°C/W

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
Off Cha	racteristics					
I _{CES}	Collector Cut-Off Current	$V_{CE} = 1000V, V_{GE} = 0V$			1.0	mA
I _{GES}	G-E Leakage Current	$V_{GE} = \pm 25, V_{CE} = 0V$			± 500	nA
On Chai	racteristics					
V _{GE(th)}	G-E Threshold Voltage	$I_C = 60 \text{mA}, V_{CE} = V_{GE}$	4.0	5.0	7.0	V
	Collector to Emitter	$I_C = 10A$, $V_{GE} = 15V$		1.6	2.0	V
V _{CE(sat)}	Saturation Voltage	$I_C = 60A$, $V_{GE} = 15V$		2.5	2.9	V
C _{ies}	C Characteristics Input Capacitance	$V_{CE}=10V_{V_{GE}}=0V_{V_{CE}}$		6300		pF
C _{ies}	Input Capacitance	V -10V V - 0V		6300		pF
C _{oes}	Output Capacitance	f = 1MHz		160		pF
C _{res}	Reverse Transfer Capacitance			140		pF
Switchir	ng Characteristics					
t _{d(on)}	Turn-On Delay Time	$V_{CC} = 600V, I_{C} = 60A,$		160	400	ns
t _r	Rise Time	$V_{CC} = 600 \text{ V}, I_{C} = 600 \text{ A},$ $R_{G} = 51 \Omega, V_{GE} = 15 \text{ V},$		360	700	ns
t _{d(off)}	Turn-Off Delay Time	Resistive Load, $T_C = 25^{\circ}C$		410	700	ns
t _f Q _g	Fall Time			240	330	ns
Qg	Total Gate Charge	V - 600 V I - 60 A		230	300	nC
Q _{ge}	Gate-Emitter Charge	$V_{CE} = 600 \text{ V}, I_{C} = 60 \text{A},$		45		nC
∨ ge	Cate Ellitter Charge	$$ $V_{GF} = 15V$				

Electrical Characteristics of DIODE $T_C = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
V _{FM}	Diode Forward Voltage	I _F = 15A		1.2	1.7	V
		I _F = 60A		1.8	2.1	V
t _{rr}	Diode Reverse Recovery Time	$I_F = 60A di/dt = -20A/us$		1.2	1.5	us
lr	Instantaneous Reverse Current	VRRM = 1000V		0.05	2	uA



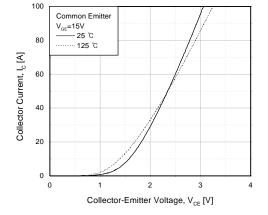
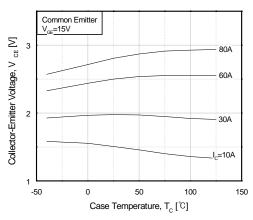


Fig 1. Typical Output Characteristics

Fig 2. Typical Saturation Voltage Characteristics



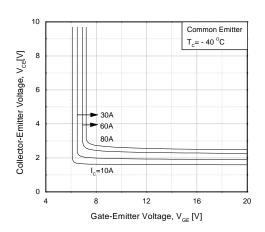
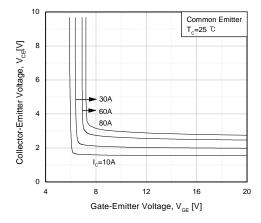


Fig 3. Saturation Voltage vs. Case
Temperature at Varient Current Level

Fig 4. Saturation Voltage vs. V_{GE}



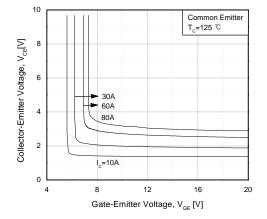


Fig 5. Saturation Voltage vs. V_{GE}

Fig 6. Saturation Voltage vs. V_{GE}

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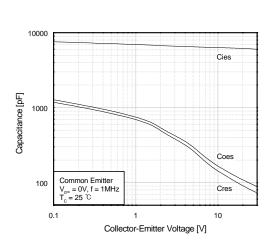


Fig 7. Capacitance Characteristics

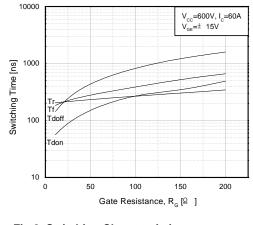


Fig 8. Switching Characteristics vs.

Gate Resistance

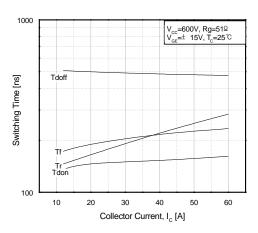


Fig 9. Switching Characteristics vs. Collector Current

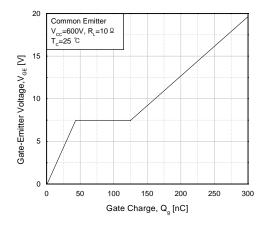


Fig 10. Gate Charge Characteristics

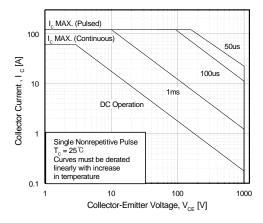


Fig 11. SOA Characteristics

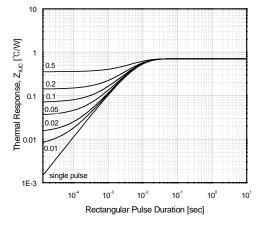


Fig 12. Transient Thermal Impedance of IGBT

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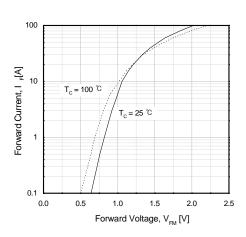


Fig 13. Forward Characteristics

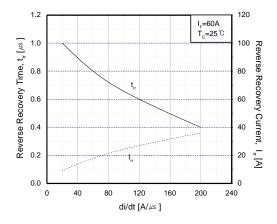


Fig 14. Reverse Recovery Characteristics vs. di/dt

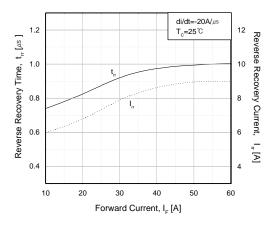


Fig 15. Reverse Recovery Characteristics vs. Forward Current

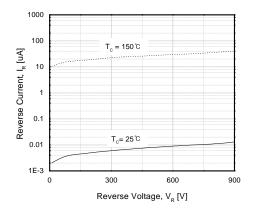


Fig 16. Reverse Current vs. Reverse Voltage

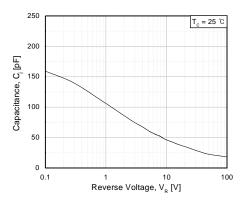


Fig 17. Junction capacitance

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