Silicon Epitaxial Planar Diode for Various Detector, Modulator, Demodulator

HITACHI

ADE-208-1503 (Z)

Rev.0 Feb. 2002

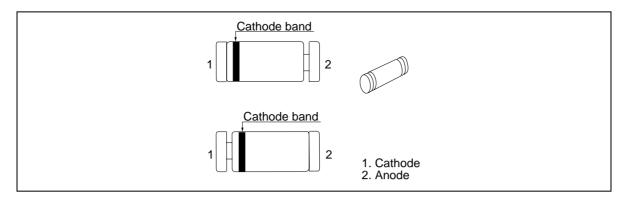
Features

- Low capacitance. (C = 4.0 pF max)
- Short reverse recovery time. $(t_{rr} = 4.0 \text{ ns max})$
- LLD package is suitable for high density surface mounting and high speed assembly.

Ordering Information

Type No.	Cathode band	Package Code
HSK4148	Light Blue	LLD

Pin Arrangement





Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit
Peak reverse voltage	V _{RM}	100	V
Reverse voltage	V _R	75	V
Peak forward current	I FM	450	mA
Non-Repetitive peak forward surge current	I *	1	А
Average forward current	I _o	150	mA
Power dissipation	Pd	500	mW
Junction temperature	Tj	200	°C
Storage temperature	Tstg	-65 to +200	C°

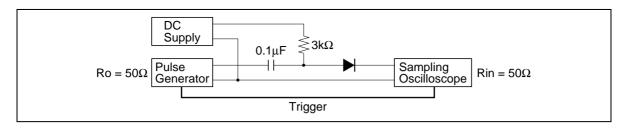
Note: Within 1s forward surge current.

Electrical Characteristics

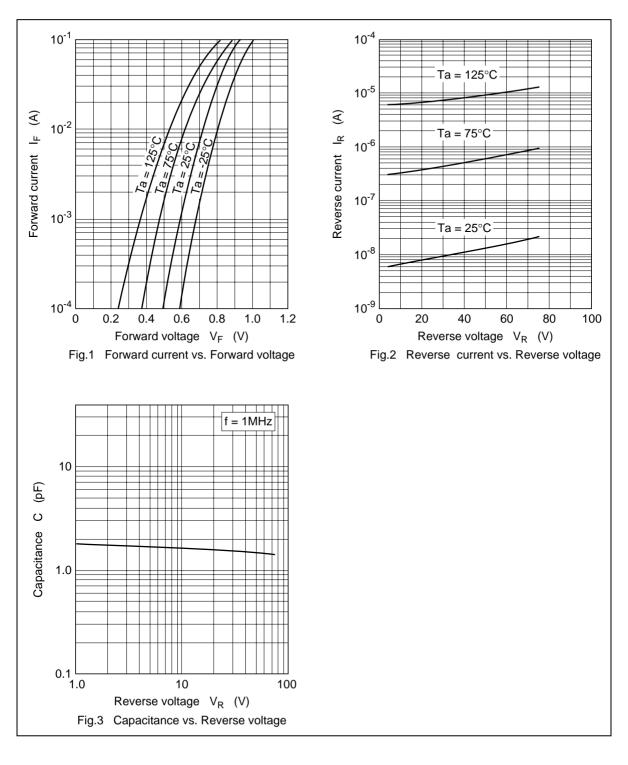
 $(Ta = 25^{\circ}C)$

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Forward voltage	V _F	_	_	1.0	V	$I_{\rm F} = 10 {\rm mA}$
Reverse current	I _R	_		25	nA	V _R = 20 V
Capacitance	С	_	_	4.0	pF	$V_{R} = 0 V, f = 1 MHz$
Reverse recovery time	t _{rr} *	_	_	4.0	ns	$I_{_{ m F}}$ = 10 mA, $V_{_{ m R}}$ = 6 V, Irr = 1 mA, R_{_{ m L}} = 100 Ω

Note: Reverse recovery time test circuit

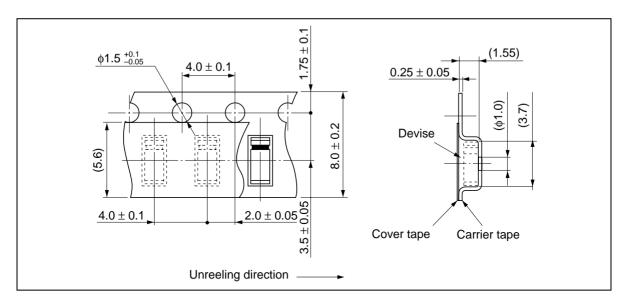


Main Characteristic

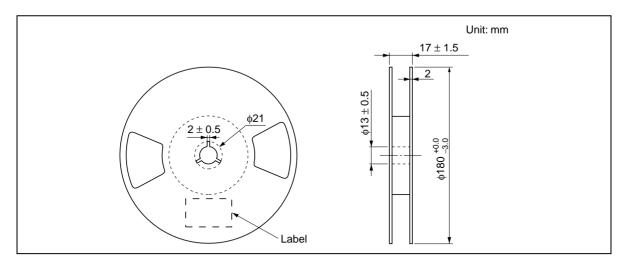


LLD Taping (TR TYPE)

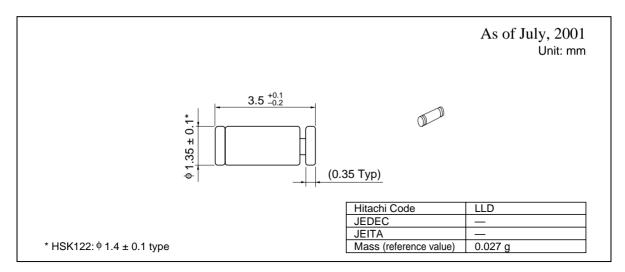
Unit: mm, (): reference only



Taping reel dimension



Package Dimensions



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