

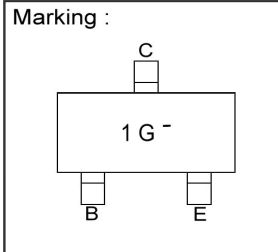
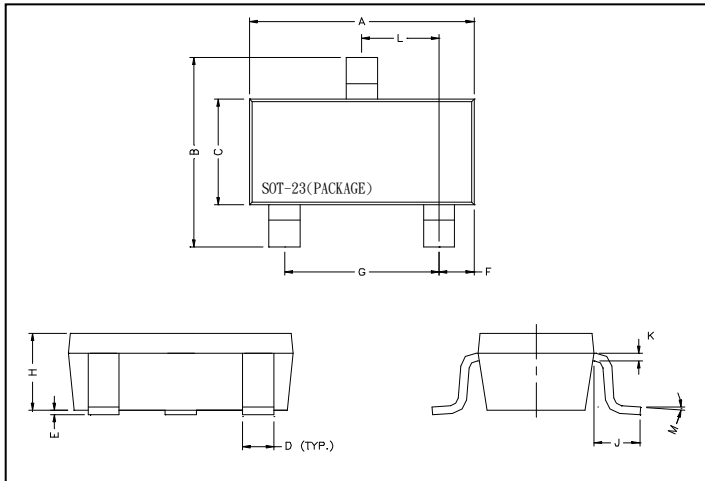
GMBTA06

NPN SILICON TRANSISTOR

Description

The GMBTA06 is designed for general purpose amplifier applications.

Package Dimensions



REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	2.70	3.10	G	1.90	REF.
B	2.40	2.80	H	1.00	1.30
C	1.40	1.60	K	0.10	0.20
D	0.35	0.50	J	0.40	-
E	0	0.10	L	0.85	1.15
F	0.45	0.55	M	0°	10°

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Ratings	Unit
Junction Temperature	T _j	+150	°C
Storage Temperature	T _{stg}	-55~+150	°C
Collector to Base Voltage	V _{CB0}	80	V
Collector to Emitter Voltage	V _{CEO}	80	V
Emitter to Base Voltage	V _{EBO}	4	V
Collector Current	I _c	500	mA
Total Power Dissipation(Note1)	P _D	350	mW

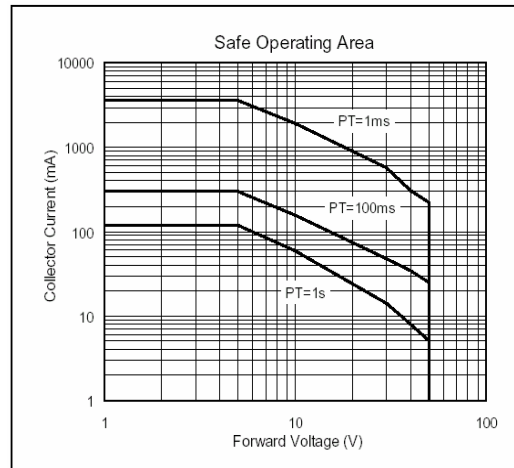
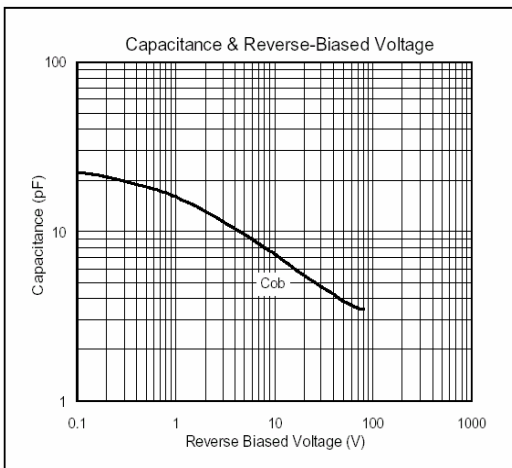
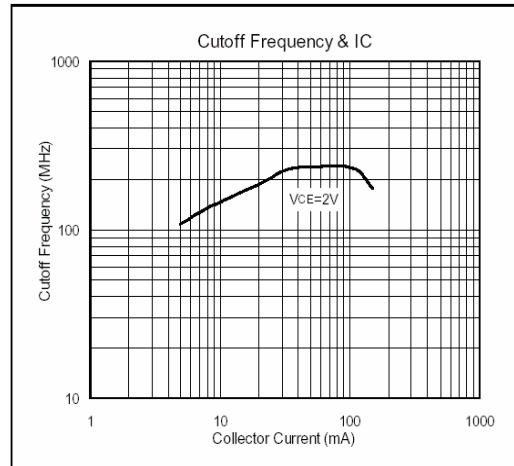
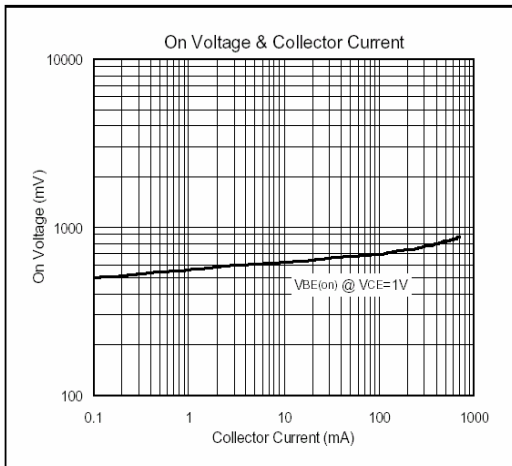
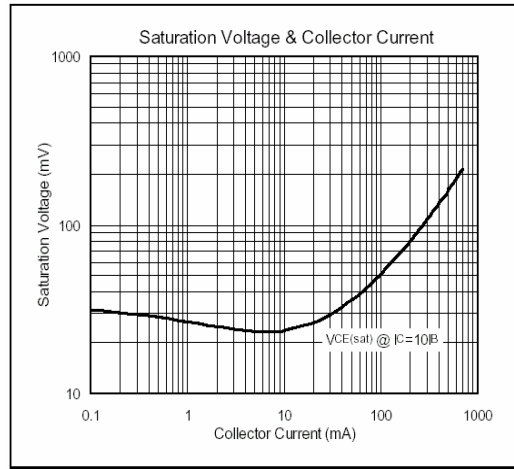
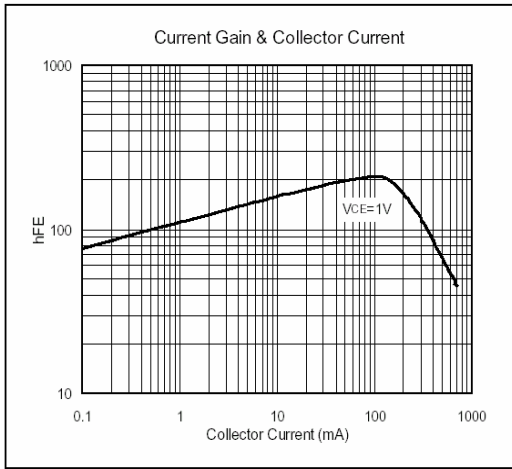
Note 1. Device mounted on FR-4=1.6*1.6*0.06in

Electrical Characteristics (Ta = 25°C, unless otherwise noted)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
V _{CB0}	80	-	-	V	I _c =100uA, I _E =0
V _{CEO}	80	-	-	V	I _c =1mA, I _B =0
V _{EBO}	4	-	-	V	I _E =100uA, I _c =0
I _{CB0}	-	-	100	nA	V _{CB} =80V, I _E =0
I _{CEO}	-	-	100	nA	V _{CE} =80V, I _B =0
*V _{CE(sat)}	-	-	250	mV	I _c =100mA, I _B =10mA
*V _{BE(on)}	-	-	1.2	V	V _{CE} =1V, I _c =100mA
*h _{FE1}	50	-	-		V _{CE} =1V, I _c =10mA
*h _{FE2}	50	-	-		V _{CE} =1V, I _c =100mA
f _T	100	-	-	MHz	V _{CE} =2V, I _c =10mA, f=100MHz

* Pulse Test: Pulse Width ≤ 380μs, Duty Cycle ≤ 2%

Characteristics Curve



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