Analog Power AM7202N

## N-Channel 200-V (D-S) MOSFET

### **Key Features:**

- Low r<sub>DS(on)</sub> trench technology
- · Low thermal impedance
- · Fast switching speed

### **Typical Applications:**

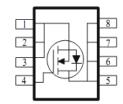
- · White LED boost converters
- PoE PD Power supplies
- Industrial DC/DC Conversion Circuits

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$V_{DS}(V) r_{DS(on)}(m\Omega)$				
200	87 @ V <sub>GS</sub> = 10V	6.2			
	102 @ V <sub>GS</sub> = 6.5V	5.8			

DFN5X6-8L







ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25°C UNLESS OTHERWISE NOTED)							
Parameter				Limit	Units		
Drain-Source Voltage			V <sub>DS</sub>	200	V		
Gate-Source Voltage				±20	V		
Continuous Drain Current <sup>a</sup>		T <sub>A</sub> =25°C	1	6.2	А		
Continuous Drain Current		T <sub>A</sub> =70°C	I <sub>D</sub>	5			
Pulsed Drain Current <sup>b</sup>			I <sub>DM</sub>	30			
Continuous Source Current (Diode Conduction) a	I <sub>S</sub>	7.1	Α				
Dower Dissinction a		T <sub>A</sub> =25°C	P <sub>D</sub>	5	W		
Power Dissipation <sup>a</sup>	T,		ı D	3.2	V V		
Operating Junction and Storage Temperature Range				-55 to 150	°C		

THERMAL RESISTANCE RATINGS							
Parameter	Symbol	Maximum	Units				
Maximum Junction-to-Ambient <sup>a</sup>	t <= 10 sec	$R_{\theta JA}$	25	°C/W			
Maximum Junction-to-Ambient	Steady State		65	C/VV			

1

#### Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

**Analog Power** AM7202N

#### **Electrical Characteristics**

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit		
Static								
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \text{ uA}$	1			V		
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±100	nA		
Zara Cata Valtaga Drain Current		$V_{DS} = 160 \text{ V}, V_{GS} = 0 \text{ V}$			1 uA			
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 160 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			25	uA		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	10			Α		
Drain Cauras On Basistanas a	r	$V_{GS} = 10 \text{ V}, I_D = 5.2 \text{ A}$			87	mΩ		
Drain-Source On-Resistance <sup>a</sup>	r <sub>DS(on)</sub>	$V_{GS} = 6 \text{ V}, I_D = 4.2 \text{ A}$			102	11122		
Forward Transconductance a	g <sub>fs</sub>	$V_{DS} = 15 \text{ V}, I_{D} = 5.2 \text{ A}$		30		S		
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	$I_S = 3.6 \text{ A}, V_{GS} = 0 \text{ V}$		0.75		V		
		Dynamic <sup>b</sup>						
Total Gate Charge	$Q_g$	$V_{DS} = 100 \text{ V}, V_{GS} = 6.5 \text{ V},$		60				
Gate-Source Charge	$Q_{gs}$	$I_{D} = 5.2 \text{ A}$		20		nC		
Gate-Drain Charge	$Q_gd$	ID = 0.2 A		34				
Turn-On Delay Time	t <sub>d(on)</sub>	$V_{DS} = 100 \text{ V}, R_{L} = 19.3 \Omega,$		26				
Rise Time	t <sub>r</sub>	$V_{DS} = 100 \text{ V}, \text{ K}_{L} - 19.3 \Omega,$ $I_{D} = 5.2 \text{ A},$		28		no		
Turn-Off Delay Time	$t_{d(off)}$	$V_{GEN} = 10 \text{ V}, R_{GEN} = 6 \Omega$		98		ns		
Fall Time	t <sub>f</sub>	VGEN = 10 V, NGEN = 0 12		26				
Input Capacitance	C <sub>iss</sub>			4463				
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		222		pF		
Reverse Transfer Capacitance	$C_{rss}$			212				

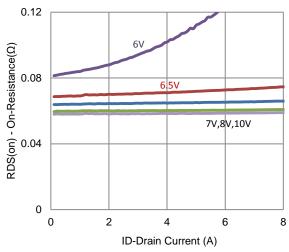
#### Notes

- Pulse test: PW <= 300us duty cycle <= 2%.
- Guaranteed by design, not subject to production testing. b.

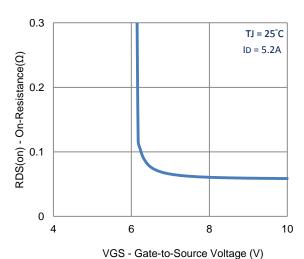
Analog Power (APL) reserves the right to make changes without further notice to any products herein. APL makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does APL assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in APL data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. APL does not convey any license under its patent rights nor the rights of others. APL products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the APL product could create a situation where personal injury or death may occur. Should Buyer purchase or use APL products for any such unintended or unauthorized application, Buyer shall indemnify and hold APL and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that APL was negligent regarding the design or manufacture of the part. APL is an Equal Opportunity/Affirmative Action Employer.

Analog Power AM7202N

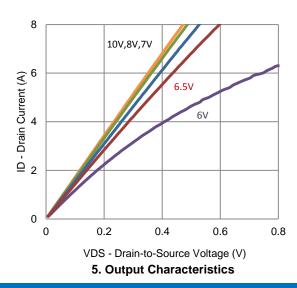
### **Typical Electrical Characteristics**

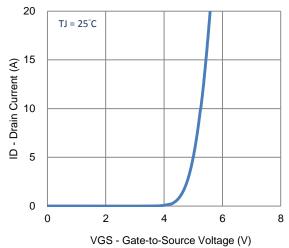


#### 1. On-Resistance vs. Drain Current

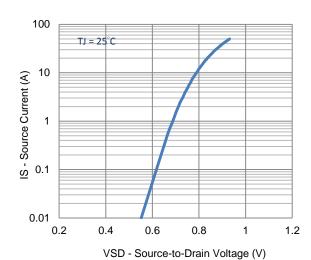


3. On-Resistance vs. Gate-to-Source Voltage

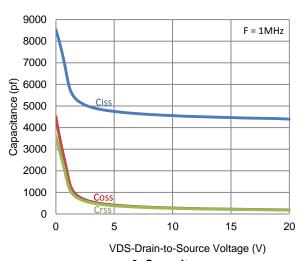




2. Transfer Characteristics

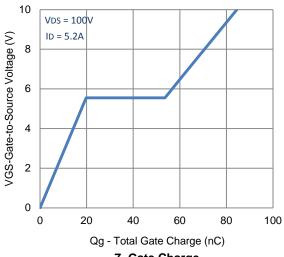


4. Drain-to-Source Forward Voltage

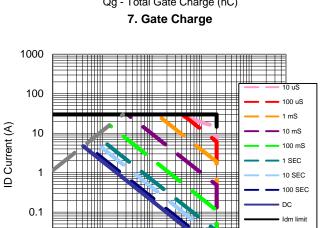


**Analog Power** AM7202N

### **Typical Electrical Characteristics**



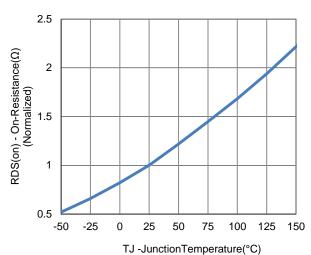
1000



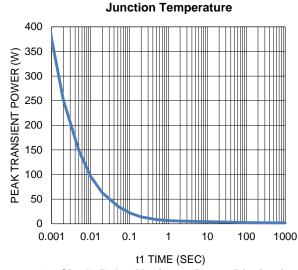
VDS Drain to Source Voltage (V) 9. Safe Operating Area

100

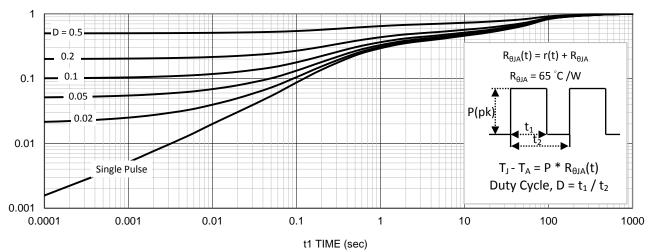
10



8. Normalized On-Resistance Vs



10. Single Pulse Maximum Power Dissipation



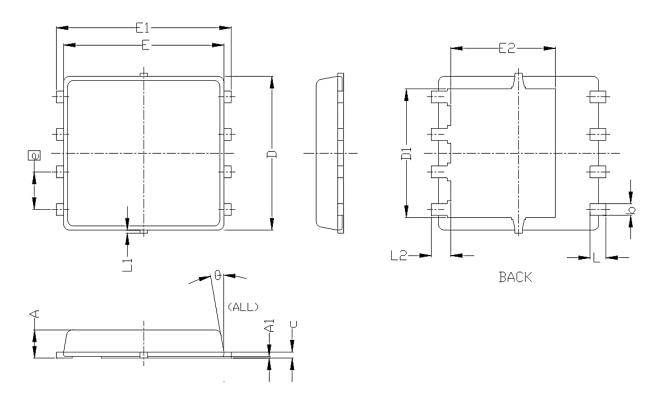
11. Normalized Thermal Transient Junction to Ambient

0.01

0.1

Analog Power AM7202N

# Package Information



SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES			
STMBULS	MIN	NOM	MAX	MIN	NOM	MAX	
A	0.85	0. 95	1.00	0.033	0.037	0.039	
Al	0.00		0.05	0.000		0.002	
b	0.30	0.40	0.50	0.012	0.016	0.020	
с	0. 15	0. 20	0. 25	0.006	0.008	0.010	
D	5. 20 BSC			0. 205 BSC			
D1	4. 35 BSC			0. 171 BSC			
E	5, 55 BSC			0. 219 BSC			
El	6. 05 BSC			0. 238 BSC			
E2	3. 62 BSC			0. 143 BSC			
e	1. 27 BSC			0. 050 BSC			
L	0.45	0.55	0.65	0.018	0.022	0.026	
L1	0		0.15	0		0.006	
L2	0.68 REF			0.027 REF			
θ	0°		10°	0°		10°	