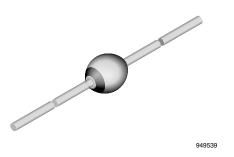
Vishay Semiconductors



Standard Avalanche Sinterglass Diode



MECHANICAL DATA

Case: SOD-57

Terminals: plated axial leads, solderable per MIL-STD-750, method 2026

Polarity: color band denotes cathode end

Mounting position: any

Weight: approx. 369 mg

FEATURES

- · Controlled avalanche characteristics
- · Glass passivated junction
- Hermetically sealed package
- · Low reverse current
- · High surge current loading
- HALOGEN • Compliant to RoHS directive 2002/95/EC and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition

APPLICATIONS

• Rectification, general purpose

PARTS TABLE				
PART	TYPE DIFFERENTIATION	PACKAGE		
BYW52	V _R = 200 V; I _{FAV} = 2 A	SOD-57		
BYW53	V _R = 400 V; I _{FAV} = 2 A	SOD-57		
BYW54	V _R = 600 V; I _{FAV} = 2 A	SOD-57		
BYW55	V _R = 800 V; I _{FAV} = 2 A	SOD-57		
BYW56	V _R = 1000 V; I _{FAV} = 2 A	SOD-57		

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT	
Reverse voltage = repetitive peak reverse voltage	See electrical characteristics	BYW52	$V_R = V_{RRM}$	200	V	
		BYW53	$V_{R} = V_{RRM}$	400	V	
		BYW54	$V_{R} = V_{RRM}$	600	V	
		BYW55	$V_R = V_{RRM}$	800	V	
		BYW56	$V_{R} = V_{RRM}$	1000	V	
Peak forward surge current	t _p = 10 ms, half sine wave		I _{FSM}	50	А	
Repetitive peak forward current			I _{FRM}	12	А	
Average forward current	φ = 180 °		I _{FAV}	2	А	
Pulse avalanche peak power	t_p = 20 µs half sine wave, T_j = 175 °C		P _R	1000	W	
Pulse energy in avalanche mode, non repetitive (inductive load switch off)	l _{(BR)R} = 1 Α, Τ _j = 175 °C		E _R	20	mJ	
i ² t-rating			i²t	8	A ² s	
Junction and storage temperature range			$T_j = T_{stg}$	- 55 to + 175	°C	

MAXIMUM THERMAL RESISTANCE (T _{amb} = 25 °C, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL VALUE		UNIT
Junction ambient	Lead length I = 10 mm, T_L = constant	R _{thJA}	45	K/W
	On PC board with spacing 25 mm	R _{thJA}	100	K/W

For technical questions within your region, please contact one of the following: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com

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COMPLIANT

FREE



BYW52, BYW53, BYW54, BYW55, BYW56

Standard Avalanche Sinterglass Diode **Vishay Semiconductors**

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX	UNIT
Forward voltage	I _F = 1 A	V _F	-	0.9	1	V
Reverse current	$V_{R} = V_{RRM}$	I _R	-	0.1	1	μA
	$V_R = V_{RRM}, T_j = 100 \ ^\circ C$	I _R	-	5	10	μA
Breakdown voltage	$I_{R} = 100 \ \mu\text{A}, \ t_{p}/T = 0.01, \ t_{p} = 0.3 \ \text{ms}$	V _(BR)	-	-	1600	V
Diode capacitance	V _R = 4 V, f = 1 MHz	CD	-	18	-	pF
Reverse recovery time	I _F = 0.5 A, I _R = 1 A, i _R = 0.25 A	t _{rr}	-	-	4	μs
	$I_F = 1 \text{ A}, \text{ dI/dt} = 5 \text{ A/}\mu\text{s}, \text{ V}_R = 50 \text{ V}$	t _{rr}	-	-	4	μs
Reverse recovery charge	I _F = 1 A, dl/dt = 5 A/μs	Q _{rr}	-	-	200	nC

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

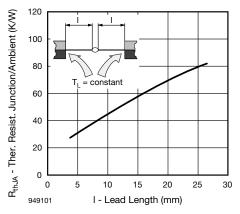


Fig. 1 - Typ. Thermal Resistance vs. Lead Length

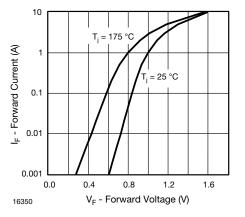
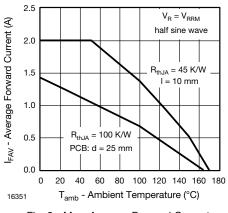


Fig. 2 - Forward Current vs. Forward Voltage





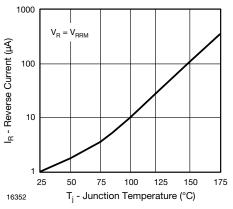


Fig. 4 - Reverse Current vs. Junction Temperature

BYW52, BYW53, BYW54, BYW55, BYW56

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Standard Avalanche Sinterglass Diode



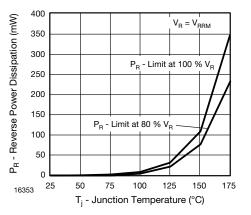


Fig. 5 - Max. Reverse Power Dissipation vs. Junction Temperature

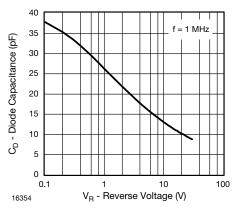
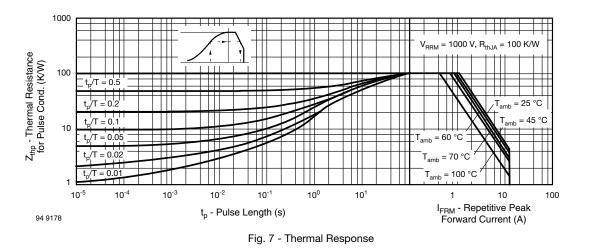
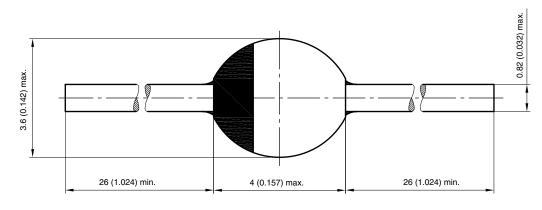


Fig. 6 - Diode Capacitance vs. Reverse Voltage



PACKAGE DIMENSIONS in millimeters (inches): SOD-57



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