

P-Channel 20 V (D-S) MOSFET

| PRODUCT SUMMARY | | | | | | |
|---------------------|--------------------------------------|---------------------------------|-----------------------|--|--|--|
| V _{DS} (V) | $R_{DS(on)}\left(\Omega\right)$ Max. | I _D (A) ^a | Q _g (Typ.) | | | |
| | 0.065 at V _{GS} = - 4.5 V | - 4.5 | | | | |
| - 20 | 0.090 at V _{GS} = - 2.5 V | - 3.7 | 13.8 nC | | | |
| | 0.1175 at V _{GS} = - 1.8 V | - 3.3 | | | | |

FEATURES

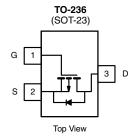
- TrenchFET[®] Power MOSFET
- 100 % R_g Tested

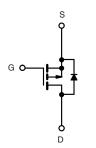


ROHS COMPLIANT HALOGEN FREE

APPLICATIONS

- Power Management for Portable and Consumer
 - Load Switches
 - DC/DC Converters





P-Channel MOSFET

| ABSOLUTE MAXIMUM RATING | $S (T_A = 25 ^{\circ}C, unle$ | ess otherwise no | ted) | | |
|--|---|------------------|--------------------------------|------|--|
| Parameter | | Symbol | Limit | Unit | |
| Drain-Source Voltage | V _{DS} | - 20 | | | |
| Gate-Source Voltage | | V_{GS} | ± 12 | V | |
| | T _C = 25 °C | | - 4.5 | | |
| Continuous Drain Current ($T_J = 150$ °C) | $T_C = 70 ^{\circ}C$ $T_A = 25 ^{\circ}C$ | I _D | - 3.7 - 3.5 ^{b, c} | | |
| | T _A = 70 °C | | - 2.6 ^{b, c} | Α | |
| Pulsed Drain Current (t = 300 μs) | I _{DM} | - 20 | | | |
| Continuous Source-Drain Diode Current | $T_C = 25 ^{\circ}C$ $T_A = 25 ^{\circ}C$ | I _S | - 1.4 - 1 ^{b, c} | | |
| | $T_C = 25 ^{\circ}C$ $T_C = 70 ^{\circ}C$ | _ | 1.7 1.1 | | |
| Maximum Power Dissipation | $T_A = 25 ^{\circ}\text{C}$ $T_A = 70 ^{\circ}\text{C}$ | P _D | 1 ^{b, c} | W | |
| Operating Junction and Storage Temperature R | T _J , T _{stg} | - 55 to 150 | °C | | |
| Soldering Recommendations (Peak Temperatur | | 260 | | | |

| THERMAL RESISTANCE RATINGS | | | | | | |
|---|--------------|------------|---------|---------|-------|--|
| Parameter | | Symbol | Typical | Maximum | Unit | |
| Maximum Junction-to-Ambient ^{b, d} | t ≤ 5 s | R_{thJA} | 100 | 130 | °C/W | |
| Maximum Junction-to-Foot (Drain) | Steady State | R_{thJF} | 60 | 75 | 5/ ** | |

Notes:

- a. $T_C = 25$ °C.
- b. Surface mounted on 1" x 1" FR4 board.
- c. t = 5 s.
- d. Maximum under steady state conditions is 175 °C/W.



| Parameter | Symbol | Test Conditions | Min. | Тур. | Max. | Unit | |
|--|-------------------------|---|-------|--------|--------|-------|--|
| Static | | | | | | | |
| Drain-Source Breakdown Voltage | V_{DS} | $V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$ | - 20 | | | V | |
| V _{DS} Temperature Coefficient | $\Delta V_{DS}/T_{J}$ | I _D = - 250 μA | | - 14 | | mV/°C | |
| V _{GS(th)} Temperature Coefficient | $\Delta V_{GS(th)}/T_J$ | 10 = - 230 μΛ | | 2.5 | | | |
| Gate-Source Threshold Voltage | V _{GS(th)} | $V_{DS} = V_{GS}, I_{D} = -250 \mu A$ | - 0.4 | | - 1 | V | |
| Gate-Source Leakage | I _{GSS} | $V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$ | | | ± 10 | | |
| Gale-Source Leakage | | $V_{DS} = 0 \text{ V}, V_{GS} = \pm 4.5 \text{ V}$ | | | ± 1 | | |
| Zero Gate Voltage Drain Current | I _{DSS} | V _{DS} = - 20 V, V _{GS} = 0 V | | | - 1 | μΑ | |
| Zero Gate Voltage Drain Current | | $V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 \text{ °C}$ | | | - 10 | | |
| On-State Drain Current ^a | I _{D(on)} | $V_{DS} \le -5 \text{ V}, V_{GS} = -4.5 \text{ V}$ | - 15 | | | Α | |
| | | V _{GS} = - 4.5 V, I _D = - 4 A | | 0.0465 | 0.0650 | | |
| Drain-Source On-State Resistance ^a | R _{DS(on)} | $V_{GS} = -2.5 \text{ V}, I_D = -4 \text{ A}$ | | 0.0740 | 0.0900 | Ω | |
| | | V _{GS} = - 1.8 V, I _D = - 2 A | | 0.1135 | 0.1175 | | |
| Dynamic ^b | | | | | | | |
| Total Gata Chargo | Qq | V _{DS} = - 10 V, V _{GS} = - 12V, b = - 4.5 A | | 23.8 | 36 | nC | |
| Total Gate Charge | Ų _g | | | 13.8 | 21 | | |
| Gate-Source Charge | Q_{gs} | $V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -4.5 \text{ A}$ | | 1.9 | | | |
| Gate-Drain Charge | Q_{gd} | | | 3 | | | |
| Gate Resistance | R_{g} | f = 1 MHz | 2.2 | 11 | 22 | Ω | |
| Turn-On Delay Time | t _{d(on)} | | | 22 | 33 | - ns | |
| Rise Time | t _r | V_{DD} = - 10 V, R_L = 2.8 Ω | | 21 | 32 | | |
| Turn-Off Delay Time | t _{d(off)} | $I_D \cong -3.6 \text{ A}, V_{GEN} = -4.5 \text{ V}, R_g = 1 \Omega$ | | 62 | 93 | | |
| Fall Time | t _f | | | 14 | 21 | | |
| Turn-On Delay Time | t _{d(on)} | | | 9 | 18 | 115 | |
| Rise Time | t _r | V_{DD} = - 10 V, R_L = 2.8 Ω | | 6 | 12 | | |
| Turn-Off Delay Time | t _{d(off)} | $I_D \cong$ - 3.6 A, V_{GEN} = - 8 V, R_g = 1 Ω | | 65 | 98 | | |
| Fall Time | t _f | | | 15 | 23 | | |
| Drain-Source Body Diode Characterist | ics | | | | | | |
| Continuous Source-Drain Diode Current | I _S | T _C = 25 °C | | | - 1.4 | Α | |
| Pulse Diode Forward Current | I _{SM} | | | | - 20 | | |
| Body Diode Voltage | V _{SD} | $I_S = -3.6 \text{ A}, V_{GS} = 0 \text{ V}$ | | - 0.8 | - 1.2 | V | |
| Body Diode Reverse Recovery Time t _{rr} | | | | 13 | 20 | ns | |
| Body Diode Reverse Recovery Charge | Q _{rr} | I _F = - 3.6 A, dl/dt = 100 A/μs, T _{.J} = 25 °C | | 5 | 10 | nC | |
| Reverse Recovery Fall Time | t _a | 3.3 / η, απαί = 100 / νμο, 1 ₃ = 20 0 | | 8 | | ns | |
| Reverse Recovery Rise Time | t _b | [| | 5 | | | |

Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

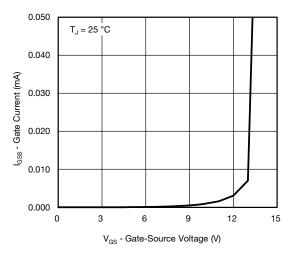
a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$

b. Guaranteed by design, not subject to production testing.

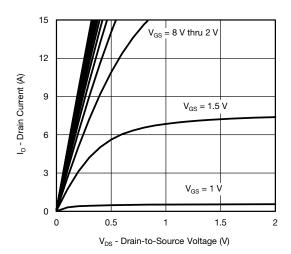




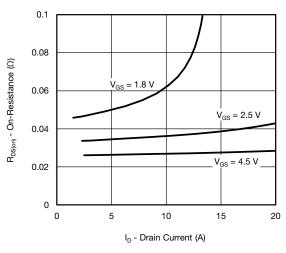
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



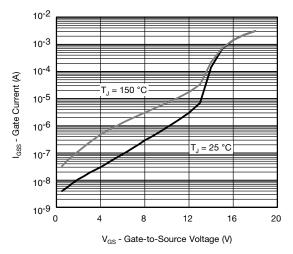
Gate Current vs. Gate-Source Voltage



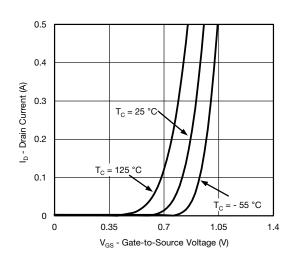
Output Characteristics



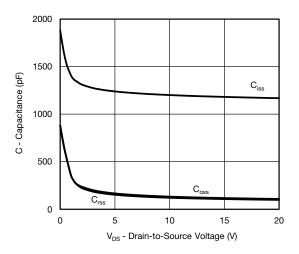
On-Resistance vs. Drain Current



Gate Current vs. Gate-Source Voltage



Transfer Characteristics

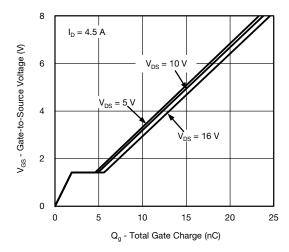


Capacitance

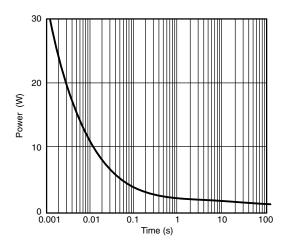




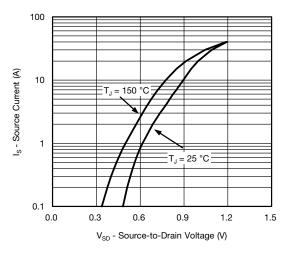
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



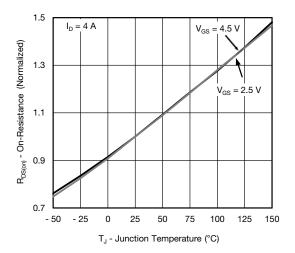
Gate Charge



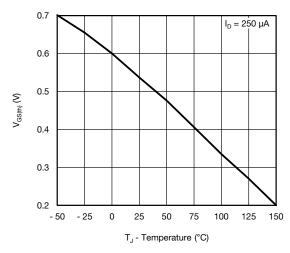
Single Pulse Power, Junction-to-Ambient



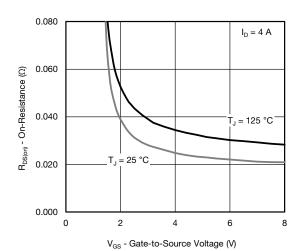
Soure-Drain Diode Forward Voltage



On-Resistance vs. Junction Temperature

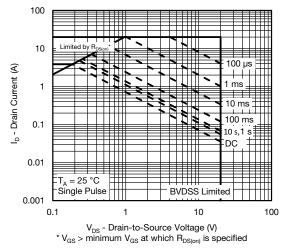


Threshold Voltage

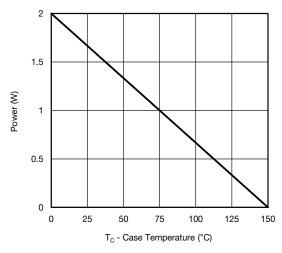


On-Resistance vs. Gate-to-Source Voltage

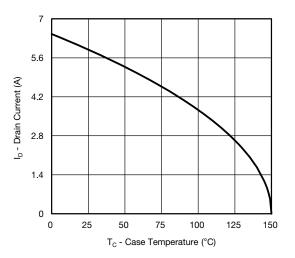
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



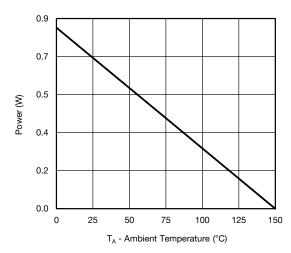
Safe Operating Area, Junction-to-Ambient



Power Junction-to-Case



Current Derating*

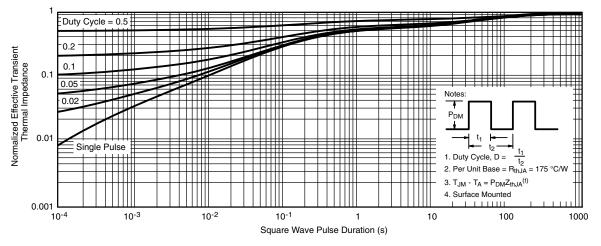


Power Junction-to-Ambient

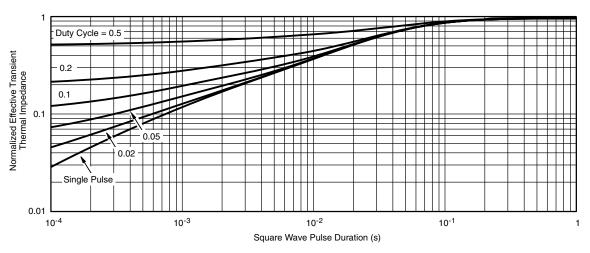
^{*} The power dissipation P_D is based on $T_{J(max.)}$ = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



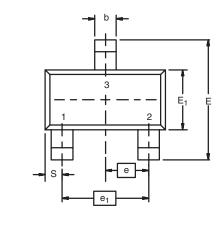
Normalized Thermal Transient Impedance, Junction-to-Ambient

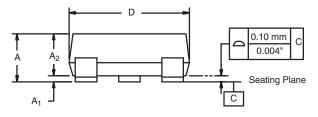


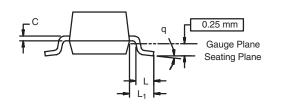
Normalized Thermal Transient Impedance, Junction-to-Foot



SOT-23 (TO-236): 3-LEAD





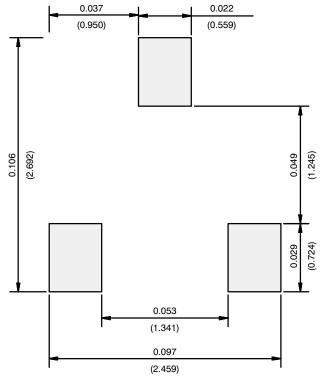


| Dim | MILLIMETERS | | INCHES | | | |
|--------------------------|-------------|----------|-----------|------------|--|--|
| | Min | Max | Min | Max | | |
| Α | 0.89 | 1.12 | 0.035 | 0.044 | | |
| A ₁ | 0.01 | 0.10 | 0.0004 | 0.004 | | |
| A ₂ | 0.88 | 1.02 | 0.0346 | 0.040 | | |
| b | 0.35 | 0.50 | 0.014 | 0.020 | | |
| С | 0.085 | 0.18 | 0.003 | 0.007 | | |
| D | 2.80 | 3.04 | 0.110 | 0.120 | | |
| E | 2.10 | 2.64 | 0.083 | 0.104 | | |
| E ₁ | 1.20 | 1.40 | 0.047 | 0.055 | | |
| е | 0.95 | 0.95 BSC | | 0.0374 Ref | | |
| e ₁ | 1.90 BSC | | 0.074 | 8 Ref | | |
| L | 0.40 | 0.60 | 0.016 | 0.024 | | |
| L ₁ | 0.64 Ref | | 0.025 Ref | | | |
| S | 0.50 Ref | | 0.020 Ref | | | |
| q | 3° | 8° | 3° | 8° | | |
| ECN: S-03946-Rev. K. 09- | Jul-01 | • | | | | |

DWG: 5479



RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads Dimensions in Inches/(mm)

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