



SANYO Semiconductors

DATA SHEET

2SK4192LS — N-Channel Silicon MOSFET

General-Purpose Switching Device Applications

Features

- Low ON-resistance, low input capacitance, ultrahigh-speed switching.
- Adoption of high reliability HVP process.
- Attachment workability is good by Mica-less package.
- Avalanche resistance guarantee.

Specifications

Absolute Maximum Ratings at $T_a=25^\circ\text{C}$

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V_{DSS}		400	V
Gate-to-Source Voltage	V_{GSS}		± 30	V
Drain Current (DC)	I_{DC}^{*1}	Limited only by maximum temperature $T_{ch}=150^\circ\text{C}$	7	A
	I_{Dpack}^{*2}	$T_c=25^\circ\text{C}$ (SANYO's ideal heat dissipation condition ^{*3})	6.1	A
Drain Current (Pulse)	I_{DP}	$PW \leq 10\mu\text{s}$, duty cycle $\leq 1\%$	23	A
Allowable Power Dissipation	P_D		2.0	W
		$T_c=25^\circ\text{C}$ (SANYO's ideal heat dissipation condition ^{*3})	30	W
Channel Temperature	T_{ch}		150	$^\circ\text{C}$
Storage Temperature	T_{stg}		-55 to +150	$^\circ\text{C}$
Avalanche Energy (Single Pulse) ^{*4}	E_{AS}		161	mJ
Avalanche Current ^{*5}	I_{AV}		7	A

Note : ^{*1} Shows chip capability

^{*2} Package limited

^{*3} SANYO's condition is radiation from backside.

The method is applying silicone grease to the backside of the device and attaching the device to water-cooled radiator made of aluminium.

^{*4} $V_{DD}=99\text{V}$, $L=5\text{mH}$, $I_{AV}=7\text{A}$

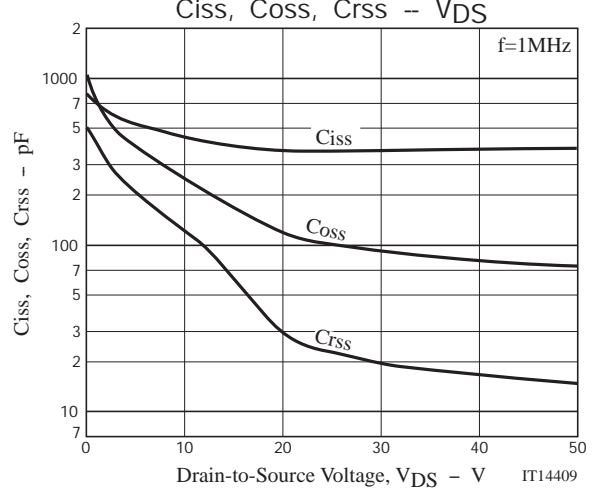
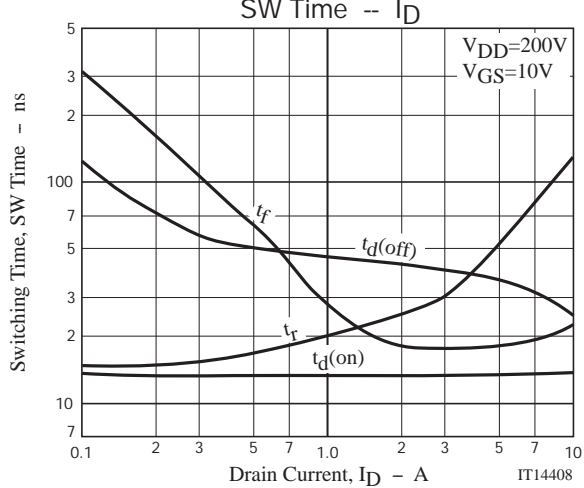
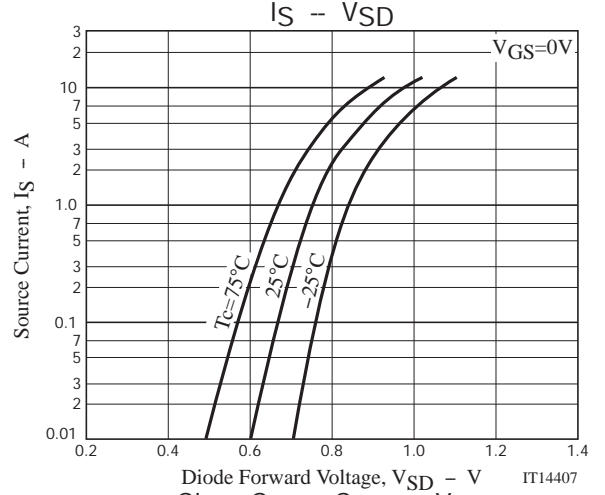
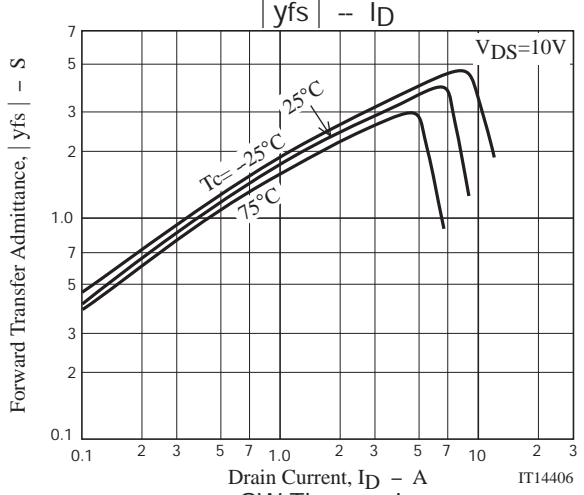
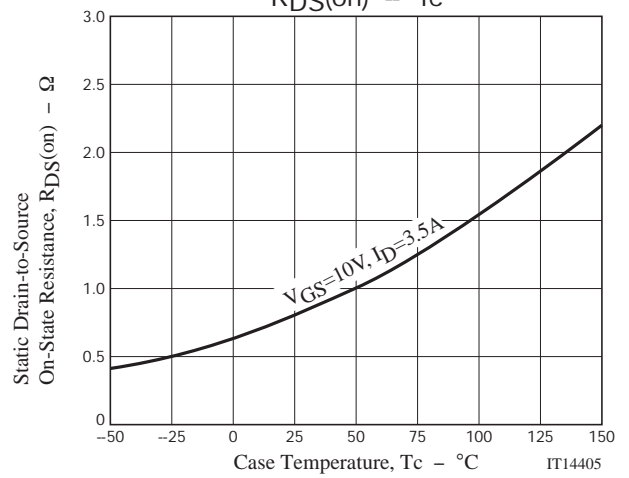
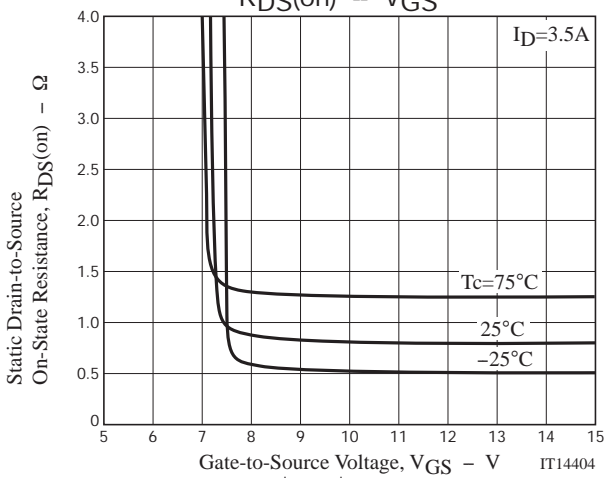
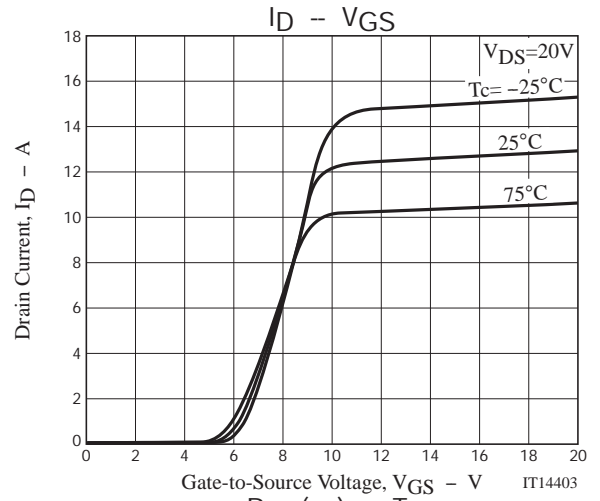
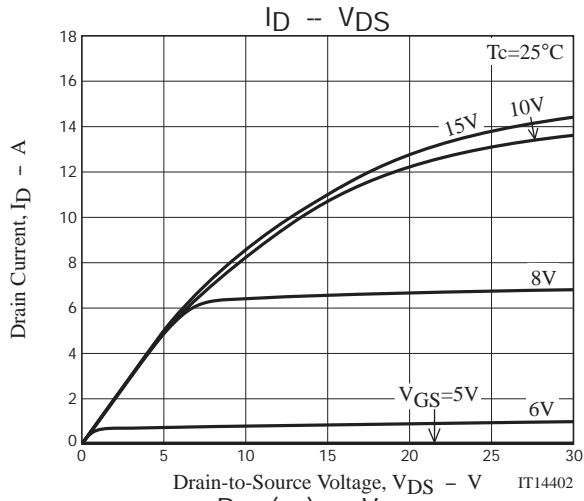
^{*5} $L \leq 5\text{mH}$, single pulse

Marking : K4192

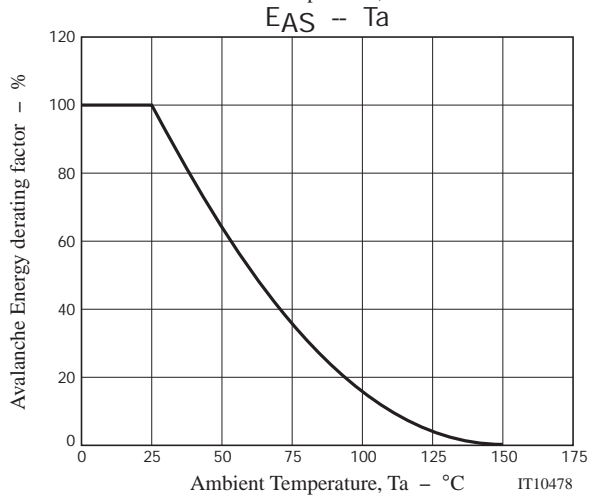
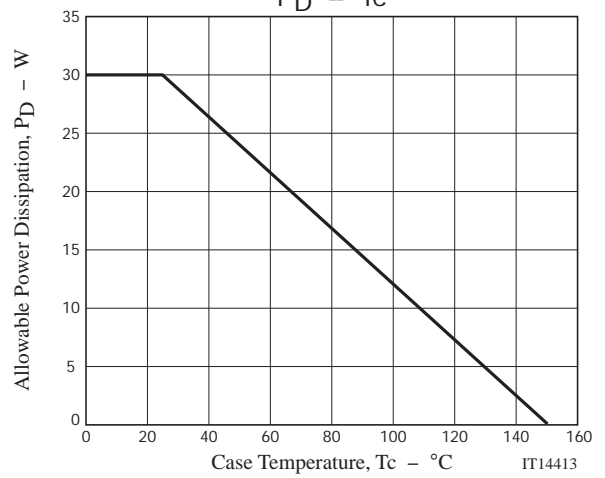
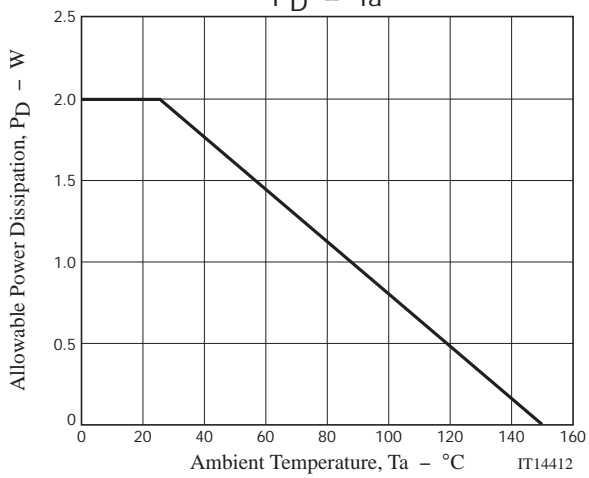
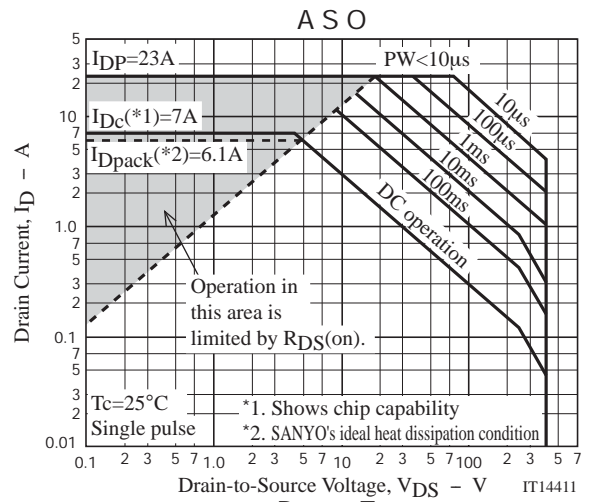
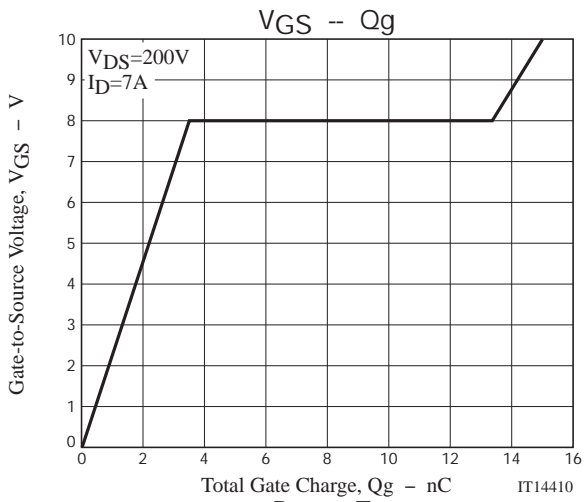
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2SK4192LS



2SK4192LS



Note on usage : Since the 2SK4192LS is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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