

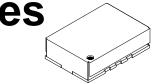


19013 36th Ave. West • Suite H • Lynnwood, WA 98036, USA

PE1145T LV PECL Series

- 6 Pad Leadless Surface Mount Oscillator
- Differential LV PECL Output
- Enable/ Disable Function
- Alternate Pinouts Compatible with All Major Suppliers Available

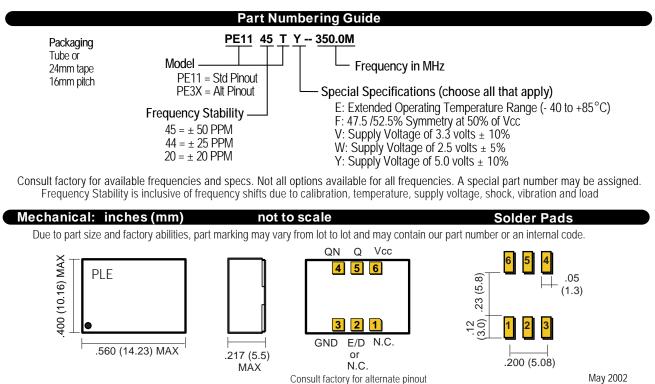
Standard Specifications



170.00 MHz – **650.00 MHz** Consult factory for higher frequencies

Overall Frequency Stability Operating Temperature Range Supply Voltage (Vcc) Supply Current (Icc) Jitter Output Load Enable/Disable Option (E/D)	PE1145T: ± 50 PPM, PE1144T: ± 25 PPM, PE1120T: ± 20 PPM over Operating Temp. Range 0 to +80°C is standard, but can be extended to – 40 to +85°C for certain frequencies 3.3 volts ± 10% standard, but 5.0 volts or 2.5 volts also available 115 mA typical, 130 mA maximum Consult factory Output must be terminated into 50 ohms to (Vcc - 2.0 V). See Test Circuit 5 and Note 1. Output enabled when Pin #2 is open or at CMOS Logic "1"; Output disabled when Pin #2 is at CMOS Logic "0".	
Output Waveform PECL with Differential Output (see Waveform 2)	Symmetry Tr & Tf Logic "1" Logic "0"	45/55% to 55/45% at 50% of Vcc level standard, tighter symmetry available 300 pS max (20 to 80%) Vcc - 1.025 volts minimum Vcc - 1.620 volts maximum

Note 1: In the typical PECL 100K logic output Voh is 2.35 volts and Vol is 1.60 volts at 3.3 Vcc. The center voltage of the PECL is therefore 1.975 volts. If a 50 Ω resistor is placed between the output and Vcc – 2 volts (1.3 volts), the current through the resistor is (1.975 – 1.3) / 50 = 13.5 mA. The same load can be simulated by a resistor of 147 ± 1% ohms to ground (1.975 / 0.0135 = 146.29 ohms). If additional load current is placed on the output, its load current must be subtracted from the 13.5 mA to calculate a new load resistor. Using similar calculations, use 274 ± 1% ohms to ground for 5.0V operation.



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