

HID & SYSTEM MANAGEMENT PRODUCTS, H/PC IC FAMILY
DESCRIPTION

The SerialCoder™ UR5HC703 is an extremely low-power, “off-the-shelf” serial keyboard encoder. Robust, tiny and flexible, the IC is a good match for any application where the use of complicated keyboard protocols are not required and where asynchronous serial interface hardware is available.

The SerialCoder™ provides unprecedented power consumption minimization, making it ideal for use in add-on keyboards for handheld and cellular / web phones and single-board systems designed for portable applications. The IC provides extremely low-power operation, transparent to the host. Power consumption is reduced to just the circuit's leakage when all keys are released. The average current consumption is less than 1 μA at room temperature and 10 μA at 85°C.

If a single key or groups of keys are stuck in the depressed position, the IC will enter a total shutdown after a 10-minute wait — to protect against unintended and unnecessary discharge of the batteries in the host.

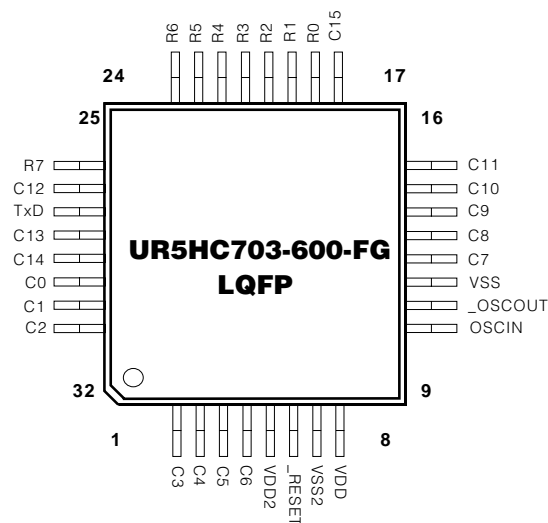
Custom versions of the SerialCoder™ are available with hardware handshaking for the communication channel, support for extra-low-power operations of the host, various baud rates and full-duplex I/O, etc. The SerialCoder™ is simple to implement. It requires few external components and utilizes a tiny, low-profile 32-pin LQFP package that measures 7mm x 7mm.

FEATURES

- 8 x 16 matrix encoding
- 9600 Baud 8N1 serial data format
- IC is independent of the keyboard layout
- Extremely low-power operation, transparent to the host
- Average current consumption of less than 1 μA at room temperature; 10 μA at 85°C
- Customized versions available
- TTL/CMOS output-only asynchronous serial interface to the host (direct connection to the UART)
- Very simple serial protocol — two-byte identification string on power-up; single-byte matrix-position for each key-press or key-release
- Robust algorithm for ghost-key elimination

APPLICATIONS

- Built-in keyboards for Palm and H/PC devices
- Add-on accessory keyboards for Palm and H/PC devices
- Portable personal computers
- Instrumentation
- Remote control
- Cellular phones

PIN ASSIGNMENTS


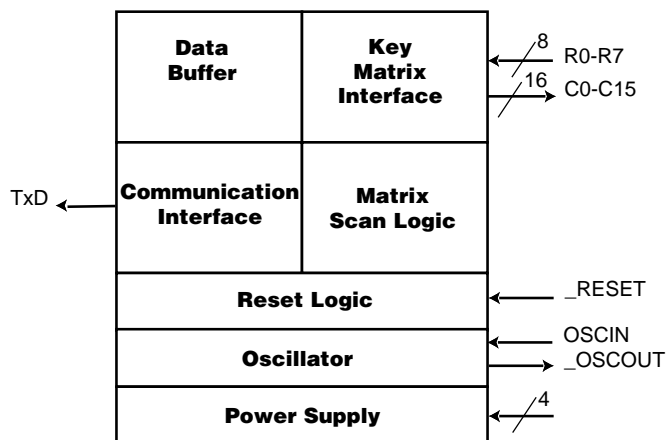


ORDERING CODE

Package Options	Pitch in mm	Ta = -20° C to +85° C
32-pin, Plastic LQFP	0.8	UR5HC703-600-FG

Other Materials	Type	Order number
SerialCoder™ Eval. Kit	Evaluation Kit	EVK5-703-600-XXX

SERIALCODER™ FUNCTIONAL DIAGRAM



PIN DEFINITIONS

Mnemonic	Pin #	Type	Name and Function
Power Supply			
Vdd, Vdd2	8,5	PWR	Positive Supply Voltage
Vss, Vss2	11,7	PWR	Negative Power Supply: Signal Ground
Reset			
_RESET	6	I	Hardware reset pin: Reset Input for orderly start-up. Low logic level is required until power supply reaches minimum operating voltage
Oscillator pins			
OSCI	9	I	Oscillator input: Connect ceramic resonator with built-in load capacitors or CMOS clock from external oscillator 2 MHz operating frequency
_OSCO ceramic	10	O	Oscillator Output: Connect resonator with built-in load capacitors or keep open if external oscillator is used
Host Interface			
TxD	27	0	Serial Data Output: Idle at high voltage (logical 1), non-inverted data (direct connection to the UART)
Scanned matrix pins			
ROW0-ROW7	18-25	I, pup	Row matrix inputs: With pulsed pull-up current sources
COL0-COL15	1-4, 12-17 26,28-32	O, nD	Column matrix output



PROTOCOL

Serial transmissions are at the rate of 9600 Baud, 8 data bits, no parity, least significant bit is transmitted first, idle/stop level is high (logical 1), start bit level is low (logical 0), non-inverted data.

Within 5ms after the power-up the IC sends a two-byte identification string of 0xF9, 0xFB. These two bytes are transmitted only once after each reset of the IC.

These two ID values also represent key release action in the locations [Row=1, Column=15] and [Row=3, Column=15] on the key matrix. Since the values of these bytes represent release action of the keys, extraneous characters will never be generated, even if synchronization between the driver and IC is lost, or power fluctuations/erroneous resets are applied to the chip. If logistics of the driver do not permit "shared" use of the identification values 0xF9 and 0xFB, then the user is advised not to incorporate keys in the locations [Row=1, Column=15] and [Row=3, Column=15] for the key matrix design.

Subsequent single-byte transmissions indicate the row (0-7), column (0-15), and press/release action for each change of the state of every key. If after the current key release all of the keys on the key matrix are in the released state, the last transmission is repeated. Host's driver looking for two bytes with exactly the same value and key release indication (b7=1) can recognize that the keyboard is completely idle.

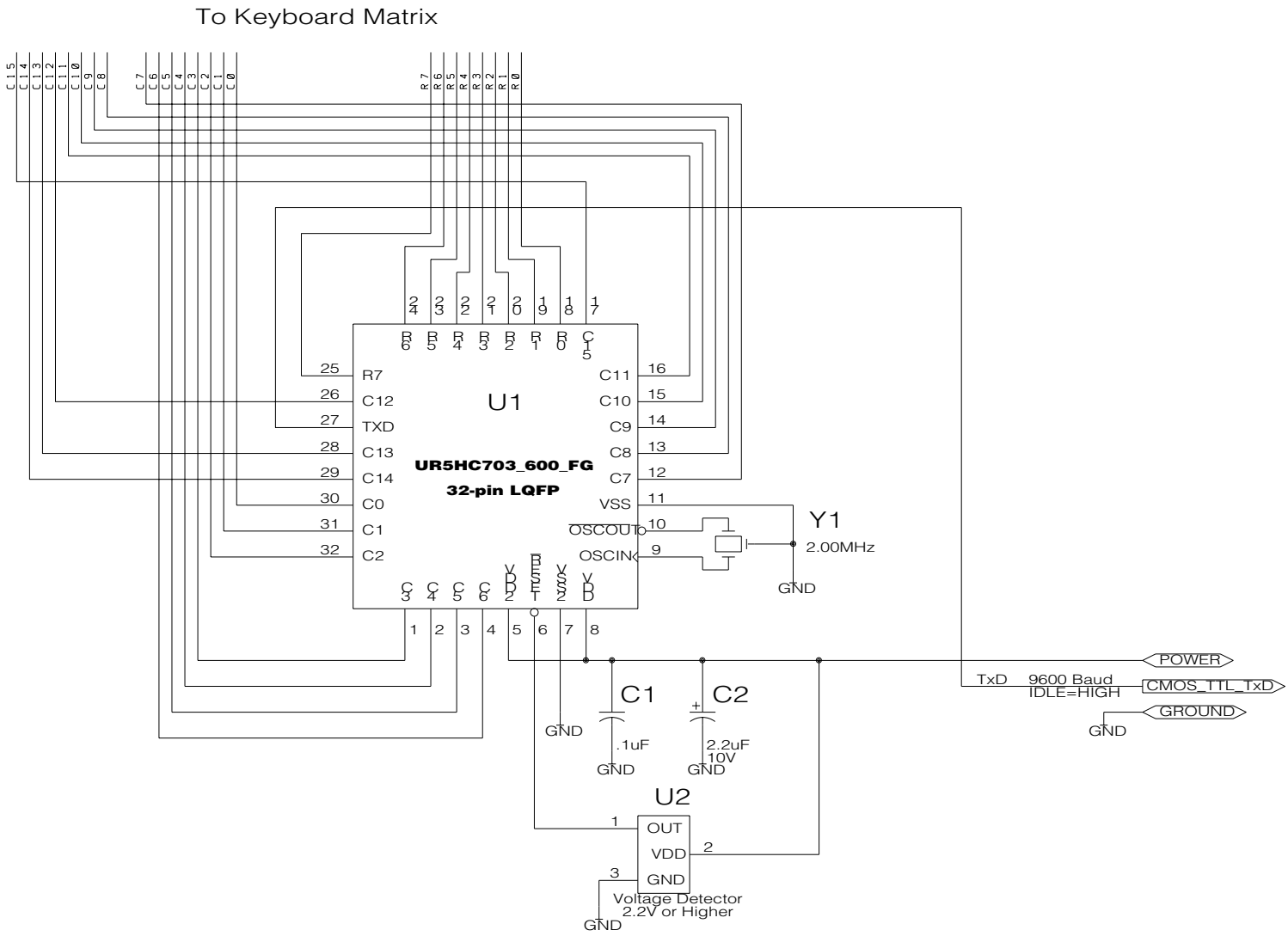
Byte Transmission

Bit Number	Comment
b7	0 for key press, 1 for key release
b6	Column location in the key matrix (0-15)
b5	
b4	
b3	
b2	Row location in the key matrix (0-7)
b1	
b0	



SEMTECH

SAMPLE SCHEMATIC FOR THE UR5HC703-600-FG



To Keyboard Matrix



SERIALCODER™ ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings

Ratings Unit	Symbol	Value	Unit
(VSS = 0V, Ambient Temperature TA is in the range TLOW to THIGH)			
Supply Voltage	VDD	-0.3 to +7.0	V
Input voltage: All input pins	VIN	-0.3 to VDD +0.3	V
Output current: Total peak for all pins	ΣIOH (Peak) ΣIOL (Peak)	-80 80	mA
Total average for all pins	ΣIOH (Avg) ΣIOL (Avg)	-40 40	mA
Peak for each pin	IOH (Peak) IOL (Peak)	-10 10	mA
Average for each pin	IOH (Avg) IOL (Avg)	-5 5	mA
Temperature range			
Operating Temperature	TLOW to THIGH	-20 to 85	°C
Storage Temperature	TSTG	-40 to 125	°C

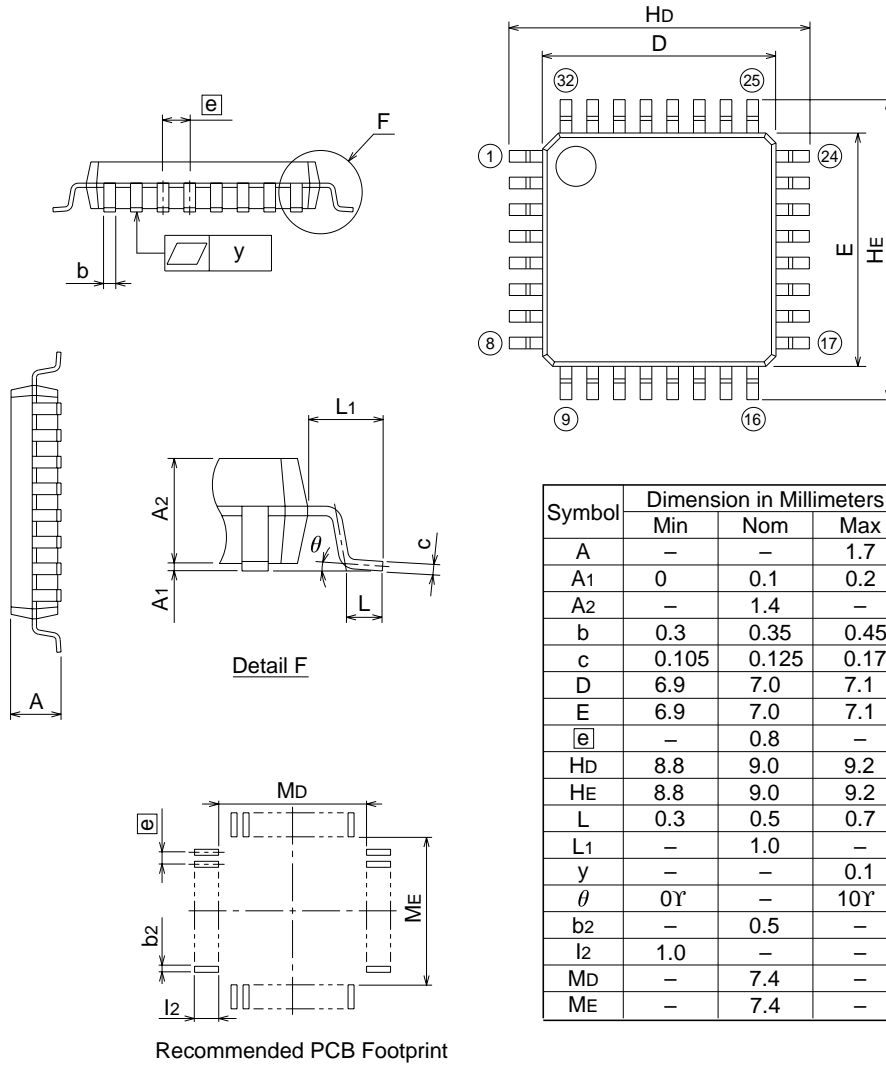
DC Electrical Characteristics, Temperature range=T low to T high unless otherwise noted)

Characteristic Unit	Symbol	Min	Typ	Max	Unit
Operating Voltage	VDD	2.2		5.5	V
Input Voltage					
High	VIH	.8 VDD		VDD	V
Low	VIL	0		0.3 VDD	V
	VIL (RESET)	0		0.2 VDD	V
	VIL (OSCIN)	0		0.16 VDD	V
Input Current High					
	IiH			5.0	μA
	IiH (OSCIN)		4.0		μA
Input Current Low					
	IiL	-5.0			μA
	IiL (OSCIN)		-4.0		μA
Output Voltage					
	VOH IOH=-1.0mA	VDD-1.0		VDD	V
	VOL IOL=1.0mA			1.0	V
Power Consumption					
	IDD ONE OR MORE KEYS ARE DEPRESSED			600	μA
	IDD ALL KEYS ARE RELEASED TA=25°C		.1	1.0	μA
	TA=85°C			10.0	μA

Control Timing (Vdd=2.2 to 5.5 V, Vss=0 Vdc, Temperature range=T low to T high unless otherwise noted)

Characteristic	Symbol	Value	Unit
Frequency of Operations	fosc		
Ceramic Resonator with built-in load capacitors		2.0	MHz
External Oscillator		2.0	MHz

Note: Communications Baud rate and active-state power consumption are scaled linearly with operating frequency. Higher operating frequencies are possible within a reduced operating voltage range. Consult Semtech for further information.





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