# HYBRID IC VLA313-2450

## ISOLATED TYPE DC-DC CONVERTER

#### DESCRIPTION

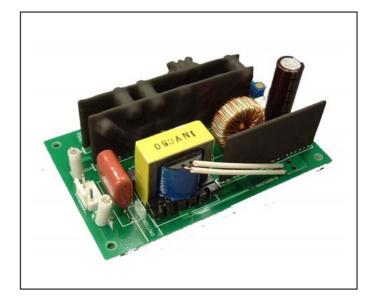
The VLA313-2450 is an isolated DC-DC converter designed to control the industrial equipment. It can input the rectified AC480V (DC850Vmax) directly and convert to low voltage. Total output power is 50W.

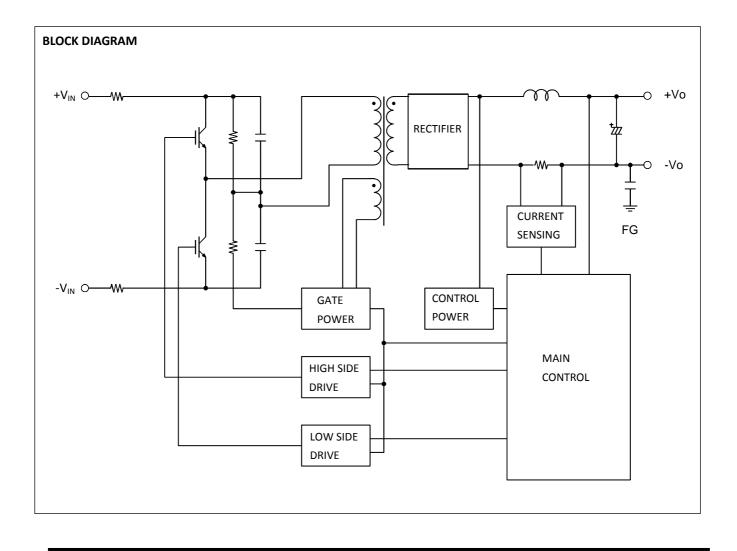
#### FEATURES

- •Input ----- 475V to 850V DC
- •Output-----+24V, 2.1A (50.4W)
- •Electrical isolation (between input and output) ------ 2500Vrms 1 minute
- •Over current protection (auto resumption)
- •Over voltage protection

#### APPLICATIONS

On-board pre-regulator for industrial control equipment







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Symbol	Parameter	Conditions	Ratings	Unit
V <sub>IN</sub>	Input voltage	-	850	V
Ι <sub>ο</sub>	Output current	-	2.1	А
Topr	Operating temperature	No condensation (*1)	-10 ~ +55	°C
Tstg	Storage temperature	No condensation	-20 ~ +75	°C
Viso	Electrical isolation between input and output	Sine wave voltage, 60Hz, 1min	2500	Vrms
	Electrical isolation between output and FG	Sine wave voltage, 60Hz, 1min	2500	Vrms

MAXIMUM RATINGS (unless otherwise noted, Ta=25°C)

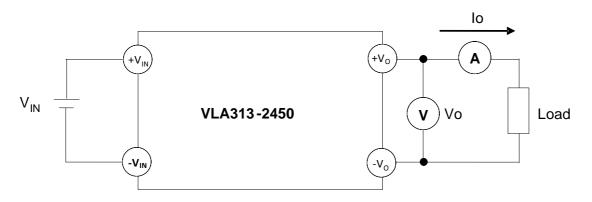
(\*1) Please refer to de-rating characteristics.

## **ELECTRICAL CHARACTERISTICS** (unless otherwise noted, V<sub>IN</sub>=680V, Ta=25°C)

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Тур.	Max.	Unit
V <sub>IN</sub>	Input voltage	Recommended range	475	680	850	V
Vo	Output voltage	lo=0.2 ~ 2.1A	22.8	24.0	25.2	V
Reg <sub>-I</sub>	Input regulation	lo=2.1A, V <sub>IN</sub> =475~850V	-	-	100	mV
Reg <sub>-L</sub>	Load regulation	lo=0.2 ~ 2.1A	-	-	150	mV
Vp-р	Ripple voltage	lo=2.1A (*2)	-	-	240	mV
η	Efficiency	lo=2.1A	-	82	-	%

(\*2) Not contain the spike noise.

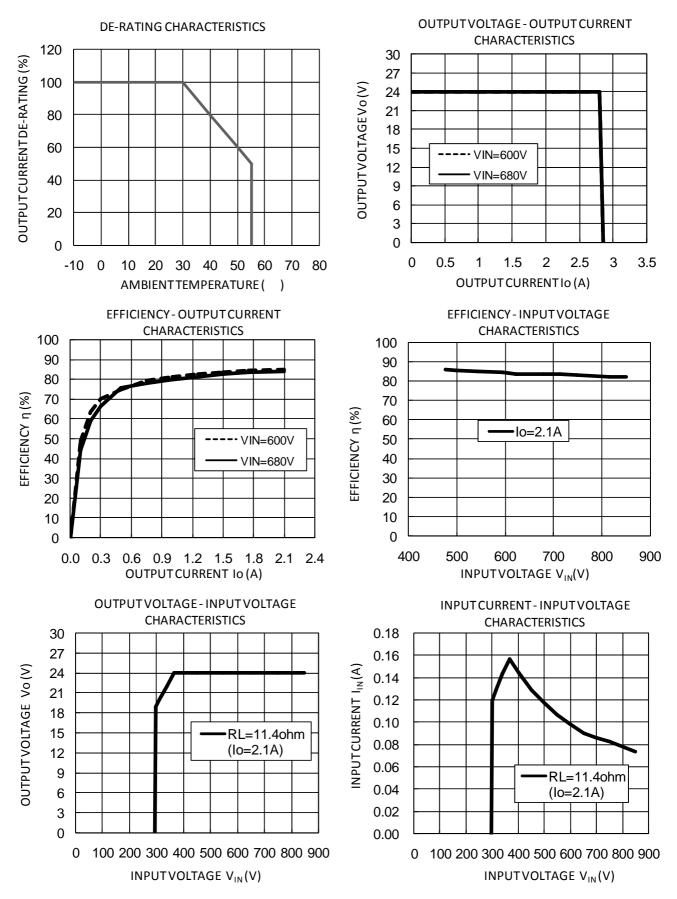
# **TEST CIRCUIT DIAGRAM**



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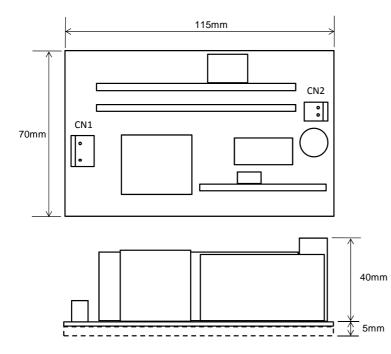
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#### **TYPICAL CHARACTERISTICS**



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### **OUTLINE DIMENSION**



#### CONNECTOR

	CN1	CN2
Parts No.	B2P3-VH, JST	B2P-VH, JST
Polarity (pin1)	-V <sub>IN</sub>	+Vo
Polarity (pin2)		-Vo
Polarity (pin3)	+V <sub>IN</sub>	

#### FOR SAFETY USING

Great detail and careful attention are given to the production activity of Hics, such as the development, the quality of production, and in it's reliability. However the reliability of Hics depends not only on their own factors but also in their condition of usage. When handling Hics, please note the following cautions.

CAUTIONS				
Packing	The materials used in packing Hics can only withstand normal external conditions. When exposed to outside shocks, rain and certain environmental contaminators, the packing materials will deteriorates. Please take care in handling.			
Carrying	<ol> <li>Don't stack boxes too high. Avoid placing heavy materials on boxes.</li> <li>Boxes must be positioned correctly during transportation to avoid breakage.</li> <li>Don't throw or drop boxes.</li> <li>Keep boxes dry. Avoid rain or snow.</li> <li>Minimal vibration and shock during transportation is desirable.</li> </ol>			
Storage	<ul> <li>When storing Hics, please observe the following notices or possible deterioration of their electrical characteristics, risk of solderability, and external damage may occur.</li> <li>1) Devices must be stored where fluctuation of temperature and humidity is minimal, and must not be exposed to direct sunlight. Store at the normal temperature of 5 to 30 degrees Celsius with humidity at 40 to 60%.</li> <li>2) Avoid locations where corrosive gasses are generated or where much dust accumulates.</li> <li>3) Storage cases must be static proof.</li> <li>4) Avoid putting weight on boxes.</li> </ul>			
Extended storage	When extended storage is necessary, Hics must be kept non-processed. When using Hics which have been stored for more than one year or under severe conditions, be sure to check that the exterior is free from flaw and other damages.			
Maximum ratings	To prevent any electrical damages, use Hics within the maximum ratings. The temperature, current, voltage, etc. must not exceed these conditions.			
Polarity	To protect Hics from destruction and deterioration due to wrong insertion, make sure of polarity in inserting leads into the board holes, conforming to the external view for the terminal arrangement.			

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