NEW PRODUC⁻

DFLR1200 / DFLR1400 / DFLR1600



1.0A SURFACE MOUNT GLASS PASSIVATED RECTIFIER PowerDI[™]123

Features

- Qualified to AEC-Q101 Standards for High Reliability
- Ideally Suited for Automated Assembly
- Green Molding Compound (No Br, Sb)
- Lead Free Finish, RoHS Compliant (Note 2)

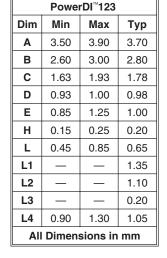
Mechanical Data

- Case: PowerDI[™]123
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture sensitivity: Level 1 per J-STD-020C
- Terminal Connections: Cathode Band
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 (€3)
- Marking & Type Code Information: See Last Page
- Ordering Information: See Last Page
- Weight: 0.01 grams (approx.)



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Maximum Ratings and Electrical Characteristics T_A = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	DFLR1200	1200 DFLR1400 DFLR1600			
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	200	200 400 600			
RMS Reverse Voltage	V _{R(RMS)}	140	280	420	V	
Average Rectified Output Current (see figure 4)	lo	1.0				
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load			Α			
Forward Voltage $@ I_F = 1.0A$	V _{FM}	1.1				
Peak Reverse Leakage Current@ $T_A = 25^{\circ}C$ at Rated DC Blocking Voltage@ $T_A = 125^{\circ}C$		3.0 100				
Typical Total Capacitance (f = 1MHz, V _R = 4.0VDC)	Ст	10				
Operating and Storage Temperature Range	T _j , T _{STG}	-65 to +150				

Thermal Characteristics

Characteristic	Symbol	Тур	Мах	Unit
Thermal Resistance, Junction to Ambient Air (Note 1)	$R_{\theta JA}$	134	_	°C/W
Thermal Resistance, Junction to Soldering Point (Note 3)	R _{0JS}	—	6	°C/W

Notes: 1. Device mounted on 1" x 1", FR-4 PCB; 2 oz. Cu pad layout as shown on Diodes Inc. suggested pad layout document AP02001.pdf.

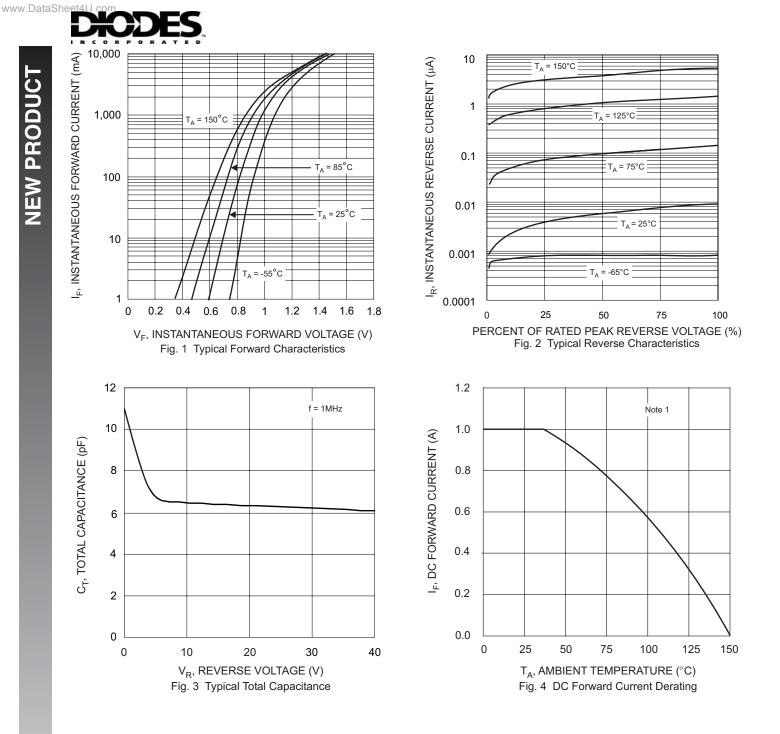
2. RoHS revision 13.2.2003. Glass and High Temperature Solder Exemptions Applied, see *EU Directive Annex Notes 5 and 7*.

3. Theoretical $R_{\theta JS}$ calculated from the top center of the die straight down to the PCB/cathode tab solder junction.

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PowerDI is a trademark of Diodes Incorporated.





Ordering Information (Note 4)

Device	Marking Code	Packaging	Shipping
DFLR1200-7	F12	PowerDI [™] 123	3000/Tape & Reel
DFLR1400-7	F14	PowerDI [™] 123	3000/Tape & Reel
DFLR1600-7	F18	PowerDI [™] 123	3000/Tape & Reel

Notes: 4. For Packaging Details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information

∐ Fxx ⋛∐

Fxx = Product Type Marking Code (See Table Above) YM = Date Code Marking Y = Year (ex: S = 2005) M = Month (ex: 9 = September)

Date Code Key

Year		2005	2006		2007		2008		2009			
Code		S	Т		U		V		W			
Month	Jan	Feb	March	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

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