# N-Channel 30-V (D-S) MOSFET

### **Key Features:**

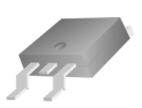
- Low r<sub>DS(on)</sub> trench technology
- · Low thermal impedance
- · Fast switching speed

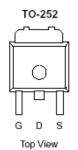
## **Typical Applications:**

- · White LED boost converters
- Automotive Systems
- Industrial DC/DC Conversion Circuits

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$r_{DS(on)}(m\Omega)$	I <sub>D</sub> (A)		
30	6 @ V <sub>GS</sub> = 10V	75		
	8 @ V <sub>GS</sub> = 4.5V	65		







ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^{\circ}$ C UNLESS OTHERWISE NOTED)						
Parameter		Symbol	Limit	Units		
Drain-Source Voltage		$V_{DS}$	30	V		
Gate-Source Voltage		$V_{GS}$	±20	V		
Continuous Drain Current a	T <sub>A</sub> =25°C	$I_D$	75	Α		
Pulsed Drain Current <sup>b</sup>		I <sub>DM</sub>	300	ζ		
Continuous Source Current (Diode Conduction) <sup>a</sup>	I <sub>S</sub>	30	Α			
Power Dissipation <sup>a</sup>	T <sub>A</sub> =25°C	$P_{D}$	50	W		
Operating Junction and Storage Temperature Range		$T_J, T_{stg}$	-55 to 175	°C		

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Maximum	Units		
Maximum Junction-to-Ambient <sup>a</sup>	$R_{\theta JA}$	40	°C/W		
Maximum Junction-to-Case	$R_{\theta JC}$	3	C/VV		

#### Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

#### **Electrical Characteristics**

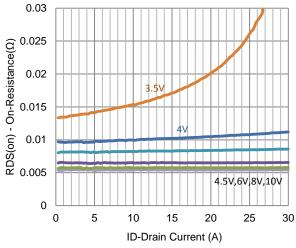
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static							
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \text{ uA}$				V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}$			1	uA	
Zero Gate Voltage Brain Gurrent	DSS	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			25	u/\	
On-State Drain Current	I <sub>D(on)</sub>	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	120			Α	
Drain-Source On-Resistance	r	$V_{GS} = 10 \text{ V}, I_{D} = 20 \text{ A}$			6	mΩ	
Dialii-Source Off-Nesistance	r <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, I_D = 18 \text{ A}$			8	11152	
Forward Transconductance	g <sub>fs</sub>	$V_{DS} = 15 \text{ V}, I_{D} = 20 \text{ A}$		20		S	
Diode Forward Voltage	$V_{SD}$	$I_{S} = 15 \text{ A}, V_{GS} = 0 \text{ V}$		0.84		V	
Dynamic							
Total Gate Charge	$Q_g$	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V},$		20			
Gate-Source Charge	$Q_gs$	$I_{DS} = 13 \text{ V}, \text{ V}_{GS} = 4.3 \text{ V},$ $I_{D} = 20 \text{ A}$		7.3		nC	
Gate-Drain Charge	$Q_gd$	1D = 20 A		11			
Turn-On Delay Time	t <sub>d(on)</sub>	$V_{DS} = 15 \text{ V}, R_1 = 0.8 \Omega,$		10			
Rise Time	t <sub>r</sub>	$I_{DS} = 13 \text{ V}, \text{ NL} = 0.0 \Omega_{2},$ $I_{D} = 20 \text{ A},$		13		nc	
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GEN} = 10 \text{ V}, R_{GEN} = 6 \Omega$		51		ns	
Fall Time	t <sub>f</sub>	VGEN = 10 V, NGEN 0 12		21			
Input Capacitance	C <sub>iss</sub>			1785			
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		323		pF	
Reverse Transfer Capacitance	$C_{rss}$			285			

#### **Notes**

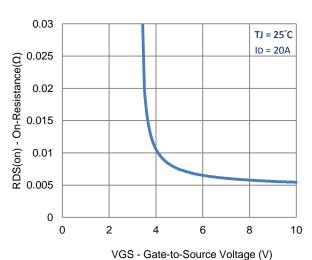
- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

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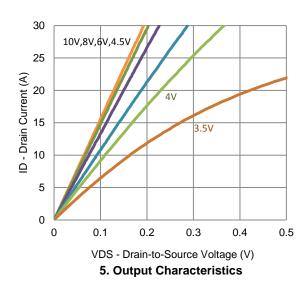
## **Typical Electrical Characteristics**



#### 1. On-Resistance vs. Drain Current

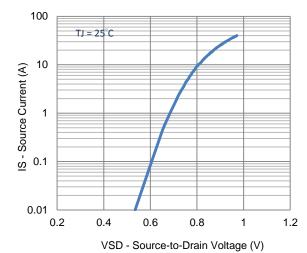


3. On-Resistance vs. Gate-to-Source Voltage

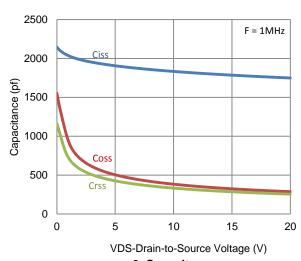


30 TJ = 25°C 25 ID - Drain Current (A) 20 15 10 5 0 0 1 2 3 5 6 VGS - Gate-to-Source Voltage (V)

2. Transfer Characteristics

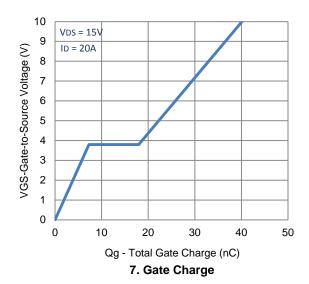


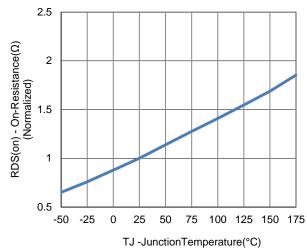
4. Drain-to-Source Forward Voltage

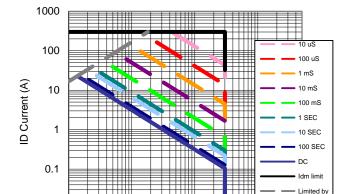


6. Capacitance

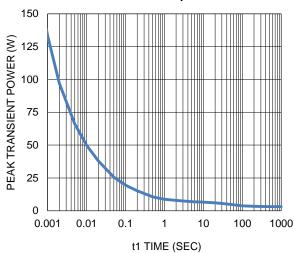
## **Typical Electrical Characteristics**







8. Normalized On-Resistance Vs Junction Temperature



VDS Drain to Source Voltage (V)

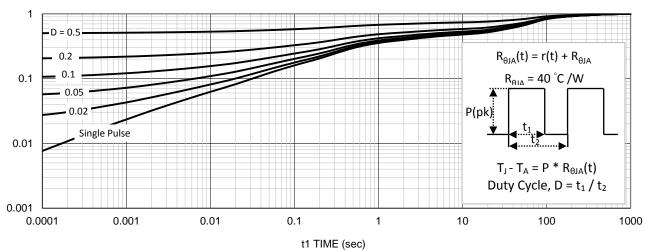
9. Safe Operating Area

10

0.01

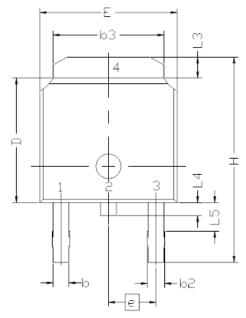
0.1

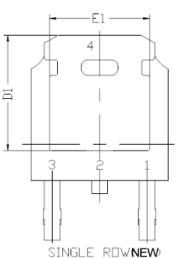
10. Single Pulse Maximum Power Dissipation

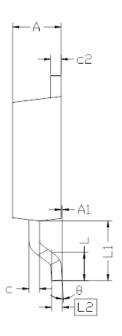


11. Normalized Thermal Transient Junction to Ambient

## **Package Information**







SYMBOL	DIMENS: MIN	IDNAL F NDM	REQMTS MAX		
E	6.40	6.60	6.731		
L	1.40	1.52	1.77		
L1	2.743 REF				
	0.	.508 BS			
L3	0.89		1.27		
L4	0.64		1.01		
L5					
D	6.00	6.10	6,223		
Н	9.40	10.00	10.40		
b	0.64	0.76	0.88		
b2	0.77	0.84	1.14		
b3	5.21	5.34	5,46		
е		286 BS			
Ā	2,20	2,30	2,38		
A1	0		0.127		
C	0.45	0.50	0.60		
c2	0.45	0.50	0,58		
D1	5,30				
E1	4.40				
	0°		10°		

#### Note:

- 1. All Dimension Are In mm.
- 2. Package Body Sizes Exclude Mold Flash, Protrusion Or Gate Burrs. Mold Flash, Protrusion Or Gate Burrs Shall Not Exceed 0.10 mm Per Side.
- 3. Package Body Sizes Determined At The Outermost Extremes Of The Plastic Body Exclusive Of Mold Flash, Gate Burrs And Interlead Flash, But Including Any Mismatch Between The Top And Bottom Of The Plastic Body.