

RQJ0305EQDQA

Silicon P Channel MOS FET Power Switching

R07DS0297EJ0200 (Previous: REJ03G1718-0100) Rev.2.00 Mar 28, 2011

Features

• Low gate drive

 V_{DSS} : -30 V and 2.5 V gate drive

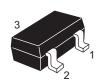
• Low drive current

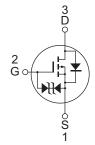
• High speed switching

• Small traditional package (MPAK)

Outline

RENESAS Package code: PLSP0003ZB-A (Package name: MPAK)





1. Source

2. Gate

3. Drain

Notes: Marking is "EQ".

Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	-30	V
Gate to source voltage	V _{GSS}	+8 / –12	V
Drain current	I _D	-2.4	А
Drain peak current	I _{D(pulse)} Note1	-10	A
Body - drain diode reverse drain current	I _{DR}	2.4	Α
Channel dissipation	Pch Note2	0.8	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 $\mu s,$ Duty cycle \leq 1%

2. When using the glass epoxy board (FR-4 40 \times 40 \times 1 mm)

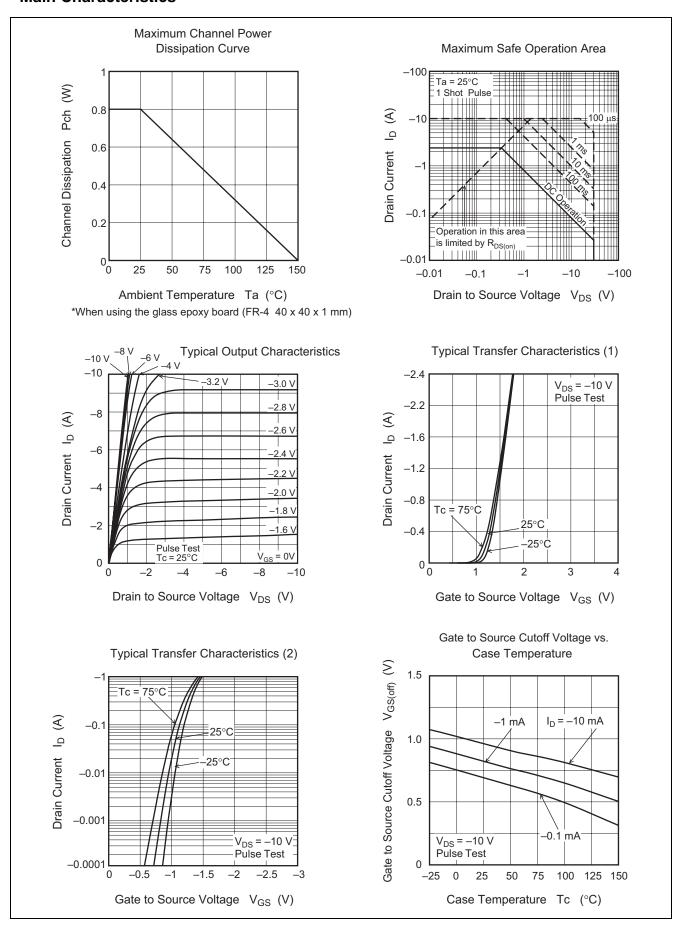
Electrical Characteristics

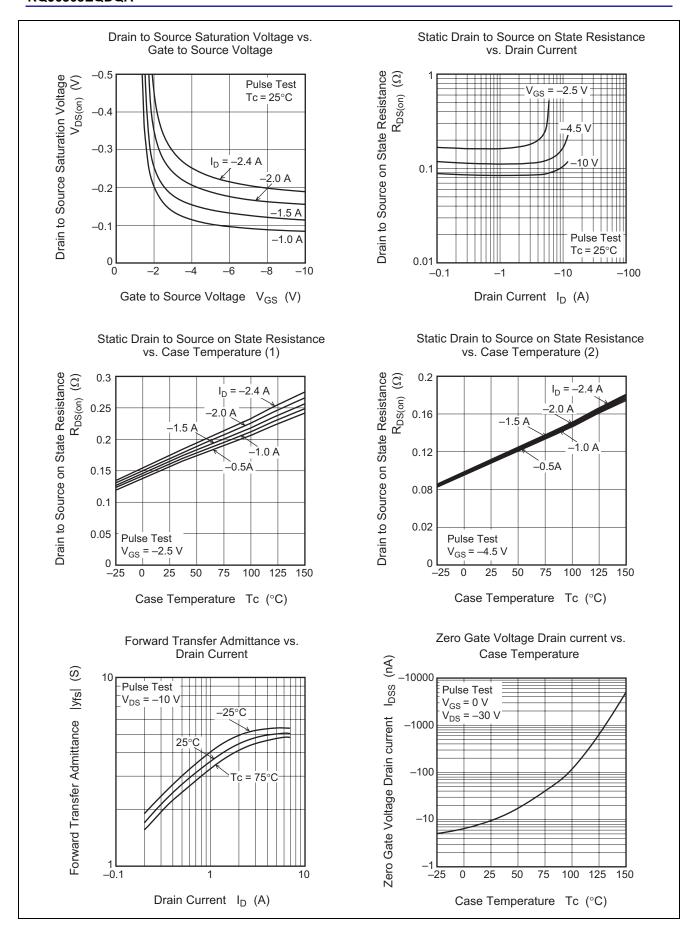
 $(Ta = 25^{\circ}C)$

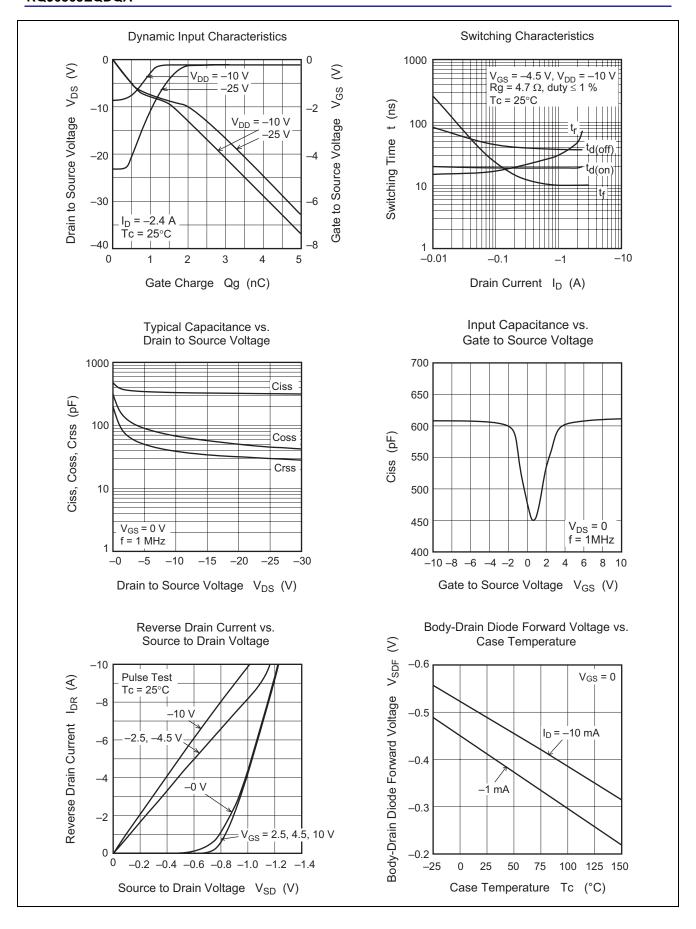
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	-30	_	_	V	$I_D = -10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	+8	_	_	V	$I_G = +100 \mu A, V_{DS} = 0$
Gate to source breakdown voltage	V _{(BR)GSS}	-12	_	_	V	$I_G = -100 \mu A, V_{DS} = 0$
Gate to source leak current	I _{GSS}	_	_	+10	μΑ	$V_{GS} = +6 \text{ V}, V_{DS} = 0$
Gate to source leak current	I _{GSS}	_	_	-10	μΑ	$V_{GS} = -10 \text{ V}, V_{DS} = 0$
Drain to source leak current	I _{DSS}	_	_	-1	μΑ	$V_{DS} = -30 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	V _{GS(off)}	-0.4	_	-1.4	V	$V_{DS} = -10 \text{ V}, I_{D} = -1 \text{ mA}$
Drain to source on state resistance	R _{DS(on)}	_	110	140	mΩ	$I_D = -1.3 \text{ A}, V_{GS} = -4.5 \text{ V}^{\text{Note3}}$
Drain to source on state resistance	R _{DS(on)}	_	165	230	mΩ	$I_D = -1.3 \text{ A}, V_{GS} = -2.5 \text{ V}^{\text{Note3}}$
Forward transfer admittance	y _{fs}	2.6	3.9	_	S	$I_D = -1.3 \text{ A}, V_{DS} = -10 \text{ V}^{\text{Note3}}$
Input capacitance	Ciss	_	330	_	pF	$V_{DS} = -10 \text{ V}, V_{GS} = 0,$
Output capacitance	Coss	_	70	_	pF	f = 1 MHz
Reverse transfer capacitance	Crss	_	40	_	pF	
Turn - on delay time	t _{d(on)}	_	17	_	ns	$I_D = -1.3 \text{ A}$
Rise time	t _r	_	37	_	ns	$V_{GS} = -4.5 \text{ V}$
Turn - off delay time	t _{d(off)}	_	39	_	ns	$R_L = 7.7 \Omega$
Fall time	t _f	_	10	_	ns	$R_g = 4.7 \Omega$
Total gate charge	Qg	_	3.0	_	nC	V _{DD} = −10 V
Gate to Source charge	Qgs	_	0.6	_	nC	$V_{GS} = -4.5 \text{ V}$
Gate to drain charge	Qgd	_	1.3	_	nC	$I_D = -2.4 \text{ A}$
Body - drain diode forward voltage	V_{DF}	_	-0.85	-1.2	V	$I_F = -2.4 \text{ A}, V_{GS} = 0^{\text{Note3}}$

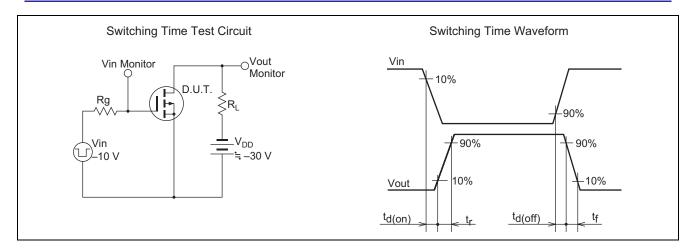
Notes: 3. Pulse test

Main Characteristics

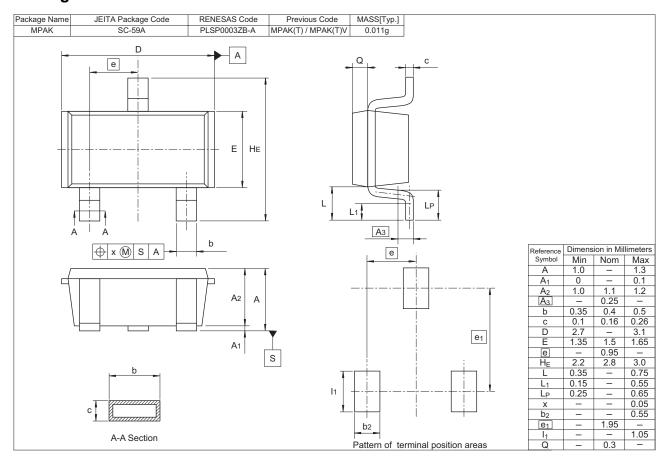








Package Dimensions



Ordering Information

Orderable Part Number	Quantity	Shipping Container
RQJ0305EQDQATL-H	3000 pcs.	φ178 mm reel, 8 mm Emboss taping

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