

### 300mA LOW DROPOUT LINEAR REGULATOR

#### **FEATURES**

- Low Dropout Voltage of 250mV at 300mA
- Guaranteed 300mA Output Current
- Very Low Quiescent Current at about 30uA
- ±2% Output Voltage Accuracy for 1.2V~3.3V
- Needs Only 1µF Capacitor for Stability
- Thermal Shutdown Protection
- Current Limit Protection
- Output Voltage Fast Discharge
- Low-ESR Ceramic Capacitor for Output Stability.
- Tiny SC-70-4L 、SC-70-5L 、SOT-23 & SOT-23-5L packages
- RoHS Compliant & Halogen Free
- High PSRR

#### **APPLICATIONS**

- DVD/CD-ROMs, CD/RWs
- **■** Wireless Devices
- LCD Modules
- Battery Power Systems
- Card Readers
- XDSL Routers

### **DESCRIPTIOON**

The APE8865 series are low dropout, positive linear regulators with very low quiescent current. The APE8865 can supply 300mA output current with a low dropout voltage at about 250mV.

The APE8865 regulator is able to operate with output capacitors as small as  $1\mu F$  for stability. Other than the current limit protection APE8865 also offers on chip thermal shutdown feature providing protection against overload or any condition when the ambient temperature exceeds the junction temperature.

The APE8865 series are offering several fixed output voltage types including  $1.2V \sim 3.3V$ .

The APE8865 series are available in low-profile, space-saving SC-70-4L  $\cdot$  SC-70-5L  $\cdot$  SOT-23 & SOT-23-5L packages.

### TYPICAL APPLICATION

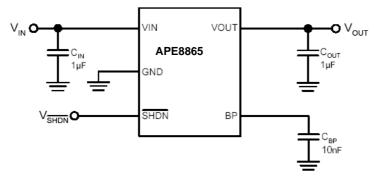
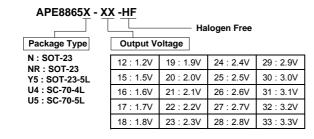


Figure 1. Typical Application Circuit of APE8865

Note: To prevent oscillation, it is recommended to use minimum 1uF X7R or X5R dielectric capacitors if ceramics are used as input / output capacitors.

#### ORDERING INFORMATION





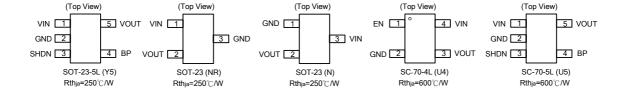
### **ABSOLUTE MAXIMUM RATINGS**

(SC70-5L) ------ 0.16W
Storage Temperature Range ------ -65°C To 150°C

Maximum Junction Temperature ----- 150°C

#### RECOMMENDED OPERATING CONDITIONS

#### PACKAGE INFORMATION



#### **ELECTRICAL SPECIFICATIONS**

(V<sub>IN</sub>=V<sub>OUT</sub>+1V or V<sub>IN</sub>=2.8V whichever is greater, C<sub>IN</sub>=1uF, C<sub>OUT</sub>=1uF, T<sub>A</sub>=25°C, unless otherwise specified)

Parameter	SYM	TEST CONDITION		MIN	TYP	MAX	UNITS
Output Voltage Accuracy	$\Delta V_{OUT}$	I <sub>O</sub> = 1mA		-2	-	2	%
Current Limit	I <sub>LIMIT</sub>	$R_{Load}$ =1 $\Omega$		300	-	-	mA
Quiescent Current	IQ	I <sub>O</sub> = 0mA		-	30	50	μΑ
			$1.2V \le V_{OUT} \le 2.0V$	-	1000	•	
Dropout Voltage (Note 1)	$V_{DROP}$	I <sub>O</sub> =300mA	$2.0V < V_{OUT} \le 2.8V$	-	350	-	mV
			$2.8V < V_{OUT} \le 3.3V$	-	250	-	
Line Regulation	$\Delta V_{LINE}$	I <sub>O</sub> =1mA, V <sub>IN</sub> =V <sub>OUT</sub> +1V to 5V		-	1	5	mV
Load Regulation (Note 2)	$\Delta V_{LOAD}$	$I_0$ =0mA to 3	I <sub>O</sub> =0mA to 300mA		6	20	mV
Ripple Rejection	PSRR	$I_o$ =1mA, $C_{OUT}$ =1uF, $f_{RIPPLE}$ = 100Hz		-	-73	-	mV
Rippie Rejection	FORK	f <sub>RIPPLE</sub> = 10KHz		-	-50	1	
Temperature Coefficient	TC	I <sub>OUT</sub> = 1mA, V <sub>IN</sub> = 5V		-	50	-	ppm/ °C
Thermal Shutdown Temperature	TSD			-	160	-	°C
Thermal Shutdown Hysteresis	ΔTSD			-	25	•	°C
Shutdown Pin Current	I <sub>SHDN</sub>			-	-	0.1	μΑ
Shutdown Pin Voltage (ON)	$V_{\overline{SHDN}(ON)}$			1.4	-	•	V
Shutdown Pin Voltage (OFF)	V <sub>SHDN(OFF)</sub>			-	-	0.4	V
Shutdown Exit Delay Time	ΔΤ	$C_{BP}$ =0.1uF, $C_{OUT}$ =1uF, $I_{OUT}$ =30mA		-	150	-	μs

Note 1 : The dropout voltage is defined as  $V_{IN}$ - $V_{OUT}$ , which is measured when  $V_{OUT}$  drop about 100mV.

Note 2 : Regulation is measured at a constant junction temperature by using 30ms current pulse and load regulation in the load range from 0mA to 300mA.

## **PIN DESCRIPTIONS**

PIN SYMBOL	PIN DESCRIPITON
VIN	Power is supplied to this device from this pin which is required an input filter capacitor. In general, the input capacitor in the range of 1µF to 10µF is sufficient.
VOUT	The output supplies power to loads. The output capacitor is required to prevent output voltage unstable. The APE8865 is stable with an output capacitor 1µF or greater. The larger output capacitor will be required for application with large transit load to limit peak voltage transits, besides could reduce output noise, improve stability, PSRR.
GND	Common ground pin
BP	Reference Noise Bypass ( the Bypass Capacitor ≥ 1nF )
SHDN	Chip Enable (Active High)

## **BLOCK DIAGRAM**

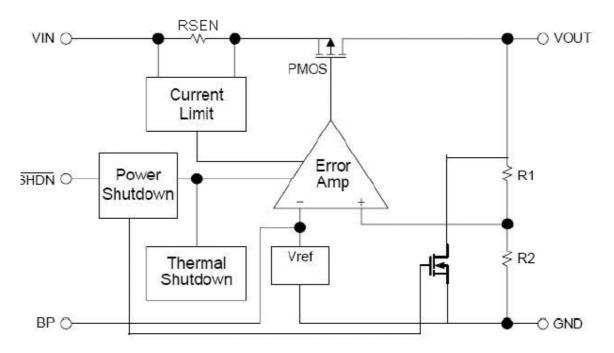


Figure 2. Block Diagram of APE8865 With Auto Discharge

## **Application Information**

The APE8865 series are low dropout linear regulators that could provide 300mA output current at dropout voltage about 300mV. Besides, current limit and on chip thermal shutdown features provide protection against any combination of overload or ambient temperature that could exceed junction temperature.

### 1.Output and Input Capacitor

The APE8865 regulator is designed to be stable with a wide range of output capacitors. The ESR of the output capacitor affects stability. Larger value of the output capacitor decreases the peak deviations and provides to improve transition response for larger current changes.

The capacitor types (aluminum, ceramic, and tantalum) have different characterizations such as temperature and voltage coefficients. All ceramic capacitors are manufactured with a variety of dielectrics, each with different behavior across temperature and applications. Common dielectrics used are X5R, X7R and Y5V. It is recommended to use 1uF X5R or X7R dielectric ceramic capacitors with  $30 m \Omega$  to  $50 m \Omega$  ESR range between device outputs to ground for transient stability. The APE8865 is designed to be stable with low ESR ceramic capacitors and higher values of capacitors and ESR could improve output stability.

So the ESR of output capacitor is very important because it generates a zero to provide phase lead for loop stability.

There are no requirements for the ESR on the input capacitor, but its voltage and temperature coefficient have to be considered for device application environment.

#### 2.Protection Features

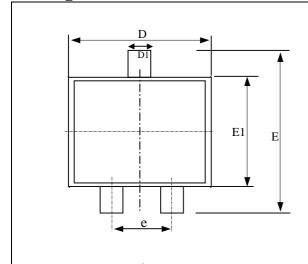
In order to prevent overloading or thermal condition from damaging the device, APE8865 regulator has internal thermal and current limiting functions designed to protect the device. It will rapidly shut off PMOS pass element during overloading or over temperature condition.

#### 3. Thermal Consideration

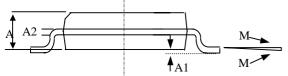
The power handling capability of the device will be limited by maximum operation junction temperature (125°C). The power dissipated by the device will be estimated by PD = IOUT × (VIN-VOUT). The power dissipation should be lower than the maximum power dissipation listed in "Absolute Maximum Ratings" section.



Package Outline: SOT-23

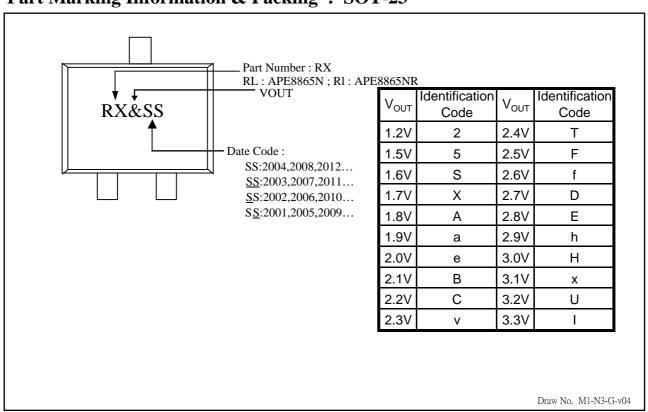


SYMBOLS	Millimeters			
	MIN	NOM	MAX	
A	0.88		1.30	
A1	0.00		0.10	
A2	0.08		0.25	
D1	0.30	0.40	0.50	
e	1.70	2.00	2.30	
D	2.70	2.90	3.10	
Е	2.20	2.60	3.00	
E1	1.20	1.40	1.60	
M	0°		10°	



- 1.All Dimension Are In Millimeters.
- 2. Dimension Does Not Include Mold Protrusions.

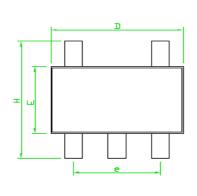
## **Part Marking Information & Packing: SOT-23**

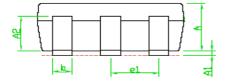


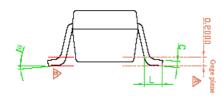


### ADVANCED POWER ELECTRONICS CORP.

## Package Outline: SOT-23-5L







SYMBOLS	Millimeters			
	MIN	NOM	MAX	
A	1.00	1.10	1.30	
A1	0.00		0.10	
A2	0.70	0.80	0.90	
b	0.30	0.40	0.50	
С	0.10	0.15	0.25	
D	2.70	2.90	3.10	
Е	1.40	1.60	1.80	
e	1.90(TYP)			
Н	2.60	2.60 2.80		
L	0.37			
θ1	0° 5°		9°	
e1	0.95(TYP)			

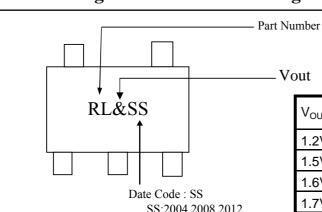
Note 1: Package Body Sizes Exclude Mold Flash Protrusions or Gate Burrs.

Note 2 : Tolerance ± 0.1000 mm(4mil) Unless Otherwise Spe- cified.

Note 3 : Coplanarity : 0.1000 mm

Note 4: Dimension L Is Measured in Gage plane.

## **Part Marking Information & Packing: SOT-23-5L**



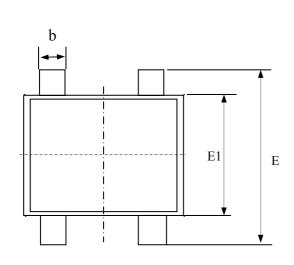
SS:2004,2008,2012... SS:2003,2007,2011... SS:2002,2006,2010... SS:2001,2005,2009...

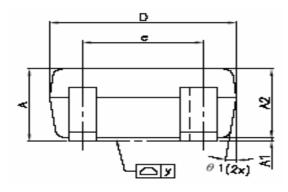
V <sub>OUT</sub>	Identification Code	V <sub>OUT</sub>	Identification Code
1.2V	2	2.4V	Т
1.5V	5	2.5V	F
1.6V	S	2.6V	f
1.7V	Χ	2.7V	D
1.8V	Α	2.8V	Е
1.9V	а	2.9V	h
2.0V	е	3.0V	Н
2.1V	В	3.1V	Х
2.2V	С	3.2V	U
2.3V	V	3.3V	I



### ADVANCED POWER ELECTRONICS CORP.

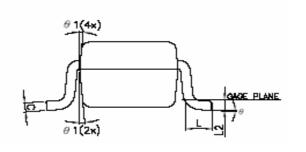
## Package Outline: SC-70-4L



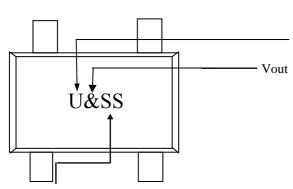


- 1.All Dimension Are In Millimeters.
- 2.Dimension Does Not Include Mold Protrusions.

	Millimeters			
SYMBOLS	MIN	NOM	MAX	
A	0.80		1.10	
A1	0.00		0.10	
A2	0.70	0.90	1.00	
b	0.25	0.30	0.40	
C	0.10		0.26	
D	1.80	2.00	2.20	
E1	1.15	1.25	1.35	
Е	1.80	2.10	2.40	
e	_	1.30		
L	0.15	0.30	0.45	
L2	_	0.15		
у			0.10	
θ	0°		8°	
θ1	4°		12°	



# Part Marking Information & Packing : SC-70-4L



Date Code: SS

SS:2004,2008,2012...

SS:2003,2007,2011...

<u>S</u>S:2002,2006,2010···

SS:2001,2005,2009···

"A~Z" showed on 3rd position --> week 1 ~ week 26,

"A~Z" showed on 4th position --> week 27 ~ week 52.

#### — Part Number

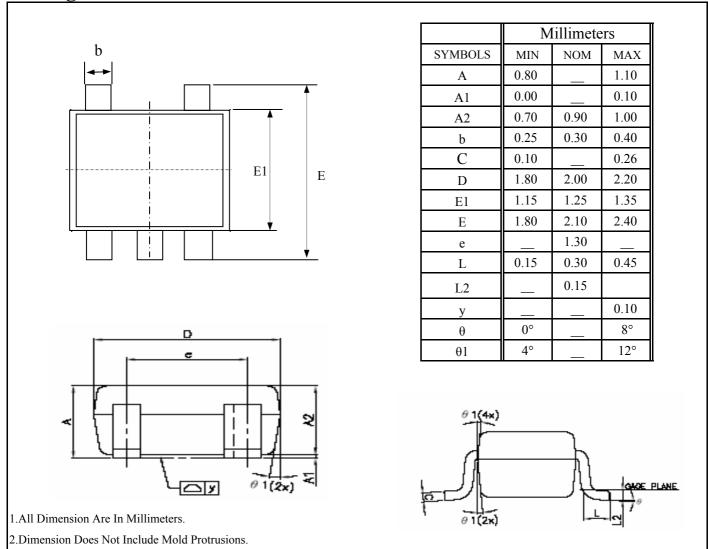
V <sub>OUT</sub>	Identification Code	V <sub>OUT</sub>	Identification Code
1.2V	В	2.4V	Т
1.5V	С	2.5V	F
1.6V	S	2.6V	f
1.7V	Χ	2.7V	W
1.8V	D	2.8V	G
1.9V	а	2.9V	h
2.0V	е	3.0V	Н
2.1V	b	3.1V	Х
2.2V	0	3.2V	U
2.3V	٧	3.3V	I

Draw No. M-U-4-G-01



### ADVANCED POWER ELECTRONICS CORP.

## Package Outline: SC-70-5L



## Part Marking Information & Packing: SC-70-5L

