ALT Isolated DC/DC Converter Module

Industry Standard Size: 36-75V Input, ±12V, ±5V double Output

Industry Standard Size: 2"X 1.6"package

Features

- **2"X 1.6"package**
- **Basic isolation**
- **High efficiency**
- **High power density**
- **2:1 wide input of 36-75V**
- **CNT function**
- **Trim function**
- **Input under-voltage lockout**
- **Output short circuit protection**
- **Output over-voltage protection**
- **Wide operating case temperature range**

Options

Choice of positive logic or negative logic for CNT function

Choice of short pins or long pins

Description

The ALT series is a new open frame DC-DC converter. It is one of the most cost effective options available in component power. The ALT series uses an industry standard package size: 50.8mm X 40.6mm X 9.66mm (2"x1.6"x0.38") and standard pin-out configuration, provides CNT and trim functions.

ALT series comes in 48V input versions, each of which uses a 2:1 input range of 36~75V. The series can provide ±12V@1.25A and ±5V@3A double outputs, outputs are isolated from input. And the converters are capable of providing up to 30 watts of output power.

Contents

Electrical Specifications

Unless otherwise indicated, specifications apply over all operating input voltage and temperature conditions. Standard test condition on a single unit is as following:

Ta(ambient): 25°C
+Vin: 48V +/ –2%
–Vin: return pin for +Vin
CNT: Open
+Vo: connect to load
–Vo: connect to load
COM: connect to load
Trim(Vadj): Open

Input Specifications

Notes: I(out1) is the load of +Vo, I(out2) is the load of –Vo.

Parameter	Device	Symbol	Min	Typ	Max	Unit
Operating Input Voltage	All	V_I	36	48	75	V_{DC}
Maximum Input Current (V_I = 0 to $V_{I,max}$, I(out1) = I(out2)= Io,max)	±5V ±12V	$I_{I,max}$ $I_{I,max}$	- -	- -	1.5 1.5	A A
Input Reflected-ripple Current (5Hz to 20MHz: 12uH source impedance: T_A = 25 ºC.)	All	I_I	-	-	40	mAp-p
No Load Input Power (V_I = $V_{I,nom}$)	All	-	-	-	1	W

CAUTION: This power module is not internally fused. An input line fuse must always be used.

Absolute Maximum Ratings

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. These are absolute stress ratings only. Functional operation of the device in not implied at these or any other conditions in excess of those given in the operational sections of the IPS. Exposure to absolute maximum ratings for extended periods can adversely affect device reliability.

Parameter	Device	Symbol	Min	Typ	Max	Unit
Input Voltage: Continuous: Transient (100ms)	 All All	 V_I $V_{I, trans}$	 0 0	 - -	 80 100	 V_{DC} V_{DC}
Operating Ambient Temperature	All	Tc/Ta	-40	-	55	ºC
Storage Temperature	All	T_{STG}	-55	-	125	ºC
Operating Humidity	All	-	-	-	85	%
I/O Isolation (Conditions: 50μA for 5 sec, slew rate of 1500V/10sec) Input-Output	 All	 -	 -	 -	 1500	 V_{DC}
Output Power	±5V ±12V	Po,max Po,max	- -	- -	30 30	W W

Output Specifications

Parameter	Device	Symbol	Min	Typ	Max		Unit
Output Ripple and Noise (Across 1µF @50V, X7R ceramic capacitor & 10µF @25V tantalum capacitor) Peak-to-Peak (5 Hz to 20 MHz)	 ±5V ±12V	 - -	 - -	 - -	Ripple 85 70	Noise 150 150	 mVp-p mVp-p
External Load Capacitance Of Every Load	All	-	100	-	2200		µF
Cross regulation (-Vo) I(out1)=Io,min I(out2)= Io,min -100%Io,max Or I(out1)=100%Io,max, I(out2)= Io,min -100%Io,max	All	-		7%	10%		%-Vonom
Output Voltage Setpoint (V_I = $V_{I,min}$ to $V_{I,max}$: Io = Io,max; T_A = 25 ºC)	±5V ±12V	$V_{o,set}$ $V_{o,set}$	4.93 4.80 11.80 11.70		5.07 5.20 12.20 12.30		V_{DC} V_{DC} V_{DC} V_{DC}
Output Regulation: Line Load(Test only for +Vo) (I(out2) = Io,max I(out1) = Io,min to Io,max)	 +Vo -Vo All	 - - -	 - - -	 0.1 0.1 0.2	 0.2 0.5 0.5		 % % %Vo
Rated Output Current (for every load)	±5V ±12V	Io Io	0.3 0.125	- -	3 1.25		A A
Output Current-limit Inception (Test is between +Vo and –Vo)	±5v ±12v	Io Io	3.1 1.35	- -	5 2.9		A A
Efficiency (V_I = $V_{I,nom}$; $I_{o,max}$; T_A = 25°C)	±5V ±12V	- -	82 85	84 87	- -		% %
Switching Frequency	All	-		330			KHz

Output Specifications (Cont)

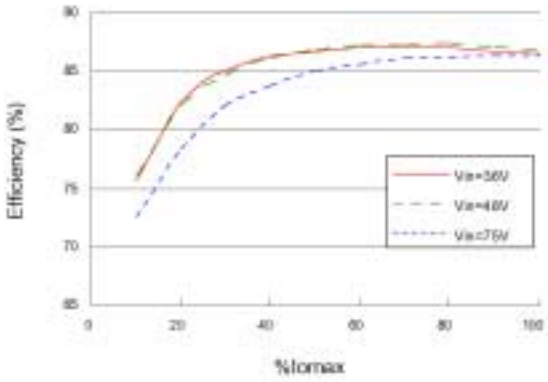
Parameter	Device	Symbol	Min	Typ	Max	Unit
Dynamic Response : (Test only for +Vo ,ΔIo/Δt = 1A/10μs ; $V_I = V_{I,nom}$; TA = 25°C)						
Load Change from I(out1) = 50% to 75% of Io,max :I(out2)= Io,max Peak Deviation Settling Time (to $V_{o,nom}$)	All	- -	- -	- -	3 200	%+Vo μsec
Load Change from I(out1) = 25% to 50% of Io,max :I(out2)= Io,max Peak Deviation Settling Time (to $V_{o,nom}$)	All	- -	- -	- -	3 200	%+Vo μsec
Turn-On Time (Io = Io,max ; Vo within 1%)	All	-	-	-	20	msec
Output Voltage Overshoot (Io = Io,max ; T_A = 25°C)	All	-	-	-	5	%Vo

Feature Specifications

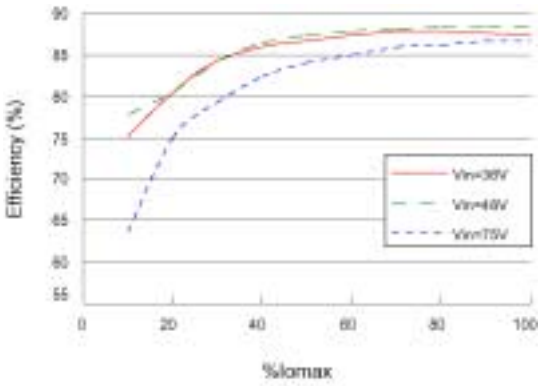
Parameter	Device	Symbol	Min	Typ	Max	Unit
Enable pin voltage : Logic Low Logic High Enable pin current : Logic Low Logic High(leakage current, @10V)	 All All All All		 -0.7 3.5 - -	 - - - -	 0.8 12 2.0 	 V V mA μA
Output Voltage Adjustment Range	All	-	90	-	110	%Vo
Output Over-voltage Clamp	±5V ±12V	Vo_{clamp} Vo_{clamp}	5.9 13.9	-	7.00 16.00	V V
Under-voltage Lockout Turn-on Point Turn-off Point	 All All	 - -	 31 30	 34 33	 36 35	 V V
Isolation Capacitance	All	-	-	2200	-	PF
Isolation Resistance	All	-	100	-	-	MΩ
Calculated MTBF (Io = Io,max ; T_A = 25°C)	All	-	-	2,000,000	-	Hours
Weight	All	-	-	-	25	g(oz.)

Characteristic Curves

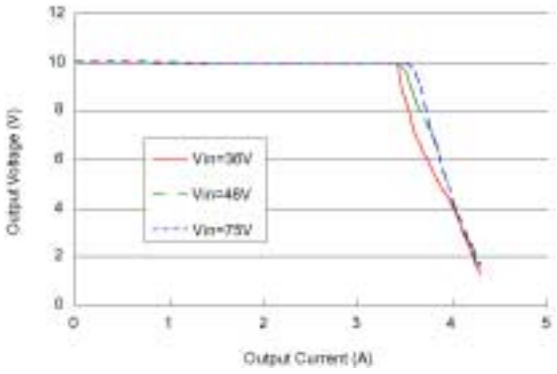
Performance Curves – Efficiency and OCP



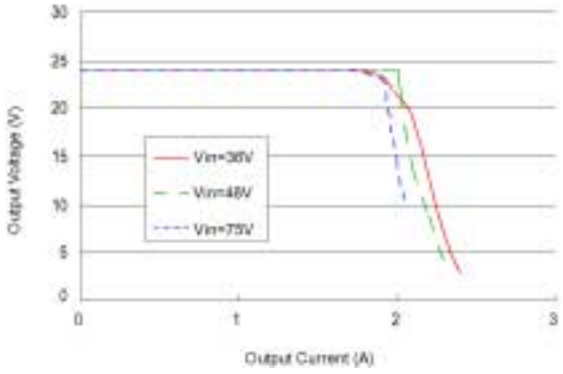
Typical Efficiency of ALT03AA48



Typical Efficiency of ALT01BB48

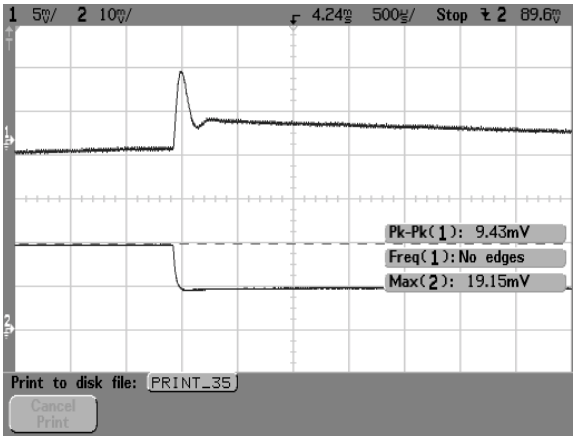


Typical OCP of ALT03AA48



Typical OCP of ALT01BB48

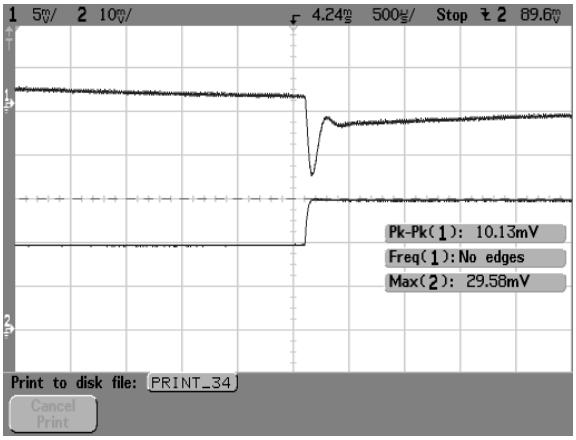
Performance Curves – Transient Response



ALT01BB48

I(out1) = 50% to 25% of Io,max load change

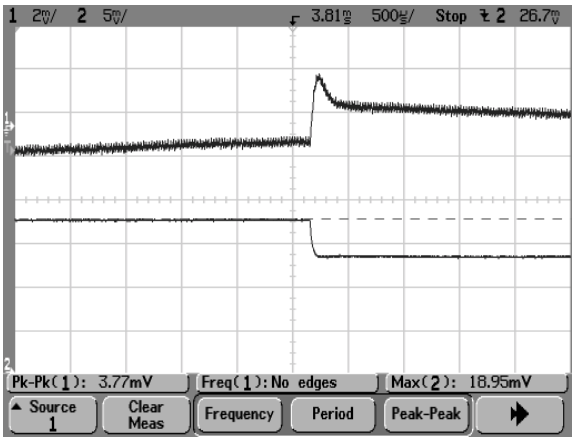
I(out2)=1.25A



ALT01BB48

I(out1) =50% to75% of Io,max load change

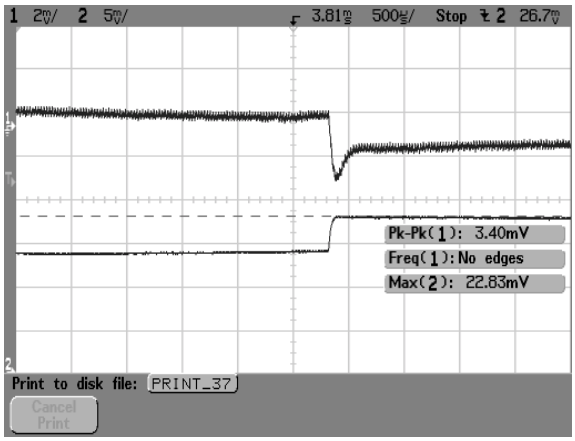
I(out2)=1.25A



ALT03AA48

I(out1) =50% to 25% of Io,max load change

I(out2)=3A

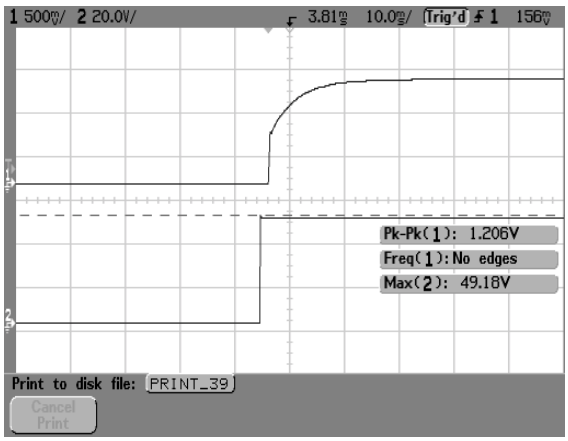


ALT03AA48

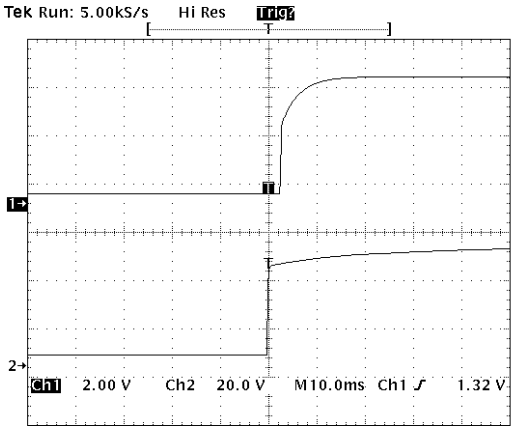
I(out1) =50% to75% of Io,max load change

I(out2)=3A

Performance Curves – Startup Characteristics

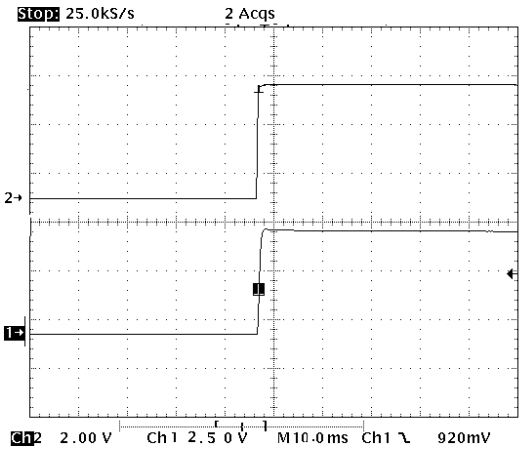


ALT01BB48 Start-up from Power On

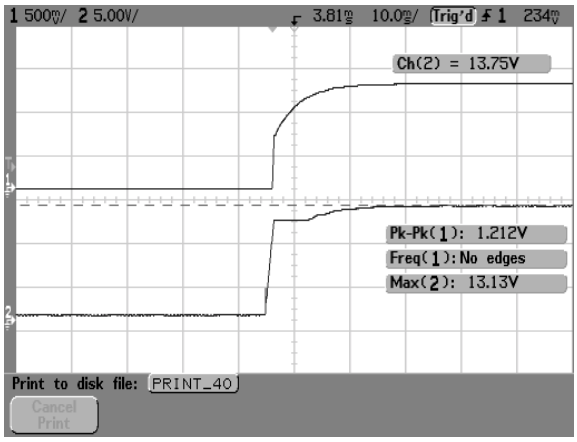


ALT03AA48 Start-up from Power On

Performance Curves – Startup from CNT Control



ALT03AA48 Start-up from CNT On



ALT01BB48 Start-up from CNT On

Feature Description

CNT Function

Two CNT logic options are available. The CNT logic, CNT voltage and the module working state are as the following Table 1.

	L	H	OPEN
N	**ON**	**OFF**	**OFF**
P	**OFF**	**ON**	**ON**

Table 1

N--- means “Negative Logic”, P--- means “Positive Logic”

L--- means “Low Voltage”, -0.7V≤L≤0.8V

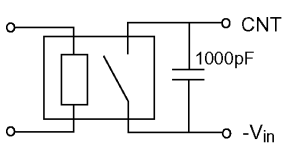
H--- means “High Voltage”, 3.5V≤H≤12V

ON--- means “Module is on”, OFF--- means “Module is off”

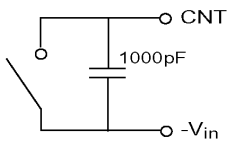
Open--- means “CNT pin is left open “

Note: Normally, V_{CNT} ≤12V，But when CNT is left open,V_{CNT} may reach to 18V.

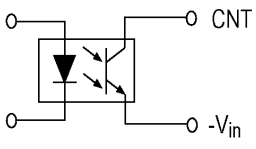
The following Figure shows a few simple CNT circuits.



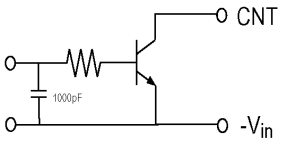
Relay CNT



Simple CNT



Isolated CNT

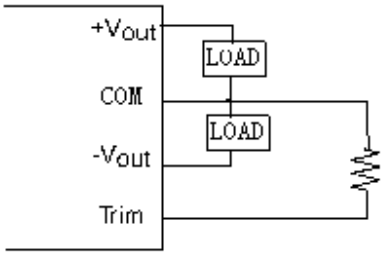


Transistor CNT

Trim

The +Vo output voltage of the ALT series can be trimmed using the trim pin provided. Applying a resistor to the trim pin through a voltage divider from the output will cause the +Vo output to increase or decrease by up to 10%. Trimming up by more than 10% of the nominal output may activate the OVP circuit or damage the converter. Trimming down more than 10% can cause the converter to regulate improperly. If the trim pin is not needed, it should be left open.

Trim up



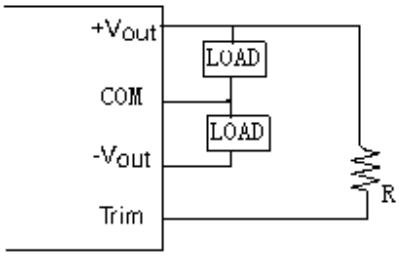
±5Vout: Rup= 1.25/y

±12Vout: Rup=1.97/y

where: $y = \frac{Vo - Ve}{Ve}$

All resistor values in kΩ,
Ve is rated output voltage, Vo is adjusting voltage.

Trim down



±5Vout: Rdown =1.25/y-2.69

±12Vout: Rdown =7.49/y-9.46

where: $y = \frac{Vo - Ve}{Ve}$

All resistor values in kΩ,
Ve is rated output voltage, Vo is adjusting voltage.

Minimum Load Requirements

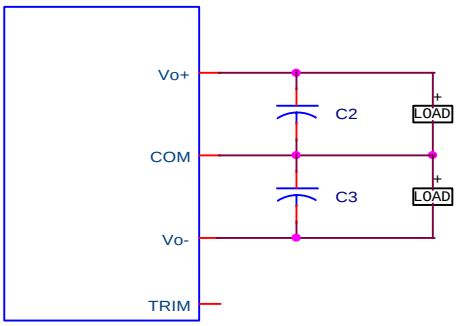
Parameter	Device	Symbol	Typ	Unit
Minimum Load	±5V	I_{MIN}	0.3	A
(for every load)	±12V	I_{MIN}	0.125	A

Output Over-current Protection

ALT series DC/DC converters feature fold-back current limiting as part of their Over-current Protection (OCP) circuits. When output current exceeds 110 to 140% of rated current, such as during a short circuit condition, the module will work on intermittent mode, also can tolerate short circuit conditions infinitely. When the over-current condition is removed, the converter will automatically restart.

Output Filters

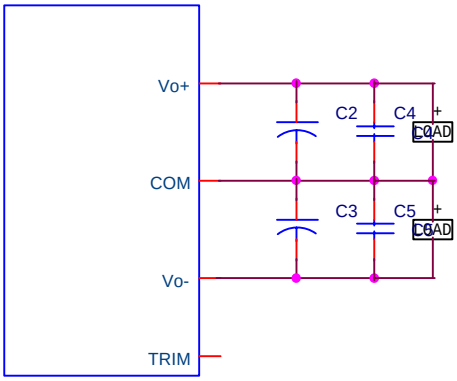
When the load is sensitive to ripple and noise, an output filter can be added to minimize the effects. A simple output filter to reduce output ripple and noise can be made by connecting a capacitor C2 and C3 across the output as shown in Figure 'Output Ripple Filter'. The recommended value for the output capacitor C2&C3 is 220μF.



Output Ripple Filter

Extra care should be taken when long leads or traces are used to provide power to the load. Long lead length increases the chance for noise to appear on the lines. Under these

conditions Capacitor can be added across the load, with a 0.47µF ceramic capacitor C4 and C5 in parallel generally as shown in Figure 'Output Ripple Filter for a Distant Load'.



Output Ripple Filter for a Distant Load

Decoupling

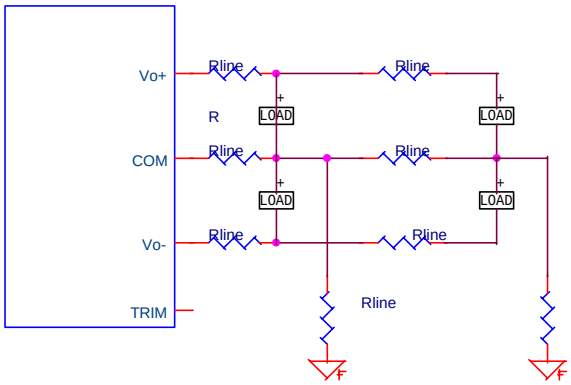
Noise on the power distribution system is not always created by the converter. High speed analog or digital loads with dynamic power demands can cause noise to cross the power inductor back onto the input lines. Noise can be reduced by decoupling the load. In most cases, connecting a 10µF tantalum or ceramic capacitor in parallel with a 0.1µF ceramic capacitor across the load will decouple it. The capacitors should be connected as close to the load as possible.

Output Over-Voltage Protection

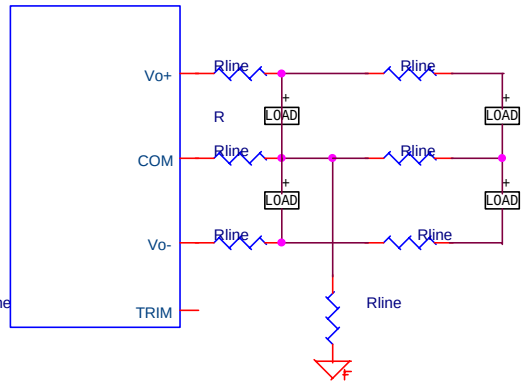
The over-voltage protection has a separate feedback loop, which activates when the output voltage is between 120% and 140% of the nominal output voltage.

Ground Loops

Ground loops occur when different circuits are given multiple paths to common or earth ground, as shown in Figure 'Ground Loops'. Multiple ground points can slightly different potential and cause current flow through the circuit from one point to another. This can result in additional noise in all the circuits. To eliminate the problem, circuits should be designed with a single ground connection as shown in Figure 'Single Point Ground'.



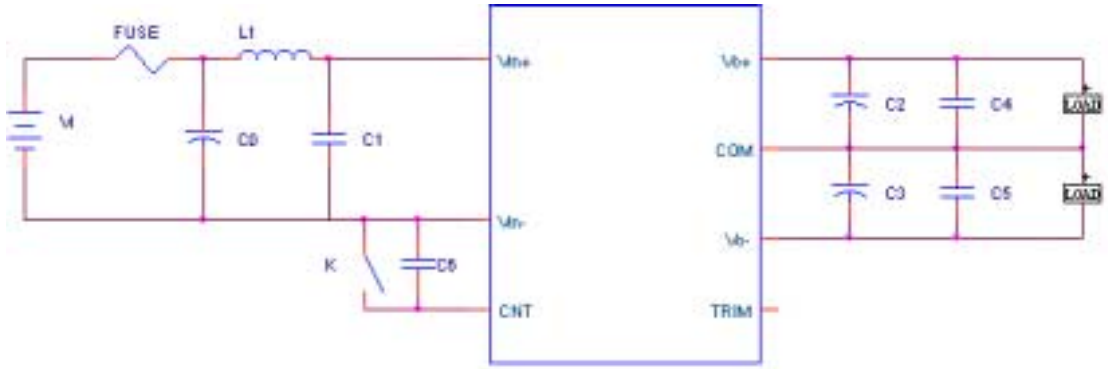
Ground loops



Single Point

Design Consideration

Typical Application



Fuse:2A

Co Recommended:47uF/100Velectrolytic type capacitor

C1 Recommended:0.47uF/100V capacitor

C2 C3 Recommended:220uF electrolytic type capacitor(ESR≤600mΩ at 100KHz)

C4 C5 Recommended: 0.47uF

C6 Recommended:1000pF

L1 Recommended:10-12uH

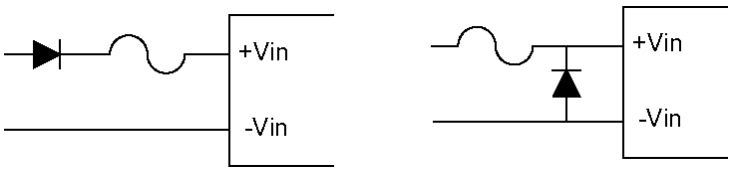
Fusing

The ALT power modules have no internal fuse. An external fuse must always be employed! To meet international safety requirements, a 250 Volt rated fuse should be used. If one of the input lines is connected to chassis ground, then the fuse must be placed in the other input line.

Standard safety agency regulations require input fusing. Recommended fuse ratings for the ALT Series are 2A.

Input Reverse Voltage Protection

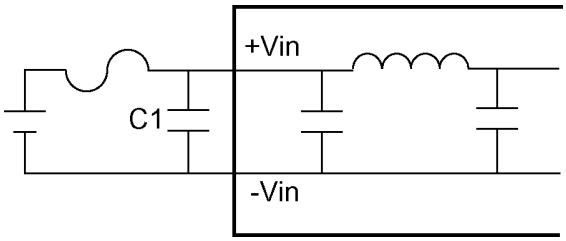
Under installation and cabling conditions where reverse polarity across the input may occur, reverse polarity protection is recommended. Protection can easily be provided as shown in Figure 'Reverse Polarity Protection Circuit'. In both cases the diode used is rated for 3A/100V. Placing the diode across the inputs rather than in-line with the input offers an advantage in that the diode only conducts in a reverse polarity condition, which increases circuit efficiency and thermal performance.



Reverse Polarity Protection Circuit

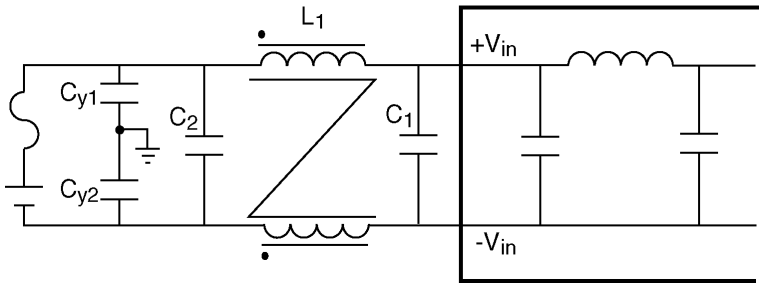
EMC

Input filters are included in the converters to help achieve standard system emissions certifications. Some users however, may find that additional input filtering is necessary. The ALT series has an internal switching frequency of 330 kHz so a high frequency capacitor mounted close to the input terminals produces the best results. To reduce reflected noise, a capacitor can be added across the input as shown in Figure 'Ripple Rejection Input Filter', forming a π filter. A 0.47µF/100V electrolytic capacitor is recommended for C1.



Ripple Rejection Input Filter

For conditions where EMI is a concern, a different input filter can be used. Figure 'EMI Reduction Input Filter' shows an input filter designed to reduce EMI effects. L1 is an 8mH common mode inductor, C1 is a 47µF/100V electrolytic capacitor, and C2 is a 0.47µF/100V metal film or ceramic high frequency capacitor, and Cy1 and Cy2 are each 4700pF high frequency ceramic capacitors.



EMI Reduction Input Filter

When a filter inductor is connected in series with the power converter input, an input capacitor C1 should be added. An input capacitor C1 should also be used when the input wiring is long, since the wiring can act as an inductor. Failure to use an input capacitor under these conditions can produce large input voltage spikes and an unstable output.

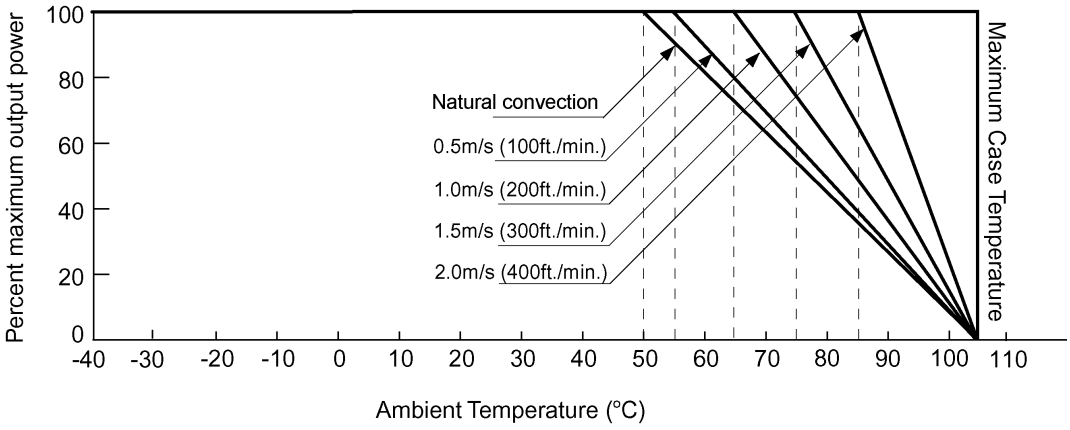
Safety Consideration

For safety-agency approval of the system in which the power module is used, the power module must be installed in compliance with the spacing and separation requirements of the end-use safety agency standard, i.e., UL1950, CSA C22.2 No. 950-95, and EN60950. The ALT series input-to-output isolation is a basic insulation. The module should be installed in end-use equipment, in compliance with the requirements of the ultimate application, and is intended to be supplied by an isolated secondary circuit. When the supply to the DC/DC power module meets all the requirements for SELV(<$60V_{DC}$), the output is considered to remain within SELV limits (level 3). If connected to a $60V_{DC}$ power system, double or reinforced insulation must be provided in the power supply that isolates the input from any hazardous voltages, including the ac mains. One Vinput pin and one Voutput pin are to be grounded or both the input and output pins are to be kept floating. Single fault testing in the power supply must be performed in combination with the DC/DC power module to demonstrate that the output meets the requirement for SELV. The input pins of the module are not operator accessible.

Note: Do not ground either of the input pins of the module without grounding one of the output pins. This may allow a non-SELV voltage to appear between the output pin and ground.

Thermal Consideration

When 48V input, 55℃ ambient temperature, and 200LFM airflow, ALT series are rated for full power, and in this condition the case temperature can reach 100℃. For operation above ambient temperature of 55℃, output power must be derated as shown in Figure 'Temperature Derating', meantime, airflow at least 200LFM over the converter must be provided to make the module working properly. the case temperature should be used to determine maximum temperature limits. The minimum operating temperature for the ALT is -40℃.



Temperature Derating Curves

MTBF

The MTBF, calculated in accordance with Bellcore TR-NWT-000332 is 2,000,000 hours. Obtaining this MTBF in practice is entirely possible. If the ambient air temperature is expected to exceed +25℃, then we also advise an oriented for the best possible cooling in the air stream.

Emerson Network Power can supply replacements for converters from other manufacturers, or offer custom solutions. Please contact the factory for details.

Mechanical Considerations

Installation

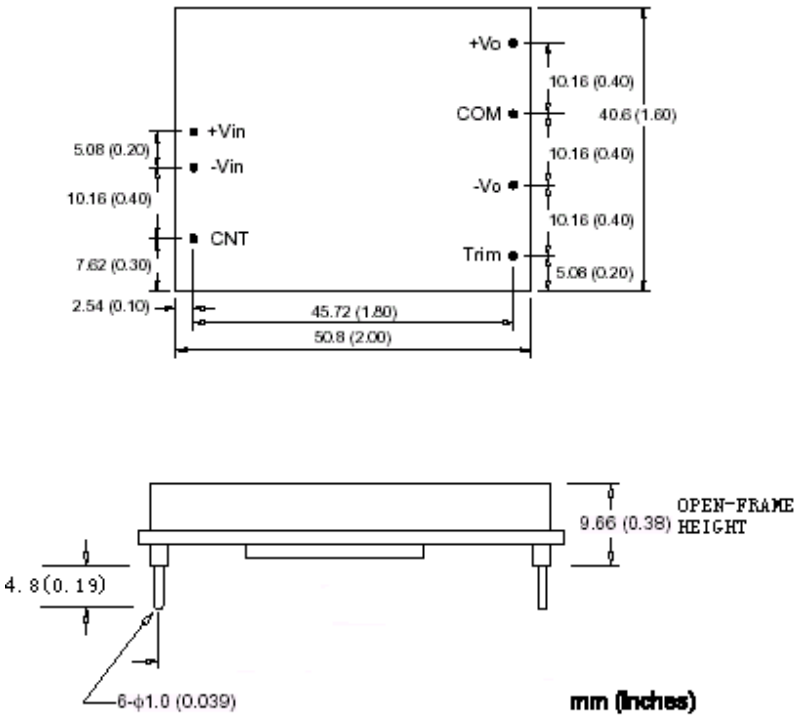
Although ALT series converters can be mounted in any orientation, free air-flowing must be taken. Normally power components are always put at the end of the airflow path or have the separate airflow paths. This can keep other system equipment cooler and increase component life spans.

Soldering

ALT series converters are compatible with standard wave soldering techniques. When wave soldering, the converter pins should be preheated for 20-30 seconds at 110℃, and wave soldered at 260℃ for less than 10 seconds.

When hand soldering, the iron temperature should be maintained at 425℃ and applied to the converter pins for less than 5 seconds. Longer exposure can cause internal damage to the converter. Cleaning can be performed with cleaning solvent IPA or with water.

Mechanical Chart



(pin side view)

No supporting point

*: Pin length

Default: 5mm±0.5mm (0.228in.±0.02in.)

Product name with suffix “-7”: 5.8mm±0.5mm (0.189in.±0.02in.)

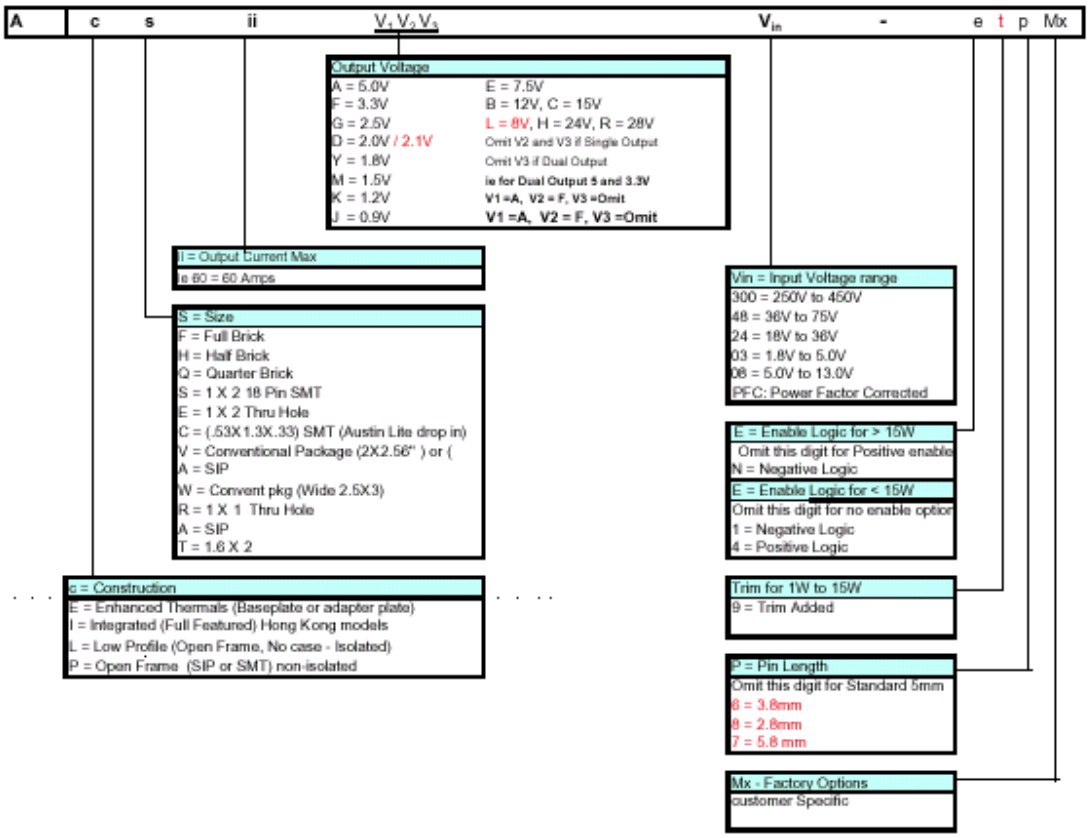
Product name with suffix “-6”: 3.8mm±0.25mm (0.15in.±0.01in.)

Product name with suffix “8”: 2.8mm±0.25mm (0.11in.±0.01in.)

Ordering Information

Model Number	Input Voltage (V_{DC})	Output Voltage (V_{DC})	Output Current (A)	Ripple (mV rms) typ.	Noise (mV pp) typ.	Efficiency % typ.
ALT03AA48	36-75	±5	3	30	150	84
ALT01BB48	36-75	±12	1.25	25	150	87

Model Numbering



Note: For some products, they may not conform with the NEW PART NUMBER DESCRIPTION above absolutely.