



# SPN65T10

## N-Channel Enhancement Mode MOSFET

### DESCRIPTION

The SPN65T10 is the N-Channel enhancement mode power field effect transistor which is produced using high cell density DMOS trench technology. This high density process is especially tailored to minimize on-state resistance. These devices are particularly suitable for synchronous rectifier application, notebook computer power management and other battery powered circuits.

### FEATURES

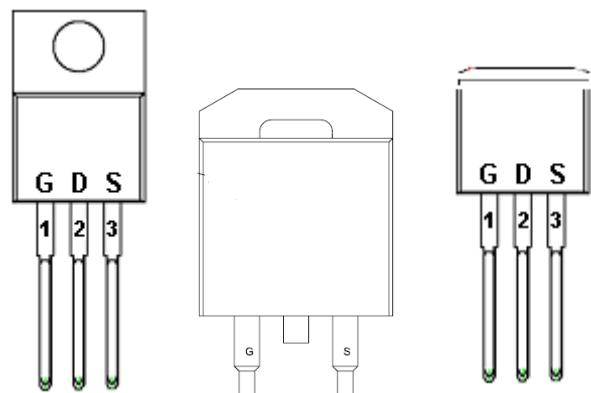
- ◆ 100V/68A,  $R_{DS(ON)} = 14m\Omega$  @  $V_{GS} = 10V$
- ◆ Super high density cell design for extremely low  $R_{DS(ON)}$
- ◆ Exceptional on-resistance and maximum DC current capability
- ◆ TO-220-3L/TO-263-2L/TO-262-3L package design

### APPLICATIONS

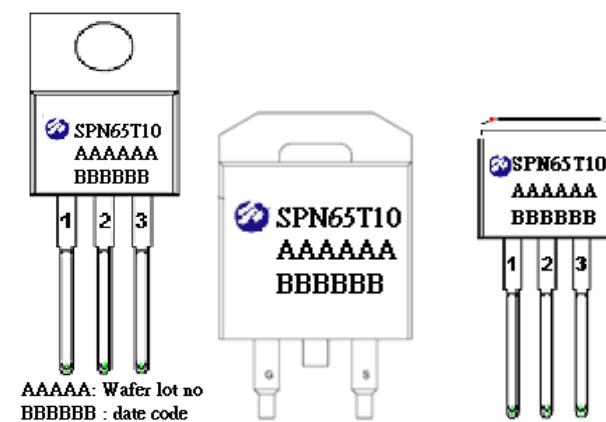
- DC/DC Converter
- Load Switch
- SMPS Secondary Side Synchronous Rectifier

### PIN CONFIGURATION

TO-220-3L      TO-263-2L      TO-262-3L



PART MARKING  
TO-220-3L      TO-263-2L      TO-262-3L





# SPN65T10

## N-Channel Enhancement Mode MOSFET

### PIN DESCRIPTION

Pin	Symbol	Description
1	G	Gate
2	D	Drain
3	S	Source

### ORDERING INFORMATION

Part Number	Package	Part Marking
SPN65T10T220TGB	TO-220-3L	SPN65T10
SPN65T10T262RGB	TO-263-2L	SPN65T10
SPN65T10K262TGB	TO-262-3L	SPN65T10

- ※ SPN65T10T220TGB : Tube ; Pb – Free ; Halogen - Free
- ※ SPN65T10T262RGB : Tape&Reel ; Pb – Free ; Halogen - Free
- ※ SPN65T10K262TGB : Tube ; Pb – Free ; Halogen - Free

### ABSOULTE MAXIMUM RATINGS

(TA=25°C Unless otherwise noted)

Parameter	Symbol	Typical	Unit
Drain-Source Voltage	V <sub>DSS</sub>	100	V
Gate –Source Voltage	V <sub>GSS</sub>	±20	V
Continuous Drain Current(T <sub>J</sub> =150°C)	T <sub>A</sub> =25°C	68	A
	T <sub>A</sub> =70°C		
Pulsed Drain Current	I <sub>DM</sub>	260	A
Power Dissipation	T <sub>A</sub> =25°C	125	W
	T <sub>A</sub> =70°C		
Avalanche Energy with Single Pulse ( T <sub>j</sub> =25°C , L = 1mH , I <sub>AS</sub> = 22A , V <sub>DS</sub> =100V. )	E <sub>AS</sub>	240	mJ
Operating Junction Temperature	T <sub>J</sub>	-55/150	°C
Storage Temperature Range	T <sub>STG</sub>	-55/150	°C
Thermal Resistance-Junction to Ambient	R <sub>θJA</sub>	62.5	°C/W



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### ELECTRICAL CHARACTERISTICS

(TA=25°C Unless otherwise noted)

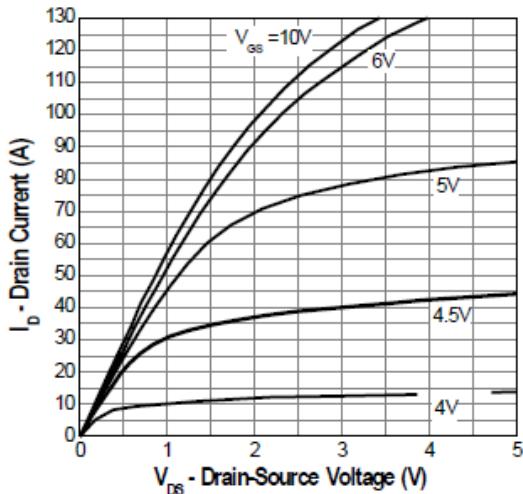
Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	100			V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	2.0		4.0	
Gate Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V			±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =100V, V <sub>GS</sub> =0V V <sub>DS</sub> =80V, V <sub>GS</sub> =0V T <sub>J</sub> = 150 °C			10 100	uA
Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> =45A			14	mΩ
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =45A, V <sub>GS</sub> =0V			1.3	V
<b>Dynamic</b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =80V, V <sub>GS</sub> =4.5V I <sub>D</sub> = 30A		57		nC
Gate-Source Charge	Q <sub>gs</sub>			12		
Gate-Drain Charge	Q <sub>gd</sub>			17.5		
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V f=1MHz		2920		pF
Output Capacitance	C <sub>oss</sub>			261		
Reverse Transfer Capacitance	C <sub>rss</sub>			162		
Turn-On Time	t <sub>d(on)</sub>	V <sub>DD</sub> =50V, R <sub>L</sub> =1.6Ω I <sub>D</sub> =30A, V <sub>GEN</sub> =10V R <sub>G</sub> =10Ω		15		nS
	t <sub>r</sub>			13		
Turn-Off Time	t <sub>d(off)</sub>			55		
	t <sub>f</sub>			21		



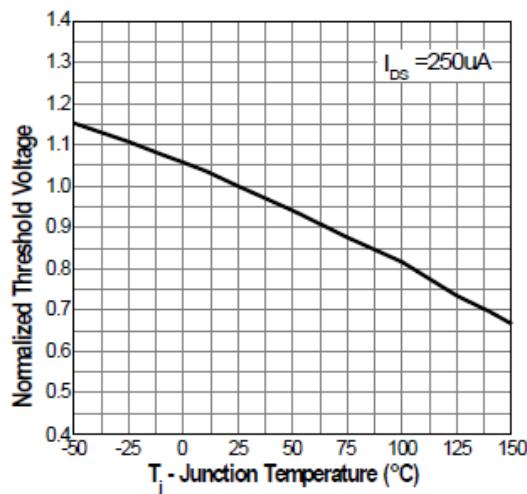
# SPN65T10 N-Channel Enhancement Mode MOSFET

## TYPICAL CHARACTERISTICS

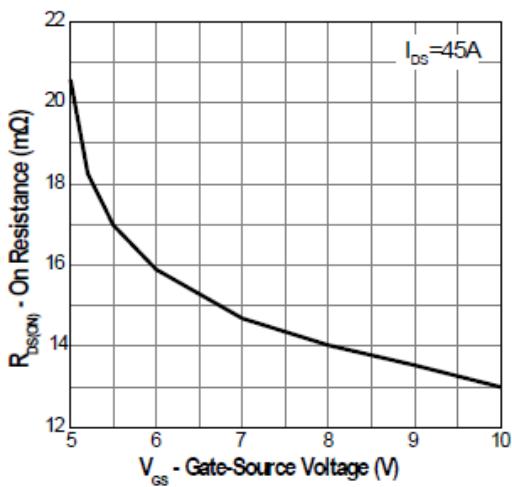
Output Characteristics



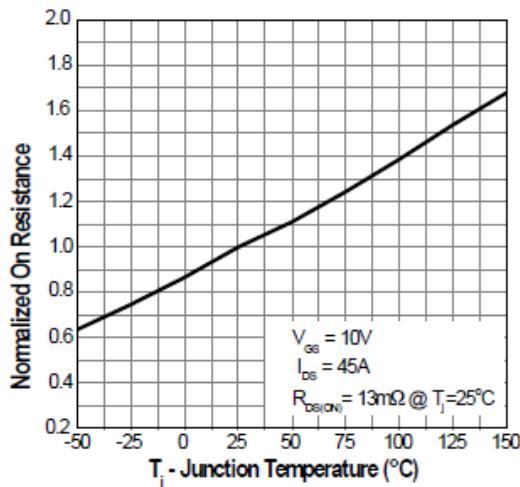
Gate Threshold Voltage vs. Temperature



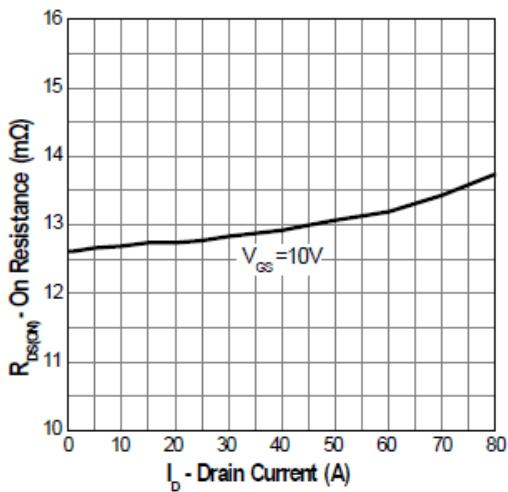
On-Resistance vs. Gate-Source Voltage



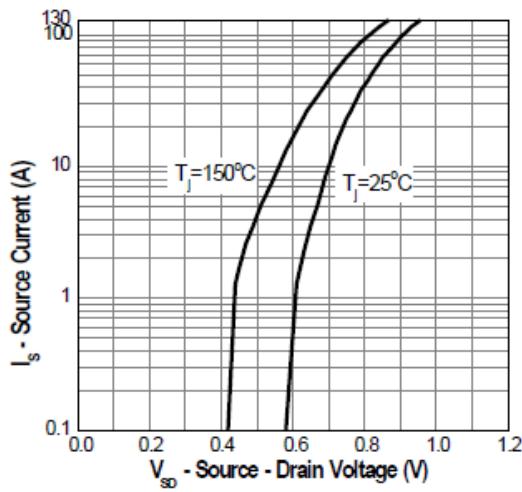
On-Resistance vs. Temperature



On-Resistance vs. Drain Current



Source-Drain Diode Forward Characteristics



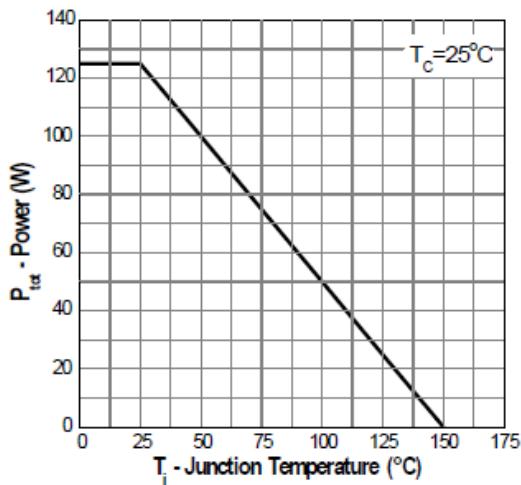


# SPN65T10

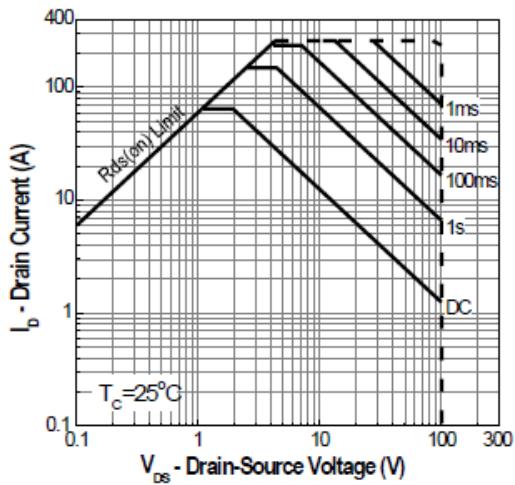
## N-Channel Enhancement Mode MOSFET

### TYPICAL CHARACTERISTICS

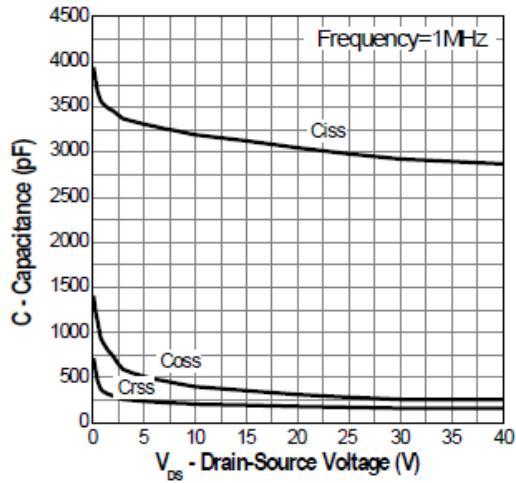
#### Power Dissipation



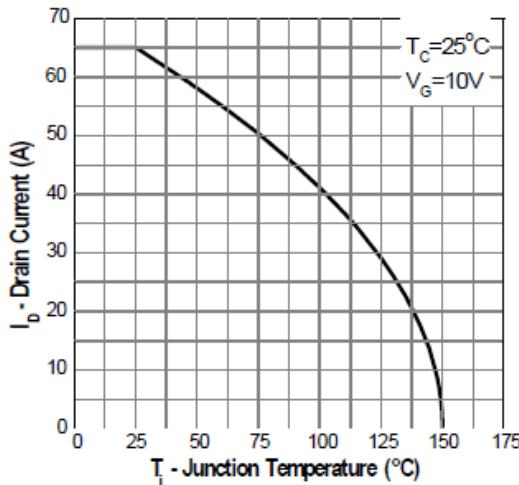
#### Safe Operation Area



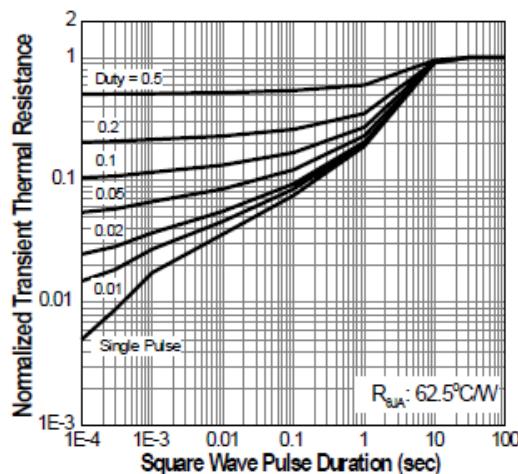
#### Capacitance Characteristics



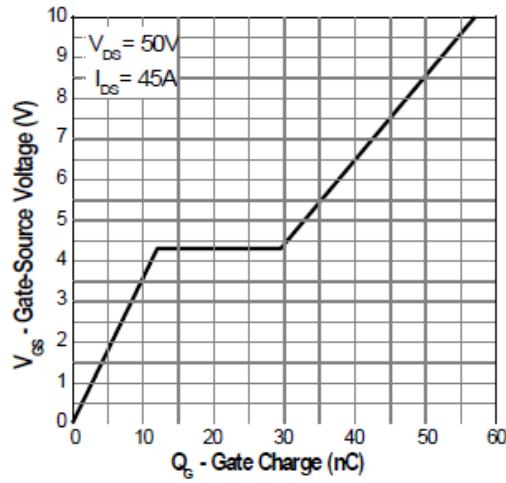
#### Drain Current vs. Temperature



#### Transient Thermal Impedance



#### Gate-Charge Characteristics

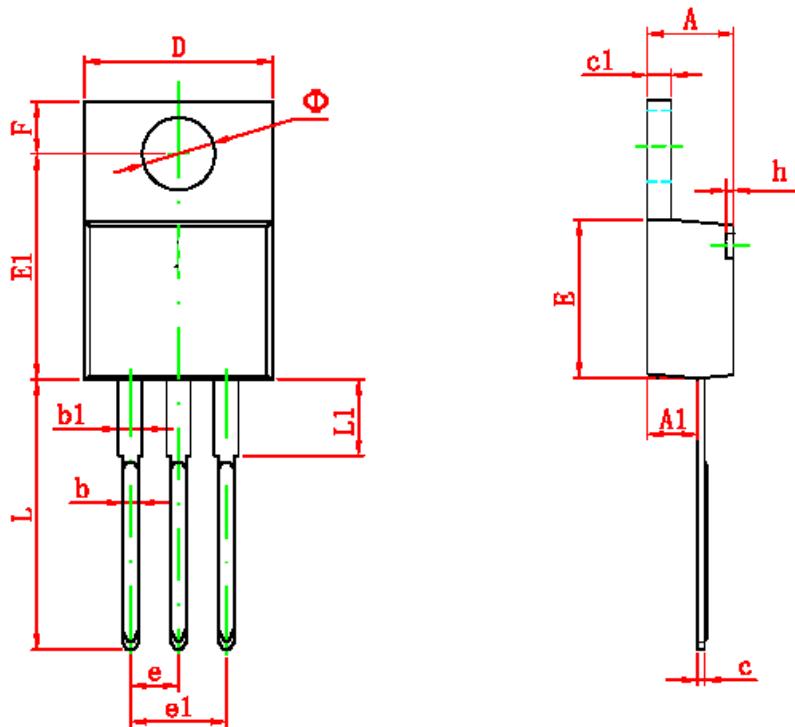




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## N-Channel Enhancement Mode MOSFET

### TO-220-3L PACKAGE OUTLINE



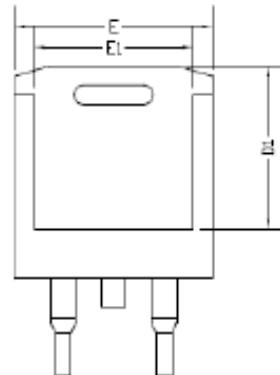
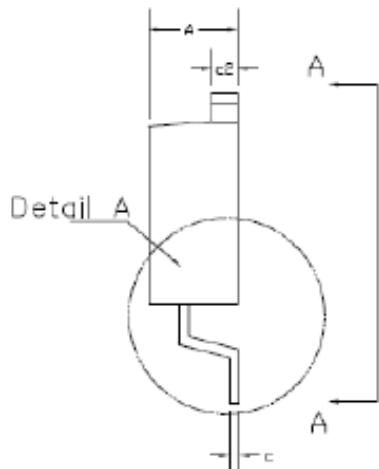
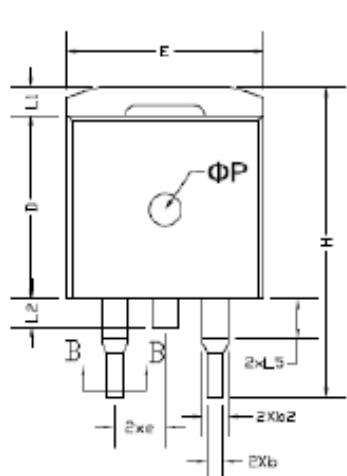
Symbol	Millimeter		Inch	
	Min	Max	Min	Max
A	4.4	4.6	0.173	0.181
A1	2.23	2.53	0.088	0.100
b2	0.75	0.85	0.030	0.033
b1	1.17	1.42	0.046	0.056
c2	0.4	0.6	0.016	0.024
c1	1.2	1.4	0.047	0.055
D	9.85	10.15	0.388	0.400
E	8.96	9.46	0.353	0.372
E1	15.5	15.95	0.610	0.628
e	2.54REF		0.1REF	
e1	5.08REF		0.2REF	
F	2.7	2.9	0.106	0.114
h	0	0.3	0.000	0.012
L	12.7	13.65	0.500	0.537
L1			0.126	



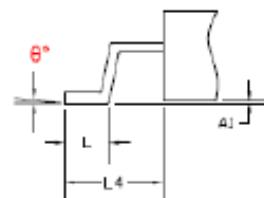
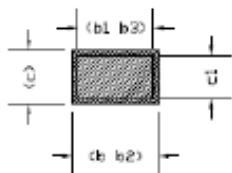
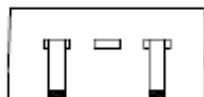
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## N-Channel Enhancement Mode MOSFET

### TO-263-2L PACKAGE OUTLINE



View A-A



Lead tip

Section B-B

Detail A

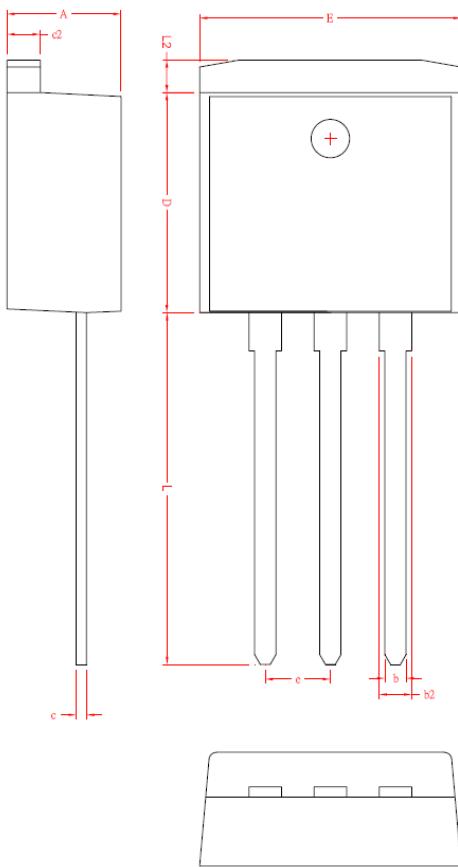
TO-263 Dimension									
Symbol	Millimeters		Inches		Symbol	Millimeters		Inches	
	Min	Max	Min	Max		Min	Max	Min	Max
A	4.400	4.600	0.173	0.181	E1	7.850	8.150	0.309	0.321
A1	0.010	0.200	0.000	0.008	e	2.540REF		0.100REF	
b	0.750	0.850	0.030	0.033	L	2.350	2.750	0.092	0.108
b2	1.170	1.450	0.046	0.057	L1	4.850	5.150	0.187	0.203
c	0.400	0.600	0.016	0.024	L3	1.200	1.600	0.047	0.062
c2	1.200	1.400	0.047	0.055	L4	0.700	1.400	0.051	0.058
D	8.950	9.450	0.352	0.372	L5	0.000	3.200	0.000	0.126
D1	8.000	8.400	0.315	0.331	H	15.450	15.850	0.000	0.126
E	9.850	10.150	0.388	0.400	ΦP	1.000	2.500	0.039	0.098
θ°	0	8	--	--	--	--	--	--	--



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### TO-262-3L PACKAGE OUTLINE



Symbol	Millimeter		Inch	
	Min	Max	Min	Max
A	4.4	4.8	0.173	0.189
b	0.76	1	0.030	0.039
D	8.6	9	0.339	0.354
c	0.36	0.5	0.014	0.020
E	9.8	10.4	0.386	0.409
c2	1.25	1.45	0.049	0.057
b2	1.17	1.47	0.046	0.058
L	13.25	14.25	0.522	0.561
e	2.54REF		0.1REF	
L2	1.27REF		0.05REF	



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