

# H5N3007FL-M0

300V - 15A - MOS FET  
High Speed Power Switching

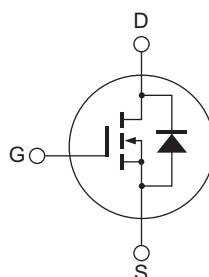
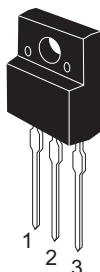
R07DS0995EJ0100  
Rev.1.00  
Jan 09, 2013

## Features

- Low on-resistance  
 $R_{DS(on)} = 0.12 \Omega$  typ. (at  $I_D = 7.5 \text{ A}$ ,  $V_{GS} = 10 \text{ V}$ ,  $T_a = 25^\circ\text{C}$ )
- Low leakage current
- High speed switching
- Built-in fast recovery diode

## Outline

RENESAS Package code: PRSS0003AF-A  
(Package name: TO-220FL)



1. Gate
2. Drain
3. Source

## Absolute Maximum Ratings

( $T_a = 25^\circ\text{C}$ )

Item	Symbol	Ratings	Unit
Drain to source voltage	$V_{DSS}$	300	V
Gate to source voltage	$V_{GSS}$	$\pm 30$	V
Drain current	$I_D$	15	A
Drain peak current	$I_{D(pulse)}$ <sup>Note1</sup>	60	A
Body-drain diode reverse drain current	$I_{DR}$	15	A
Body-drain diode reverse drain peak current	$I_{DR(pulse)}$ <sup>Note1</sup>	60	A
Avalanche current	$I_{AP}$ <sup>Note3</sup>	15	A
Avalanche energy	$E_{AR}$ <sup>Note3</sup>	13.5	mJ
Channel to case thermal impedance	$\theta_{ch-c}$	3.57	$^\circ\text{C}/\text{W}$
Channel dissipation	$P_{ch}$ <sup>Note2</sup>	35	W
Channel temperature	$T_{ch}$	150	$^\circ\text{C}$
Storage temperature	$T_{stg}$	-55 to +150	$^\circ\text{C}$

- Notes: 1.  $PW \leq 10 \mu\text{s}$ , duty cycle  $\leq 1\%$   
 2. Value at  $T_c = 25^\circ\text{C}$   
 3.  $ST_{ch} = 25^\circ\text{C}$ ,  $T_{ch} \leq 150^\circ\text{C}$

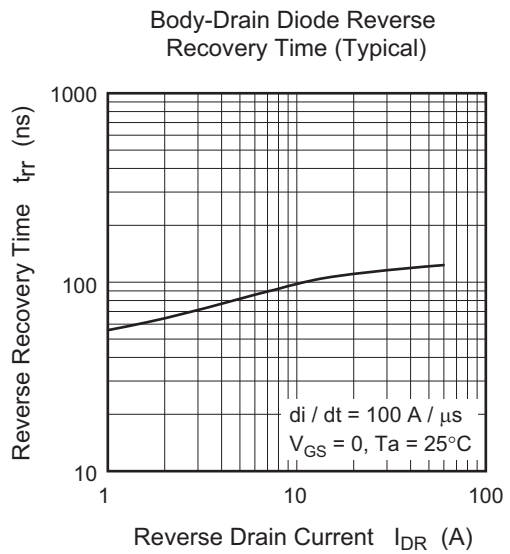
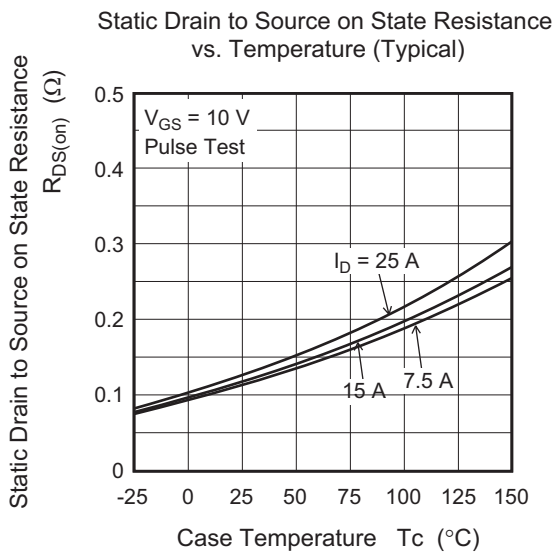
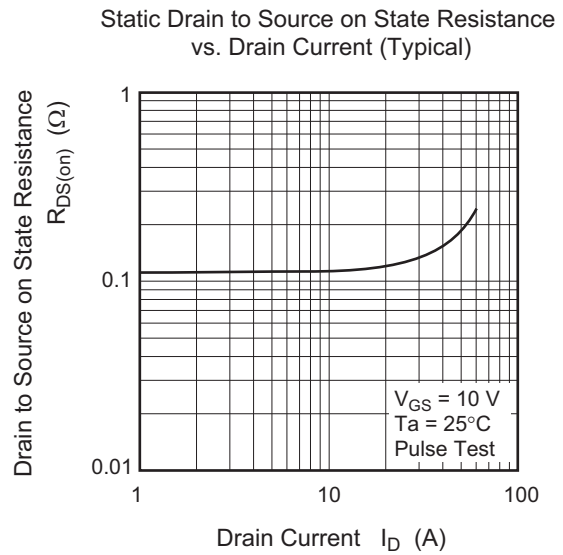
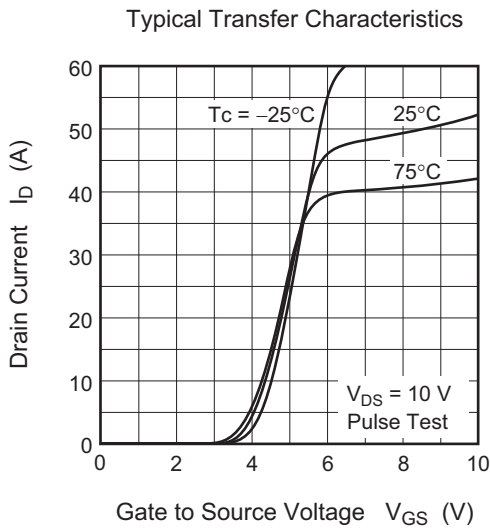
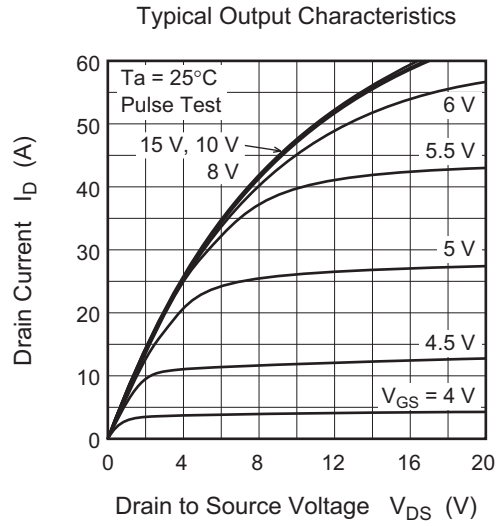
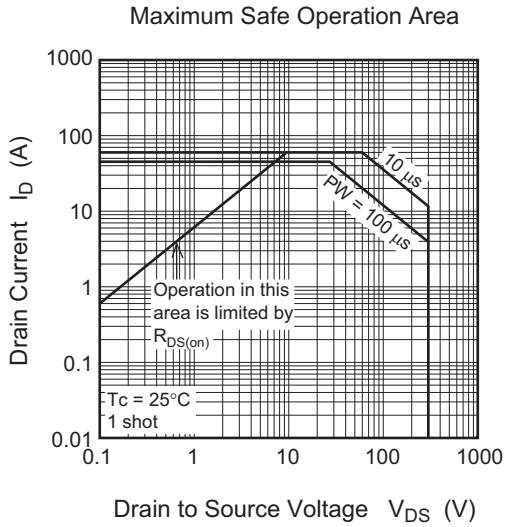
## Electrical Characteristics

(Ta = 25°C)

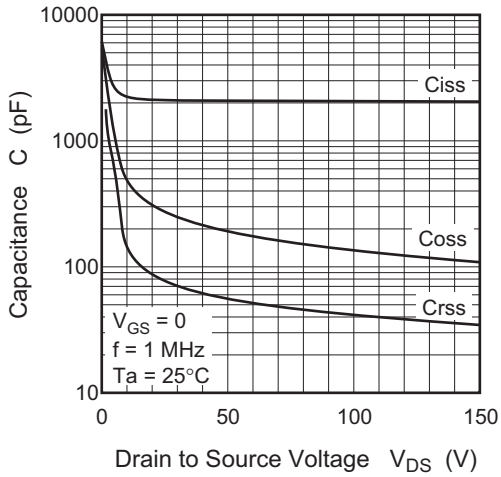
Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	300	—	—	V	$I_D = 10 \text{ mA}$ , $V_{GS} = 0$
Zero gate voltage drain current	$I_{DSS}$	—	—	10	$\mu\text{A}$	$V_{DS} = 300 \text{ V}$ , $V_{GS} = 0$
Gate to source leak current	$I_{GSS}$	—	—	$\pm 0.1$	$\mu\text{A}$	$V_{GS} = \pm 30 \text{ V}$ , $V_{DS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.5	—	4.0	V	$V_{DS} = 10 \text{ V}$ , $I_D = 1 \text{ mA}$
Forward transfer admittance	$ y_{fs} $	9	15	—	S	$I_D = 7.5 \text{ A}$ , $V_{DS} = 10 \text{ V}$ <sup>Note4</sup>
Static drain to source on state resistance	$R_{DS(on)}$	—	0.12	0.16	$\Omega$	$I_D = 7.5 \text{ A}$ , $V_{GS} = 10 \text{ V}$ <sup>Note4</sup>
Input capacitance	$C_{iss}$	—	2180	—	pF	$V_{DS} = 25 \text{ V}$
Output capacitance	$C_{oss}$	—	275	—	pF	$V_{GS} = 0$
Reverse transfer capacitance	$C_{rss}$	—	77	—	pF	$f = 1 \text{ MHz}$
Turn-on delay time	$t_{d(on)}$	—	35	—	ns	$I_D = 7.5 \text{ A}$
Rise time	$t_r$	—	50	—	ns	$V_{GS} = 10 \text{ V}$
Turn-off delay time	$t_{d(off)}$	—	160	—	ns	$R_L = 20 \Omega$
Fall time	$t_f$	—	40	—	ns	$R_g = 10 \Omega$
Total gate charge	$Q_g$	—	80	—	nC	$V_{DD} = 240 \text{ V}$
Gate to source charge	$Q_{gs}$	—	10	—	nC	$V_{GS} = 10 \text{ V}$
Gate to drain charge	$Q_{gd}$	—	40	—	nC	$I_D = 15 \text{ A}$
Body-drain diode forward voltage	$V_{DF}$	—	0.85	1.30	V	$I_F = 15 \text{ A}$ , $V_{GS} = 0$ <sup>Note4</sup>
Body-drain diode reverse recovery time	$t_{rr}$	—	110	—	ns	$I_F = 15 \text{ A}$ , $V_{GS} = 0$ $di_F/dt = 100 \text{ A}/\mu\text{s}$

Notes: 4. Pulse test

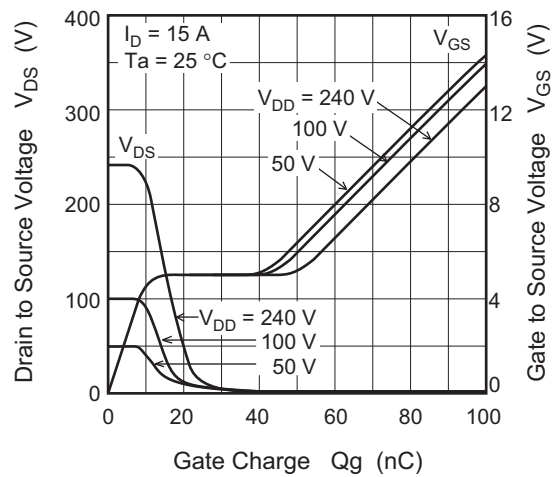
Main Characteristics



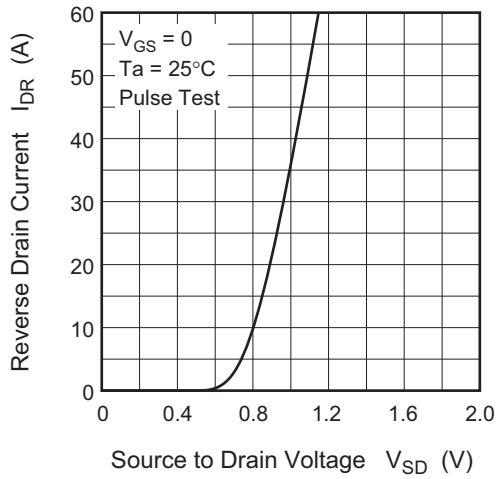
Typical Capacitance vs. Drain to Source Voltage



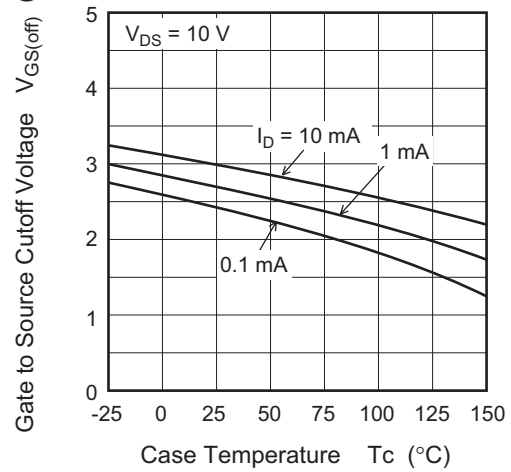
Dynamic Input Characteristics (Typical)



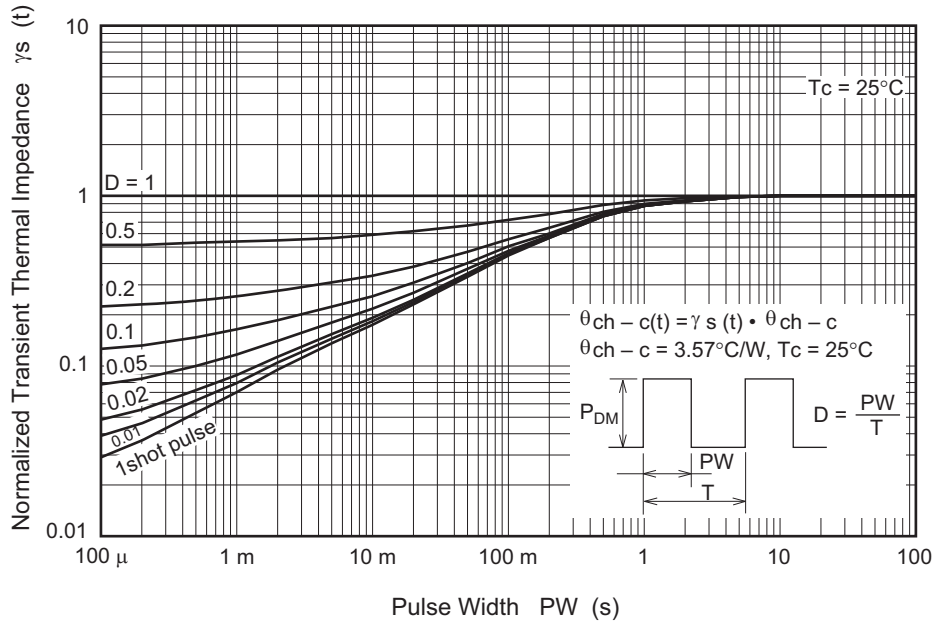
Reverse Drain Current vs. Source to Drain Voltage (Typical)



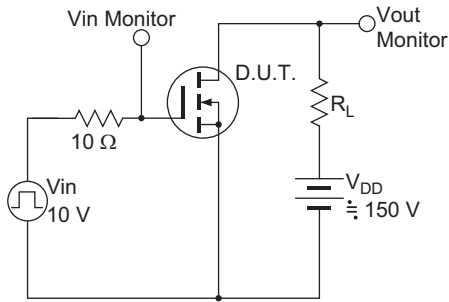
Gate to Source Cutoff Voltage vs. Case Temperature (Typical)



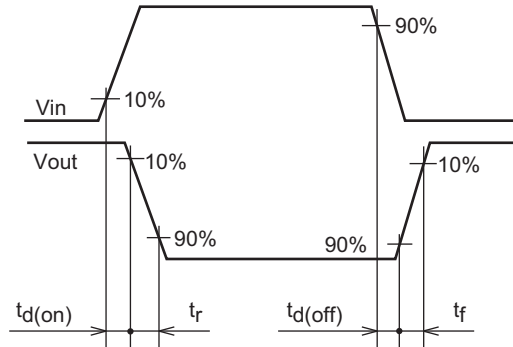
Normalized Transient Thermal Impedance vs. Pulse Width



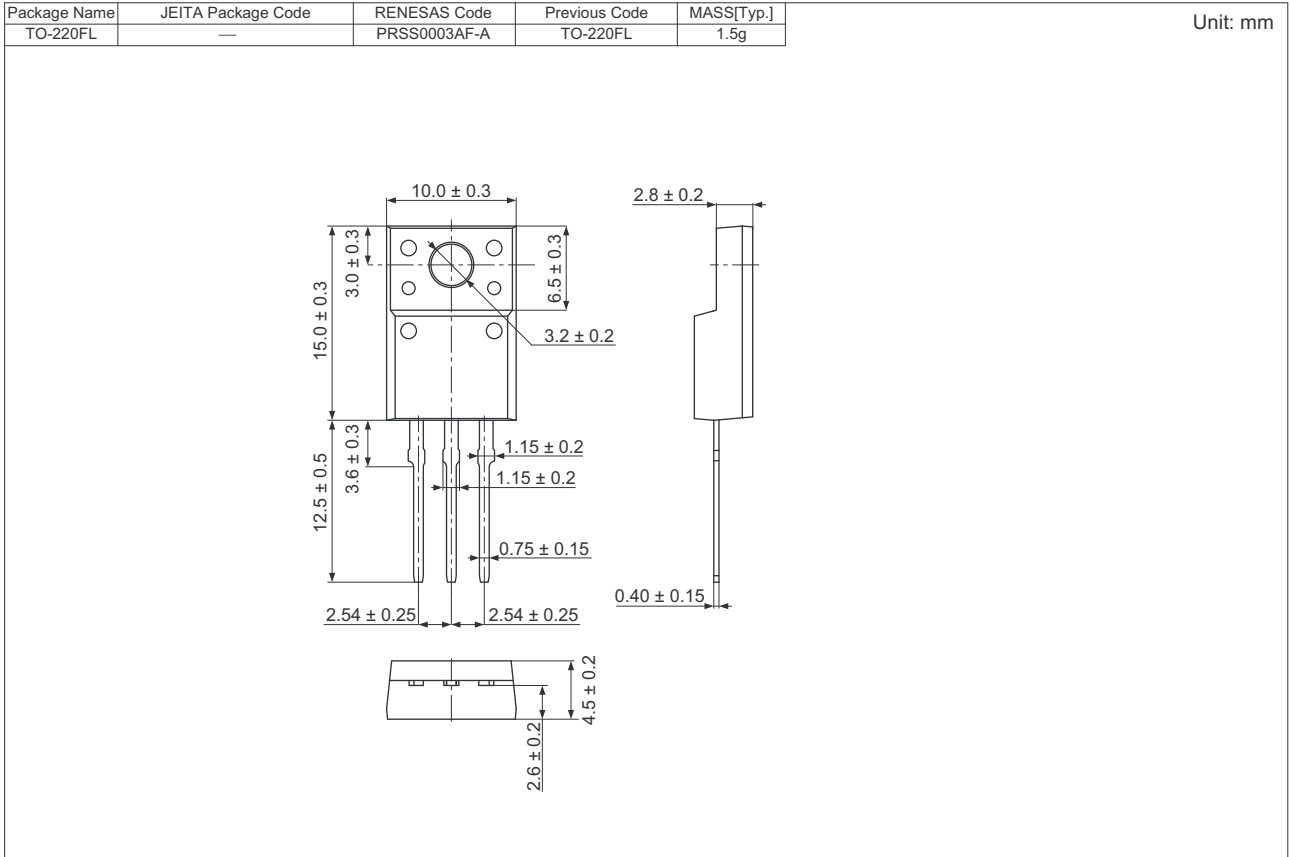
Switching Time Test Circuit



Waveform



### Package Dimensions



### Ordering Information

Orderable Part Number	Quantity	Shipping Container
H5N3007FL-M0-E#T2	50 pcs	Tube

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2880 Scott Boulevard Santa Clara, CA 95050-2554, U.S.A.  
Tel: +1-408-588-6000, Fax: +1-408-588-6130

**Renesas Electronics Canada Limited**  
1101 Nicholson Road, Newmarket, Ontario L3Y 9C3, Canada  
Tel: +1-905-898-5441, Fax: +1-905-898-3220

**Renesas Electronics Europe Limited**  
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.  
Tel: +44-1628-651-700, Fax: +44-1628-651-804

**Renesas Electronics Europe GmbH**  
Arcadiastrasse 10, 40472 Düsseldorf, Germany  
Tel: +49-211-65030, Fax: +49-211-6503-1327

**Renesas Electronics (China) Co., Ltd.**  
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Tel: +86-10-8235-1155, Fax: +86-10-8235-7679

**Renesas Electronics (Shanghai) Co., Ltd.**  
Unit 204, 205, AZIA Center, No.1233 Lujiazui Ring Rd., Pudong District, Shanghai 200120, China  
Tel: +86-21-5877-1818, Fax: +86-21-6887-7858 / -7898

**Renesas Electronics Hong Kong Limited**  
Unit 1601-1613, 16/F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong  
Tel: +852-2886-9318, Fax: +852 2886-9022/9044

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Tel: +60-3-7955-3390, Fax: +60-3-7955-9510

**Renesas Electronics Korea Co., Ltd.**  
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Tel: +82-2-558-3737, Fax: +82-2-558-5141