TY Semicondutor®

Product specification

2SK3484

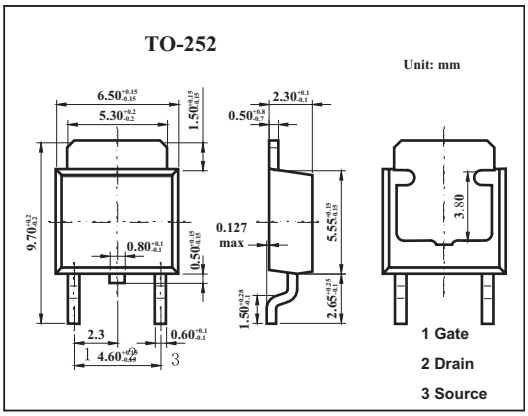
■ Features

- Super low on-state resistance:

  $R_{DS(on)1}$ = 125mΩ MAX. (V_{GS} = 10 V, I_D = 8A)

  $R_{DS(on)2}$ = 148mΩ MAX. (V_{GS} = 4.5 V, I_D = 8A)

- Low C_{iss}: C_{iss} = 900 pF TYP.



■ Absolute Maximum Ratings Ta = 25℃

Parameter	Symbol	Rating	Unit
Drain to source voltage	V_{DSS}	100	V
Gate to source voltage	V_{GSS}	±20	V
Drain current	I_D	±16	A
	I_{dp} *	±22	A
Power dissipation T_C=25℃ T_A=25℃	P_D	30 1.0	W
Channel temperature	T_{ch}	150	℃
Storage temperature	T_{stg}	-55 to +150	℃

* PW≤10μs,Duty Cycle≤1%

■ Electrical Characteristics Ta = 25℃

Parameter	Symbol	Testconditons	Min	Typ	Max	Unit
Drain cut-off current	I_{DSS}	V_{DS}=100V,V_{GS}=0			10	μA
Gate leakage current	I_{GSS}	V_{GS}=±20V,V_{DS}=0			±10	μA
Gat cutoff voltage	$V_{GS(off)}$	V_{DS}=10V,I_D=1mA	1.5	2.0	2.5	V
Forward transfer admittance	$\|Y_{fs}\|$	V_{DS}=10V,I_D=8A	4.5	9.5		S
Drain to source on-state resistance	$R_{DS(on)1}$	V_{GS}=10V,I_D=8A		100	125	mΩ
	$R_{DS(on)2}$	V_{GS}=4.5V,I_D=8A		110	148	mΩ
Input capacitance	C_{iss}	V_{DS}=10V,V_{GS}=0,f=1MHZ		900		pF
Output capacitance	C_{oss}			110		pF
Reverse transfer capacitance	C_{rss}			50		pF
Turn-on delay time	t_{on}	I_D=8A,$V_{GS(on)}$=10V,R_G=0Ω,V_{DD}=50V		9.0		ns
Rise time	t_r			5.0		ns
Turn-off delay time	t_{off}			30		ns
Fall time	tf			4.0		ns
Total Gate Charge	Q_G	I_D =16A, V_{DD} =80V, V_{GS} = 10 V		20		nC
Gate to Source Charge	Q_{GS}			3.0		nC
Gate to Drain Charge	Q_{GD}			5.0		nC