TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7S66F,TC7S66FU

Bilateral Switch

The TC7S66 is a high Speed C^2MOS Bilateral Switch fabricated with silicon gate C^2MOS technology.

It consists of a high speed switch capable of controlling either digital or analog signals while maintaining the C^2MOS low power dissipation.

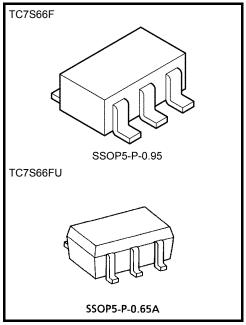
Control input (C) is provided to control the switch.

The switch turns ON while the C input is high, and the switch turns OFF while low.

Input is equipped with protection circuits against static discharge or transient excess voltage.

Features

- High speed: $t_{pd} = 7 \text{ ns (typ.)} @V_{CC} = 5 \text{ V}$
- Low power dissipation: $I_{CC} = 1 \mu A \text{ (max) } @Ta = 25^{\circ}C$
- High noise immunity: V_{NIH} = V_{NIL} = 28% V_{CC} (min)
- Low ON resistance: $R_{ON} = 100 \Omega$ (typ.) @ $V_{CC} = 9 V$
- Low T.H.D: THD = 0.05% (typ.) @V_{CC} = 5 V
- Pin and function compatible with TC4S66F



Weight

SSOP5-P-0.95 : 0.016 g (typ.) SSOP5-P-0.65A : 0.006 g (typ.)

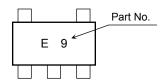
Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit	
DC Supply voltage	V_{CC}	–0.5 to 13	V	
Control input voltage	V _{IN}	-0.5 to V _{CC} + 0.5	٧	
Switch I/O voltage	V _{I/O}	-0.5 to V _{CC} + 0.5	V	
Control diode current	ICK	±20	mA	
Output diode current	lok	±20	mA	
Through I/O current	ΙΤ	±12.5	mA	
DC V _{CC} /ground current	Icc	±25	mA	
Power dissipation	PD	200	mW	
Storage temperature range	T _{stg}	-65 to 150	°C	
Lead temperature (10 s)	TL	260	°C	

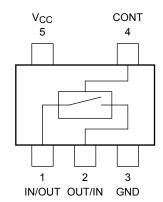
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

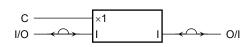
Marking



Pin Configuration (top view)



Logic Diagram



Truth Table

Control	Switch Function
Н	ON
L	OFF

Operating Ranges

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	2 to 12	V
Control input voltage	V _{IN}	0 to V _{CC}	٧
Switch I/O voltage	V _{I/O}	0 to V _{CC}	٧
Operating temperature range	T _{opr}	-40 to 85	°C
		0 to 1000 (V _{CC} = 2.0 V)	
Input rise and fall time	4 4.	0 to 500 (V _{CC} = 4.5 V)	ns
	t _r , t _f	0 to 400 (V _{CC} = 6.0 V)	115
		0 to 250 (V _{CC} = 10.0 V)	

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Electrical Characteristics

DC Electrical Characteristics

Characteristics Symbol Test Condition		Symbol Test Condition .		_	Ta = 25°C		Ta = -40 to 85°C		Unit	
		V _{CC} (V)	Min	Тур.	Max	Min	Max			
		V _{IHC}	_	2.0	1.5	_	_	1.5	_	
	High level			4.5	3.15	_	_	3.15	_	
	nigii ievei			9.0	6.3	_	_	6.3	_	
Control input				12.0	8.4	_	_	8.4	_	V
voltage				2.0	_	_	0.5	_	0.5	V
	Low level	V _{ILC}		4.5	_	_	1.35	_	1.35	
	LOW level	VILC	_	9.0	_	_	2.7	_	2.7	
				12.0	_	_	3.6	_	3.6	
ON resistance			V _{IN} = V _{IHC}	4.5		192	340	_	400	Ω
			$V_{I/O} = V_{CC}$ to GND $V_{I/O} \le 1$ mA	9.0		110	170	_	200	
		RON $V_{IN} = V_{IHC}$ $V_{I/O} = V_{CC} \text{ or GN}$ $V_{I/O} \le 1 \text{ mA}$	V \O ≥ 1 MA	12.0		90	160	_	180	
				2.0		320	_	_	_	
			V _{IN} = V _{IHC}	4.5		140	200	_	260	
			V _{I/O} ≤ 1 mA	9.0		100	150	_	190	
				12.0		90	140	_	180	
Input/output lea current (switch		l _{OFF}	$V_{OS} = V_{CC}$ or GND $V_{IS} = GND$ or V_{CC} $V_{IN} = V_{ILC}$	12.0	-	_	±100	_	±1000	nA
Switch input leacurrent (switch on, out		I _{IZ}	V _{OS} = V _{CC} or GND V _{IN} = V _{IHC}	12.0	_	_	±100	_	±1000	nA
Control input of	Control input current I_{IN} $V_{IN} = V_{CC}$ or GND		V _{IN} = V _{CC} or GND	12.0	_	_	±100	_	±1000	nA
				6.0	_	_	1.0	_	10.0	μА
Quiescent devi	ce current	ent I_{CC} $V_{IN} = V_{CC}$ or GND	9.0	_	_	4.0	_	40.0		
				12.0	_	_	8.0	_	80.0	

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AC Electrical Characteristics (C $_L = 50\ pF,\ input\ t_r = t_f = 6\ ns)$

Characteristics	Symbol	Test Condition		Ta = 25°C		Ta = -40 to 85°C		Unit	
	, ,		V _{CC} (V)	Min	Тур.	Max	Min	Max	
		_	2.0	_	20	75	_	100	ns
Phase difference between	φI-O		4.5	_	7	15	_	20	
input and output	ΨΙ-Ο		9.0		4	12		15	
			12.0		4	11		14	
			2.0	_	20	150	_	190	
Output enable time	t _{pZL}	$R_L = 1 \text{ k}\Omega$	4.5	_	13	30	_	38	ns
Output chable time	t _{pZH}		9.0	_	9	18	_	33	115
			12.0	_	8	18	_	27	
	t _{pLZ} t _{pHZ}	$R_L = 1 \text{ k}\Omega$	2.0	_	40	170	_	220	ns - -
Output disable time			4.5	_	11	35	_	44	
Output disable time			9.0	_	10	30	_	38	
			12.0		9	27	_	33	
		$R_L = 1 \text{ k}\Omega$ $C_L = 15 \text{ pF}$	2.0		30	-	_	—	
Maximum control input			4.5		30	-	_	—	
frequency		V _{OUT} = 1/2 V _{CC}	9.0		30				IVII IZ
			12.0		30				
Control input capacitance	C _{IN}				5	10		10	_
Switch terminal capacitance	C _{I/O}	_		_	6			_	pF
Feedthrough capacitance	C _{IOS}				0.5			_	_
Power dissipation capacitance	C _{PD}		(Note)	_	15	_	_	_	_

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

Analog Switch Characteristics (GND = 0 V, Ta = 25°C) (Note)

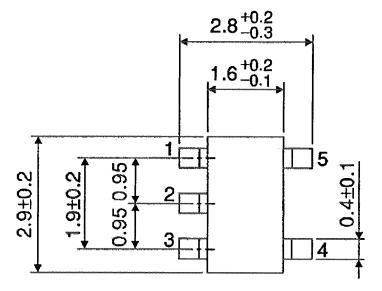
Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Total harmonic distortion (T.H.D)	_	$f_{IN} = 1 \text{ kHz}, V_{IN} = 4 \text{ V}_{pp} (V_{CC} = 4.5 \text{ V})$ $R_L = 10 \text{ k}\Omega, V_{IN} = 8 \text{ V}_{DD} (V_{CC} = 9.0 \text{ V})$	4.5	0.05	%
Total Harmonic distortion (1.11.b)		C _L = 50 pF	9.0	0.04	
Maximum propagation frequency (switch on)	f _{MAX}	Adjust f _{IN} voltage to obtain 0dBm at V _{OS} increase f _{IN} frequency until dB meter reads –3dB.	4.5	200	- MHz
		$R_L = 50 \Omega$, $C_L = 10 pF$ $f_{IN} = 1 MHz$, Sine wave	9.0	200	
Feedthrough (switch on)	_	V_{IN} is centered at $V_{CC}/2$ adjust input for 0dBm R_L = 600 Ω , C_L = 50 pF f_{IN} = 1 MHz, Sine wave	4.5	-60	- dB
			9.0	-60	ub
Crosstalk (control switch)		$R_L = 600 \Omega, C_L = 50 pF$	4.5	60	- mV
CIOSSIAIN (COITHOL SWITCH)		$f_{IN} = 1$ MHz, Pulse ($t_f = t_f = 6$ ns)	9.0	100	

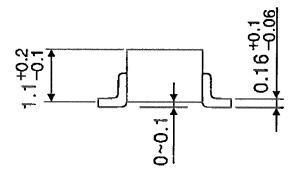
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Note: These characteristics are determined by design of devices.

Package Dimensions

SSOP5-P-0.95 Unit: mm



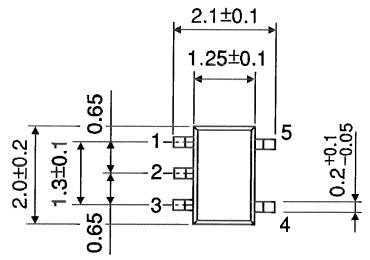


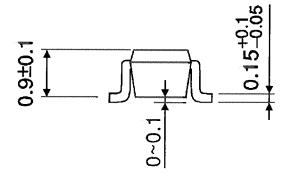
Weight: 0.016 g (typ.)

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Package Dimensions

SSOP5-P-0.65A Unit: mm





Weight: 0.006 g (typ.)

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