UESDA6V1W5 Preliminary **TVS**

4-BIT WIDE MONOLITHIC SUPPRESSOR

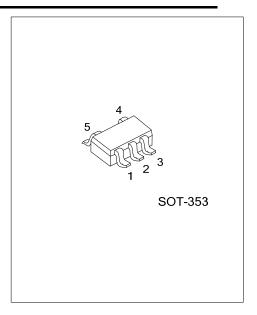
DESCRIPTION

The UTC UESDA6V1W5 is a 4-bit wide monolithic suppressor, it uses UTC's advanced technology to provide customers with low leakage current and high integration, etc.

The UTC UESDA6V1W5 is suitable for ESD protection and high density boards..

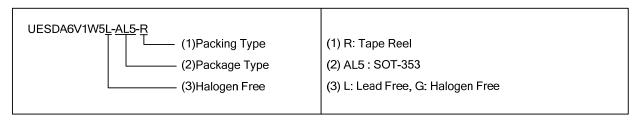
FEATURES

- * Low leakage current (Max.=1µA)
- * High integration
- * Breakdown voltage (Min.=6.1V)

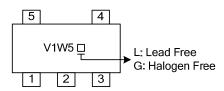


ORDERING INFORMATION

Ordering Number		Dookogo	Dooking	
Lead Free	Halogen Free	Package	Packing	
UESDA6V1W5L-AL5-R	UESDA6V1W5G-AL5-R	SOT-353	Tape Reel	

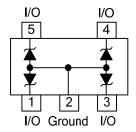


MARKING



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■ FUNCTIONAL DIAGRAM



■ PIN DESCRIPTION

PIN NO.	PIN NAME	DESCRIPTION	
1	I/O	Terminal of ESD 1	
2	GND	Ground	
3	I/O	Terminal of ESD 2	
4	I/O	Terminal of ESD 3	
5	I/O	Terminal of ESD 4	

■ ABSOLUTE MAXIMUM RATINGS (T_A=25°C)

PARAMETER		SYMBOL	RATINGS	UNIT	
ESD Discharge	HEC1000-4-2 F	Air Discharge	V _{ESD}	15	kV
		Contact Discharge		8	kV
Peak Pulse Power (8/20 µs)		V_{PP}	150	W	
Operating Junction Temperature		T_J	125	°C	
Operating Temperature (Note 2)		T_OPR	-40~+125	°C	
Storage Temperature		T _{STG}	-55~+150	°C	

Note: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ ELECTRICAL CHARACTERISTICS (T_A=25°C)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Stand-Off Voltage	V_{RM}				3	V
Breakdown Voltage	V_{BR}	I _R =1mA	6.1		7.2	V
Leakage Current	I _{RM}	V _{RM} =3V			1	μA
Voltage Temperature Coefficient (Note 2)	αΤ				6	10 ⁻⁴ /°C
Capacitance Per Line	С	0V Bias		90		pF
Dynamic Resistance (Note 1)	R_d			350		mΩ
Forward Voltage Drop	V _F	I _F =200mA			1.25	V

Notes: 1. Square pulse I_{PP}=15A, t_P=2.5µs

2. $\Delta V_{BR} = \alpha T \times (T_A - 25^{\circ}C) \times V_{BR}$ (25°C)

^{2.} The values of operating parameters versus temperature are given through curve and αT parameter.

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