International Rectifier

HYBRID - HIGH RELIABILITY RADIATION TOLERANT DC-DC CONVERTER

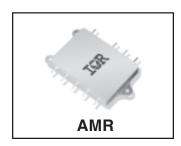
Description

The AMR28XXD series of DC-DC converter modules has been specifically designed for operation in moderate radiation environments supplementing the higher radiation performance available in the International Rectifier ART2815T converter series. Environments presented to space vehicles operating in low earth orbits, launch boosters, orbiting space stations and similar applications requiring a low power, high performance converter with moderate radiation hardness performance will be optimally served by the AMR28XXD series.

The physical configuration of the AMR28XXD series permits mounting directly to a heat conduction surface without the necessity of signal leads penetrating the heat sink surface. This package configuration permits greater independence in mounting and more mechanical security than traditional packages. International Rectifier's rugged ceramic seal pins are used exclusively in the package thereby assuring long term hermeticity.

The AMR28XXD has been designed for high density using chip and wire hybrid technology that complies with class H requirement of MIL-PRF-38534. Manufactured in a facility fully qualified to MIL-PRF-38534, these converters are fabricated utilizing DLA Land and Maritime qualified processes. For available screening options, refer to device screening table in the data sheet. Applicable generic lot qualification test data including radiation performance can be made available on request. Variations to the standard screening can be accommodated. Consult IR San Jose for special requirements.

AMR28XXD SERIES 28V Input, Dual Output



Features

- 30 Watts Output Power
- Available in \pm 5, \pm 12 and \pm 15 Volt Outputs
- 16 40 VDC Input Range (28 VDC Nominal)
- Total Ionizing Dose > 25KRads (Si)
- SEE Hardened to LET up to 60 MeV.cm²/mg
- -55°C to +125°C Operating Range
- Indefinite Short Circuit Protection
- External Synchronization
- Shutdown from External Signal
- Flexible Mounting
- Fully Isolated Input to Output and to Case
- Complimentary EMI Filter Available
- Electrical Performance Similar to ATR28XXD Series

Specifications

| Absolute Maximum Ratings | | Recommended Operating Conditions | | | |
|---|----------------------|----------------------------------|---------------------------|--|--|
| Input Voltage range -0.5V to +50VDC (Continuous | | Input Voltage range | +16V to +40VDC | | |
| | 80V (100ms) | Output Power | Less than or equal to 30W | | |
| Soldering temperature | 300°C for 10 seconds | Operating case temperature | -55°C to +125°C | | |
| Storage case temperature | -65°C to +135°C | | | | |

$\textbf{Static Characteristics} \quad \text{-55}^{\circ}\text{C} \leq \text{T}_{CASE} \leq \text{+125}^{\circ}\text{C}, \ \text{V}_{IN} = 28 \ \text{V}_{DC} \ \pm 5\%, \ \text{C}_{L} = 0, \ \text{unless otherwise specified}.$

| Parameter | Group A Subgroups | Test Conditions | Min | Nom | Max | Unit |
|--|---|--|--|---------------------------|--|------------------|
| Input Voltage | | | 16 | 28 | 40 | V |
| Output Voltage AMR2805D AMR2812D AMR2815D AMR2805D AMR2812D AMR2815D | 1 1 1 2, 3 2, 3 2, 3 2, 3 | lout=0 | ±4.95 ±11.88 ±14.85 ±4.90 ±11.70 ±14.70 | ±5.00 ±12.00 ±15.00 | ±5.05 ±12.12 ±15.15 ±5.10 ±12.30 ±15.30 | V |
| Output Current ^{1, 2} AMR2805D AMR2812D AMR2815D | 1, 2, 3 1, 2, 3 1, 2, 3 | Vin = 16, 28, 40 Volts | 600 250 200 | | 5400 2250 1800 | mA |
| Output Power AMR2805D AMR2812D AMR2815D | 1, 2, 3 1, 2, 3 1, 2, 3 | 100% load | | | 30 30 30 | W |
| Output Ripple Voltage ³ AMR2805D AMR2812D AMR2815D | 1, 2, 3 1, 2, 3 1, 2, 3 | Vin = 16, 28, 40 Volts BW = 20 Hz to 2 MHz | | | 60 85 85 | mV _{PP} |
| Output Voltage Regulation 4 Line AMR2805D AMR2812D AMR2815D Load AMR2805D | 1, 2, 3 1, 2, 3 1, 2, 3 | V _{in} = 16, 28, 40 Volts lout = 0, 50%, and 100% load | | ±10 ±30 ±40 | ±30 ±75 ±75 | mV |
| AMR2812D AMR2815D | 1, 2, 3 1, 2, 3 | | | ±50 ±50 | ±120 ±150 | |

For Notes to Specifications, refer to page 5

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 $\textbf{Static Characteristics} \text{ (Continued) } -55^{\circ}\text{C} \leq \text{T}_{CASE} \leq +125^{\circ}\text{C}, \text{ V}_{IN} = 28 \text{ V}_{DC} \pm 5\%, \text{ C}_{L} = 0, \text{unless} \text{ otherwise pecified.}$

| Parame | ter | Group A Subgroups | Test Conditions | Min | Nom | Max | Unit |
|--------------------------------------|--|--|---|----------------------------------|---|-----------------------------------|------------------|
| Cross Regulation ⁵ | AMR2805D AMR2812D AMR2815D | 1, 2, 3 1, 2, 3 1, 2, 3 | 10% TO 90% Load change | | | 10 5.0 5.0 | % |
| Input Current No Load Inhibit | AMR2805D AMR2812D AMR2815D AMR2805D AMR2812D AMR2815D | 1, 2, 3 1, 2, 3 1, 2, 3 1, 2, 3 1, 2, 3 1, 2, 3 | lout=0, Inhibit =open Inhibit shorted to input return | | 20 20 20 20 8.0 8.0 8.0 | 70 75 100 15 18 18 | mA |
| Input Ripple Current ^{3, 4} | | 1, 2, 3 | Vin = 16, 28, 40 Volts, 100% load, BW = 20 Hz to 2 MHz | | | 50 | mA _{PP} |
| Efficiency ⁴ | AMR2805D AMR2812D AMR2815D AMR2805D AMR2812D AMR2815D | 1 1 1 2, 3 2, 3 2, 3 | 100% load | 76 80 79 72 75 74 | | | % |
| Isolation | | 1 | Input to output or any pin to case (except case ground pin) at 500Vdc | 100 | | | MΩ |
| Capacitive Load ^{6, 7} | | 4 | No effect on dc performance | | | 200 | μF |
| Short Circuit Power Dissipation | | 1, 2, 3 | | | | 9.0 | W |
| Switching Frequency ⁴ | | 4, 5, 6 | 100% load | 500 | 550 | 600 | KHz |
| Sync frequency range | | 4, 5, 6 | 100% load | 500 | | 700 | KHz |
| мтвғ | | | MIL-HDBK-217F, SF@ Tc=35°C | 800 | | | Khrs |
| Weight | | | | | | 68 | g |

 $\textbf{Dynamic Characteristics} \quad \text{-}55^{\circ}\text{C} \leq \text{T}_{CASE} \leq \text{+}125^{\circ}\text{C}, \ \text{V}_{IN} \underline{=} 28 \ \text{V}_{DC} \pm 5\%, \ \text{C}_{L} \underline{=} 0, \ \text{unless otherwise specified}.$

| Parameter | | roup A bgroups | Test Conditions | Min | Nom | Max | Unit |
|---|---------|--------------------|---------------------------------|--------|--------|-------|-------|
| | | ogroups | rest conditions | IVIIII | 140111 | IVIGA | Oilit |
| Output Response To Step Transient Load Changes 4, 9 | | | | | | | |
| | | | Load step 50% ⇔ 100% | | | | |
| AMR2 | | 4, 5, 6 | · | -400 | | +400 | |
| AMR2 | | 4, 5, 6 | | -400 | | +400 | |
| AMR2 | 2815D 4 | 4, 5, 6 | | -400 | | +400 | mV pk |
| | | | Load step 0% ⇔ 50% | | | | |
| AMR2 | | 4, 5, 6 | | -400 | | +400 | |
| AMR2 | | 4, 5, 6 | | -800 | | +800 | |
| AMR2 | 2815D 4 | 4, 5, 6 | | -800 | | +800 | |
| Bassyam, Time Step Transien | | | | | | | |
| Recovery Time, Step Transien Load Changes 4, 9, 10 | 11. | | | | | | |
| Load Changes | 0050 | 4 5 0 | Load step 50% ⇔ 100% | | | 70 | |
| AMR2 | | 4, 5, 6 | | | | 70 | |
| AMR2 | | 4, 5, 6 | | | | 70 | μS |
| AMR2 | 3 USD 4 | 4, 5, 6 | | | | 70 | |
| A B 4000 | 100ED | 4 5 0 | Load step 0% => 50% | | | E00 | |
| AMR2 | | 4, 5, 6 | | | | 500 | μS |
| AMR2 | 812D 4 | 4, 5, 6 | | | | 500 | r |
| AMR2 | 2015D 4 | 4, 5, 6 | Load step 50% => 0% | | | 500 | |
| AMPO | 0050 | 4 5 0 | 20dd 5tep 6070 => 070 | | | 5 | |
| AMR2 | | 4, 5, 6 | | | | 5 | ms |
| AMR2 | | 4, 5, 6 | | | | 5 | 1115 |
| AMR2 | 2815D 4 | 4, 5, 6 | | | | | |
| Output Response Transient S | ten | | | | | | |
| Output Response Transient St Line Changes 4, 7, 11 | СР | | Input step from/to 16 to 40Vdc, | | | | |
| AMR2 | | 4, 5, 6 | 100% load | -500 | | +500 | |
| AMR2 | | 4, 5, 6 4, 5, 6 | 100 % load | -1200 | | +1200 | |
| AMR2 | | 4, 5, 6 4, 5, 6 | | -1500 | | +1500 | mV pk |
| AIVINZ | .6130 - | +, 5, 6 | | -1500 | | +1500 | |
| Recovery Time Transient Step | Line | | | | | | |
| Recovery Time Transient Step Changes 4, 7, 10, 11 | | | Input step from/to 16 to 40Vdc, | | | | |
| AMR2 | | 4, 5, 6 | 100% load | | | 10 | |
| AMR2 | | 4, 5, 6 | | | | 10 | ms |
| AMR2 | | 4, 5, 6 | | | | 10 | 0 |
| Town On Organish and 4 | | | | | | | |
| Turn On Overshoot 4 | 2005 | 156 | 09/ load to 1009/ load | | | 4E0 | |
| AMR2 | | 4, 5, 6 | 0% load to 100% load | | | 450 | |
| AMR2 | | 4, 5, 6 | | | | 600 | mV pk |
| AMR2 | 815D 4 | 4, 5, 6 | | | | 750 | |
| Turn On Delay 4, 12 | | | | | | | |
| AMR2 | 805D 4 | 4, 5, 6 | 0% load to 100% load | | | 25 | |
| AMR2 | | 4, 5, 6 | | | | 25 | ms |
| AMR2 | 815D 4 | 4, 5, 6 | | | | 25 | 0 |
| Short Circuit Recovery ⁷ | | | | | | | |
| AMR2 | 805D | 1 5 6 | | | | OF. | |
| AMR2 | | 4, 5, 6 | | | | 25 | |
| AMR2 | | 4, 5, 6 | | | | 25 | ms |
| AIVIRZ | טוטט ע | 4, 5, 6 | | | | 25 | |

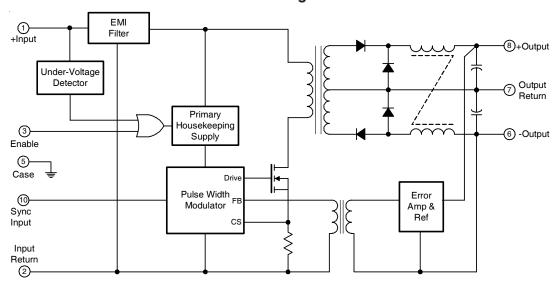
For Notes to Specifications, refer to page 5

AMR28XXD Series (28V Input, Dual Output)

Notes to Specifications

- 1. Parameter guaranteed by line and load regulation tests.
- 2. Up to 90 percent of full power is available from either output provided the total output does not exceed 30 watts.
- 3. Bandwidth guaranteed by design, Tested for 20 Hz to 2 MHz.
- 4. Load current split equally between $+V_{OUT}$ and $-V_{OUT}$.
- 5. 3 watt load on output under test. 3 watt to 27 watt load change on other output.
- 6. Capacitive load may be any value from 0 to the maximum limit without compromising DC performance. A capacitive load in excess of the maximum limit will not disturb loop stability but may interfere with the operation of the load fault detection circuitry, appearing as a short circuit during turn-on.
- 7. Parameter shall be tested as part of design characterization and after design or process changes, Parameters shall be guaranteed to the limit specified in Electrical Specifications.
- 8. Load step transition time between 2 and 10 microseconds.
- 9. Recovery time is measured from initiation of the transient to where V_{out} has returned to within ±1% of Vout at 50% load
- 10. Input step transient time between 2 and 10 microseconds.
- 11. Turn-on delay time measurement is for either a step application of power at the input or the removal of a ground signal from the inhibit pin while power is applied to the input.

Block Diagram



Application Information

Inhibit Function (Enable)

Connecting the inhibit input to input common will cause the converter to shut down. It is recommended that the inhibit pin be driven by an open collector device capable of sinking at least 400 μ A of current. The open circuit voltage of the inhibit input is 10 ±1.0 Vpc.

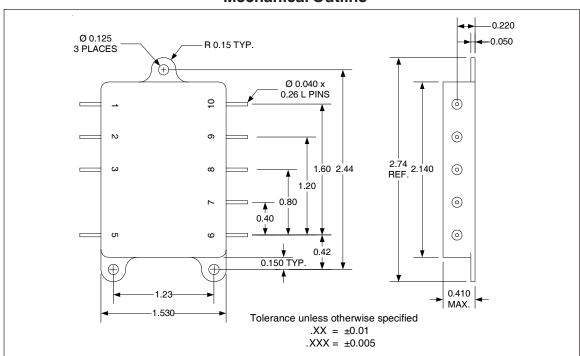
EMI Filter

An optional EMI filter is available (AFH461) that will reduce the input ripple current to levels below the limits imposed by MIL-STD-461 CE03.

Device Synchronization

When multiple DC-DC converters are utilized in a single system, significant low frequency noise may be generated due to a small difference in the switching frequency of the converters (beat frequency noise). Because of the low frequency nature of this noise (typically less than 10 KHz), it is difficult to filter out and may interfere with proper operation of sensitive systems (communication, radar or telemetry). International Rectifier provides synchronization of multiple AMR type converters to match switching frequency of the converter to the frequency of the system clock, thus eliminating this type of noise.

Mechanical Outline



Pin Designation

| Pin# | Designation | Pin # | Designation |
|------|--------------|-------|---------------|
| 1 | + Input | 6 | - Output |
| 2 | Input Return | 7 | Output Return |
| 3 | Enable | 8 | + Output |
| 4 | Blank | 9 | NC |
| 5 | Case Ground | 10 | Sync Input |

Radiation Performance Characteristics

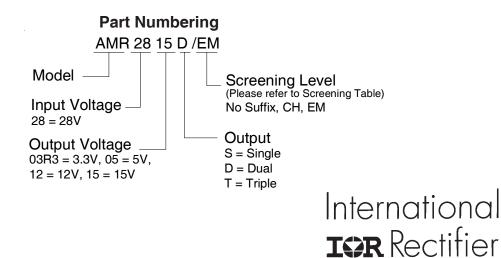
| Test Inspection | Method | | Тур | Unit |
|----------------------|---|----|-----|-------------------------|
| Total Ionizing Dose | MIL-PRF-883, Method 1019.4 | | | Krads(Si) |
| | Operating bias applied during exposure, | | | |
| Single Event Effects | Heavy Ions (LET) | 60 | | MeV•cm ² /mg |
| | Test lab: Brookhaven National Laboratory, | | | |
| | Tandem Van de Graaff Generator | | | |

Device Screening

| Requirement | MIL-STD-883 Method | No Suffix ② | CH ② | ЕМ |
|---------------------------|--------------------|-----------------|-----------------|----------------|
| Temperature Range | _ | -55°C to +125°C | -55°C to +125°C | -55°C to +85°C |
| Element Evaluation | MIL-PRF-38534 | Class H | Class H | N/A |
| Non-Destructive Bond Pull | 2023 | N/A | N/A | N/A |
| Internal Visual | 2017 | Yes | Yes | 0 |
| Temperature Cycle | 1010 | Cond C | Cond C | Cond C |
| Constant Acceleration | 2001, Y1 Axis | 3000 Gs | 3000 Gs | 3000 Gs |
| PIND | 2020 | Cond A | Cond A | N/A |
| Burn-In | 1015 | 320 hrs @ 125°C | 320 hrs @ 125°C | 48 hrs @ 125°C |
| Dulli-ili | 1015 | (2 x 160hrs) | (2 x 160hrs) | |
| Final Electrical | MIL-PRF-38534 | -55°C, +25°C, | -55°C, +25°C, | -55°C, +25°C, |
| (Group A) | & Specification | +125°C | +125°C | +85°C |
| PDA | MIL-PRF-38534 | 2% | 2% | N/A |
| Seal, Fine and Gross | 1014 | Cond A, C | Cond A, C | Cond A |
| Radiographic | 2012 | Yes | Yes | N/A |
| External Visual | 2009 | Yes | Yes | 0 |

Notes:

- ① Best commercial practice.
- ② Device with '/CH' suffix is a DLA Land and Maritime Class H compliant without radiation performance. No Suffix is a radiation rated device but not available as a DLA Land and Maritime qualified SMD per MIL-PRF-38534 International Rectifier currently does not have a DLA Land and Maritime certified Radiation Hardness Assurance Program



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Data and specifications subject to change without notice. 10/2012