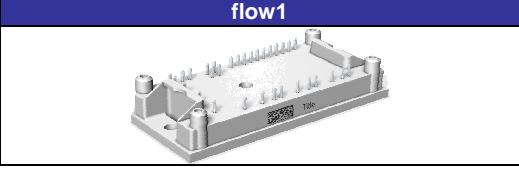


flow1	1200V/8A
Features	
<ul style="list-style-type: none"> <li>• 3~rectifier, optional BRC, Inverter, NTC</li> <li>• Very compact housing, easy to route</li> <li>• IGBT! / EmCon4 technology for low saturation losses and improved EMC behaviour</li> </ul>	
Target Applications	<ul style="list-style-type: none"> <li>• Industrial drives</li> <li>• Embedded drives</li> </ul>
Types	<ul style="list-style-type: none"> <li>• V23990-P588-A88-PM</li> <li>• V23990-P588-C88-PM</li> </ul>

## Maximum Ratings

$T_j=25^\circ\text{C}$ , unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
<b>Input Rectifier Diode</b>				
Repetitive peak reverse voltage	$V_{RRM}$		1600	V
DC forward current	$I_{FAV}$	$T_j=T_{j\max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	33 47	A
Surge forward current	$I_{FSM}$	$t_p=10\text{ms}$ 50 Hz half sine wave	250	A
$I^2t$ -value	$I^2t$	$T_j=25^\circ\text{C}$	310	$\text{A}^2\text{s}$
Power dissipation per Diode	$P_{\text{tot}}$	$T_j=T_{j\max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	37 60	W
Maximum Junction Temperature	$T_{j\max}$		150	$^\circ\text{C}$
<b>Inverter Transistor</b>				
Collector-emitter break down voltage	$V_{CE}$		1200	V
DC collector current	$I_C$	$T_j=T_{j\max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	12 15	A
Pulsed collector current	$I_{Cpulse}$	$t_p$ limited by $T_{j\max}$	24	A
Turn off safe operating area		$V_{CE} \leq 1200\text{V}$ , $T_j \leq T_{op\max}$	24	A
Power dissipation per IGBT	$P_{\text{tot}}$	$T_j=T_{j\max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	44 67	W
Gate-emitter peak voltage	$V_{GE}$		$\pm 20$	V
Short circuit ratings	$t_{sc}$ $V_{CC}$	$T_j \leq 150^\circ\text{C}$ $V_{GE}=15\text{V}$	10 800	$\mu\text{s}$ V
Maximum Junction Temperature	$T_{j\max}$		175	$^\circ\text{C}$

## Maximum Ratings

$T_j=25^\circ\text{C}$ , unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
<b>Inverter Diode</b>				
Peak Repetitive Reverse Voltage	$V_{RRM}$		1200	V
DC forward current	$I_F$	$T_j=T_j\max$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	20 20	A
Repetitive peak forward current	$I_{FRM}$	$t_p$ limited by $T_j\max$	20	A
Power dissipation per Diode	$P_{tot}$	$T_j=T_j\max$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	23 32	W
Maximum Junction Temperature	$T_j\max$		175	°C
<b>Brake Transistor</b>				
Collector-emitter break down voltage	$V_{CE}$		1200	V
DC collector current	$I_C$	$T_j=T_j\max$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	8 10	A
Pulsed collector current	$I_{Cpuls}$	$t_p$ limited by $T_j\max$	12	A
Turn off safe operating area		$VCE \leq 1200\text{V}$ , $T_j \leq T_{j\max}$	16	A
Power dissipation per IGBT	$P_{tot}$	$T_j=T_j\max$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	32 49	W
Gate-emitter peak voltage	$V_{GE}$		$\pm 20$	V
Short circuit ratings	$t_{sc}$ $V_{CC}$	$T_j \leq 150^\circ\text{C}$ $V_{GE}=15\text{V}$	10 800	$\mu\text{s}$ V
Maximum Junction Temperature	$T_j\max$		175	°C
<b>Thermal Properties</b>				
Storage temperature	$T_{stg}$		-40...+125	°C
Operation temperature under switching condition	$T_{op}$		-40...+( $T_{j\max} - 25$ )	°C
<b>Insulation Properties</b>				
Insulation voltage	$V_{is}$	$t=2\text{s}$ DC voltage	4000	V
Creepage distance			min 12,7	mm
Clearance			min 12,7	mm
Comparative tracking index	CTI		>200	

**Characteristic Values**

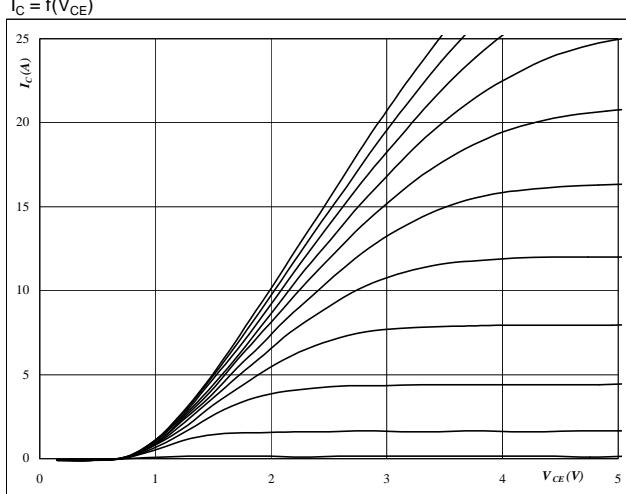
Parameter	Symbol	Conditions				Value			Unit	
		$V_{GE}$ [V] or $V_{GS}$ [V]	$V_T$ [V] or $V_{CE}$ [V] or $V_{DS}$ [V]	$I_C$ [A] or $I_F$ [A] or $I_D$ [A]	$T_J$	Min	Typ	Max		
<b>Input Rectifier Diode</b>										
Forward voltage	$V_F$			30	$T_J=25^\circ\text{C}$ $T_J=125^\circ\text{C}$	0,8	1,16 1,13	1,6	V	
Threshold voltage (for power loss calc. only)	$V_{to}$			30	$T_J=25^\circ\text{C}$ $T_J=125^\circ\text{C}$		0,90 0,78		V	
Slope resistance (for power loss calc. only)	$r_t$			30	$T_J=25^\circ\text{C}$ $T_J=125^\circ\text{C}$		8 11	12	$\text{m}\Omega$	
Reverse current	$I_r$		1500		$T_J=25^\circ\text{C}$ $T_J=150^\circ\text{C}$			2	$\text{mA}$	
Thermal resistance chip to heatsink per chip	$R_{thJH}$	Thermal grease thickness≤50um $\lambda = 1 \text{ W/mK}$					1,89		K/W	
Thermal resistance chip to heatsink per chip	$R_{thJH}$	Preapplied Phase change material					1,68		K/W	
<b>Inverter Transistor</b>										
Gate emitter threshold voltage	$V_{GE(\text{th})}$	$V_{CE}=V_{GE}$		0,00085	$T_J=25^\circ\text{C}$ $T_J=125^\circ\text{C}$	5	5,8	6,5	V	
Collector-emitter saturation voltage	$V_{CE(\text{sat})}$		15		$T_J=25^\circ\text{C}$ $T_J=125^\circ\text{C}$	1,6	1,87 2,29	2,1	V	
Collector-emitter cut-off current incl. Diode	$I_{CES}$		0	1200	$T_J=25^\circ\text{C}$ $T_J=125^\circ\text{C}$			0,0024	$\text{mA}$	
Gate-emitter leakage current	$I_{GES}$		20	0	$T_J=25^\circ\text{C}$ $T_J=125^\circ\text{C}$			100	$\text{nA}$	
Integrated Gate resistor	$R_{gint}$						-		$\Omega$	
Turn-on delay time	$t_{d(on)}$	$R_{goff}=32 \Omega$ $R_{gon}=32 \Omega$	600	25	$T_J=25^\circ\text{C}$ $T_J=125^\circ\text{C}$		71 72		ns	
Rise time	$t_r$				$T_J=25^\circ\text{C}$ $T_J=125^\circ\text{C}$		19 22			
Turn-off delay time	$t_{d(off)}$				$T_J=25^\circ\text{C}$ $T_J=125^\circ\text{C}$		194 250			
Fall time	$t_f$				$T_J=25^\circ\text{C}$ $T_J=125^\circ\text{C}$		79 110			
Turn-on energy loss per pulse	$E_{on}$				$T_J=25^\circ\text{C}$ $T_J=125^\circ\text{C}$		0,50 0,80		mWs	
Turn-off energy loss per pulse	$E_{off}$				$T_J=25^\circ\text{C}$ $T_J=125^\circ\text{C}$		0,43 0,66			
Input capacitance	$C_{ies}$						1430			
Output capacitance	$C_{oss}$	$f=1\text{MHz}$	0	25	$T_J=25^\circ\text{C}$		115		pF	
Reverse transfer capacitance	$C_{rss}$						85			
Gate charge	$Q_{\text{Gate}}$						53			
Thermal resistance chip to heatsink per chip	$R_{thJH}$	Thermal grease thickness≤50um $\lambda = 1 \text{ W/mK}$					2,16		K/W	
Thermal resistance chip to heatsink per chip	$R_{thJH}$	Preapplied Phase change material					1,87		K/W	
<b>Inverter Diode</b>										
Diode forward voltage	$V_F$			1200		$T_J=25^\circ\text{C}$ $T_J=125^\circ\text{C}$	1,35	1,88 1,81	2,05	V
Peak reverse recovery current	$I_{RRM}$	$R_{gon}=32 \Omega$			$T_J=25^\circ\text{C}$ $T_J=125^\circ\text{C}$		8 10		A	
Reverse recovery time	$t_{rr}$				$T_J=25^\circ\text{C}$ $T_J=125^\circ\text{C}$		251 411			
Reverse recovered charge	$Q_{rr}$				$T_J=25^\circ\text{C}$ $T_J=125^\circ\text{C}$		0,89 1,72			
Peak rate of fall of recovery current	$di(\text{rec})/\text{max dt}$				$T_J=25^\circ\text{C}$ $T_J=125^\circ\text{C}$		84 64			
Reverse recovered energy	$E_{rec}$				$T_J=25^\circ\text{C}$ $T_J=125^\circ\text{C}$		0,34 0,69			
Thermal resistance chip to heatsink per chip	$R_{thJH}$	Thermal grease thickness≤50um $\lambda = 1 \text{ W/mK}$					2,68		K/W	
Thermal resistance chip to heatsink per chip	$R_{thJH}$	Preapplied Phase change material					2,37		K/W	

**Characteristic Values**

Parameter	Symbol	Conditions				Value			Unit	
			V <sub>GE</sub> [V] or V <sub>GS</sub> [V]	V <sub>I</sub> [V] or V <sub>CE</sub> [V] or V <sub>DS</sub> [V]	I <sub>C</sub> [A] or I <sub>F</sub> [A] or I <sub>D</sub> [A]	T <sub>J</sub>	Min	Typ	Max	
<b>Brake Transistor</b>										
Gate emitter threshold voltage	V <sub>GE(th)</sub>	V <sub>CE</sub> =V <sub>GE</sub>			0,0005	T <sub>J</sub> =25°C T <sub>J</sub> =125°C	5	5,8	6,5	V
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>		15		15	T <sub>J</sub> =25°C T <sub>J</sub> =125°C	1,6	1,96 2,27	2,1	V
Collector-emitter cut-off incl diode	I <sub>CES</sub>		0	1200		T <sub>J</sub> =25°C T <sub>J</sub> =125°C			0,002	mA
Gate-emitter leakage current	I <sub>GES</sub>		20	0		T <sub>J</sub> =25°C T <sub>J</sub> =125°C			120	nA
Integrated Gate resistor	R <sub>git</sub>							-		Ω
Turn-on delay time	t <sub>d(on)</sub>	R <sub>goff</sub> =64 Ω R <sub>gon</sub> =64 Ω	600	15		T <sub>J</sub> =25°C T <sub>J</sub> =125°C	70 68			ns
Rise time	t <sub>r</sub>					T <sub>J</sub> =25°C T <sub>J</sub> =125°C	11 15			
Turn-off delay time	t <sub>d(off)</sub>					T <sub>J</sub> =25°C T <sub>J</sub> =125°C	211 243			
Fall time	t <sub>f</sub>					T <sub>J</sub> =25°C T <sub>J</sub> =125°C	73 82			
Turn-on energy loss per pulse	E <sub>on</sub>					T <sub>J</sub> =25°C T <sub>J</sub> =125°C	0,08 0,13			mWs
Turn-off energy loss per pulse	E <sub>off</sub>					T <sub>J</sub> =25°C T <sub>J</sub> =125°C	0,06 0,09			
Input capacitance	C <sub>ies</sub>	f=1MHz	0	25		T <sub>J</sub> =25°C	900			pF
Output capacitance	C <sub>oss</sub>						80			
Reverse transfer capacitance	C <sub>rss</sub>						55			
Thermal resistance chip to heatsink per chip	R <sub>thJH</sub>	Thermal grease thickness≤50um λ = 1 W/mK						2,95		K/W
Thermal resistance chip to heatsink per chip	R <sub>thJH</sub>	Preapplied Phase change material						2,55		K/W
<b>Brake Diode</b>										
Diode forward voltage	V <sub>F</sub>				10	T <sub>J</sub> =25°C T <sub>J</sub> =125°C	1,35	1,88 1,79	2,05	V
Reverse leakage current	I <sub>r</sub>			1200		T <sub>J</sub> =25°C T <sub>J</sub> =125°C			2,7	μA
Peak reverse recovery current	I <sub>RRM</sub>	R <sub>gon</sub> =64 Ω R <sub>goff</sub> =64 Ω	15	1200		T <sub>J</sub> =25°C T <sub>J</sub> =125°C	2,98 3,78			A
Reverse recovery time	t <sub>rr</sub>					T <sub>J</sub> =25°C T <sub>J</sub> =125°C	174 333			ns
Reverse recovered charge	Q <sub>rr</sub>					T <sub>J</sub> =25°C T <sub>J</sub> =125°C	0,215 0,215			μC
Peak rate of fall of recovery current	di(rec)/dt					T <sub>J</sub> =25°C T <sub>J</sub> =125°C	44 43			A/μs
Reverse recovery energy	E <sub>rec</sub>					T <sub>J</sub> =25°C T <sub>J</sub> =125°C	0,081 0,246			mWs
Thermal resistance chip to heatsink per chip	R <sub>thJH</sub>	Thermal grease thickness≤50um λ = 1 W/mK						3,86		K/W
Thermal resistance chip to heatsink per chip	R <sub>thJH</sub>	Preapplied Phase change material						3,33		K/W
<b>Thermistor</b>										
Rated resistance	R					T <sub>J</sub> =25°C		22000		Ω
Deviation of R25	ΔR/R						-5		5	%
Power dissipation	P							200		mW
Power dissipation constant						T <sub>J</sub> =25°C		2		mW/K
B-value	B <sub>(25/50)</sub>	Tol. ±3%				T <sub>J</sub> =25°C		3950		K
B-value	B <sub>(25/100)</sub>	Tol. ±3%				T <sub>J</sub> =25°C		3998		K
Vincotech NTC Reference						T <sub>J</sub> =25°C			B	

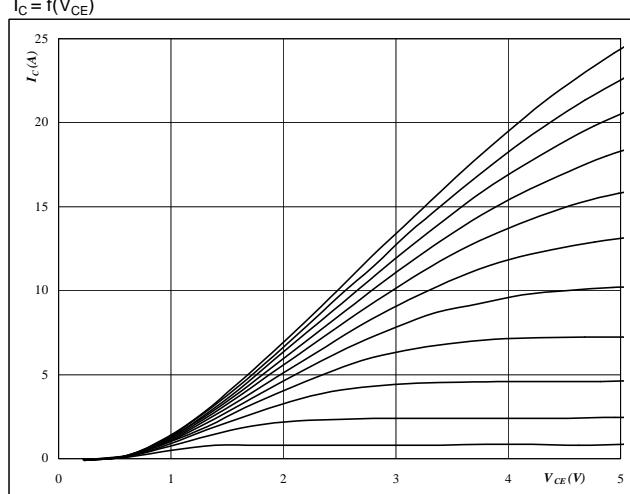
## Output Inverter

**Figure 1**  
Typical output characteristics  
 $I_C = f(V_{CE})$



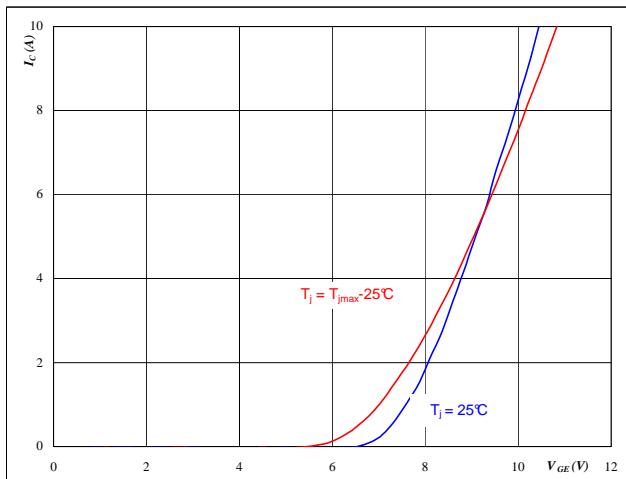
At  
 $t_p = 250 \mu s$   
 $T_j = 25^\circ C$   
 $V_{GE}$  from 7 V to 17 V in steps of 1 V

**Figure 2**  
Typical output characteristics  
 $I_C = f(V_{CE})$



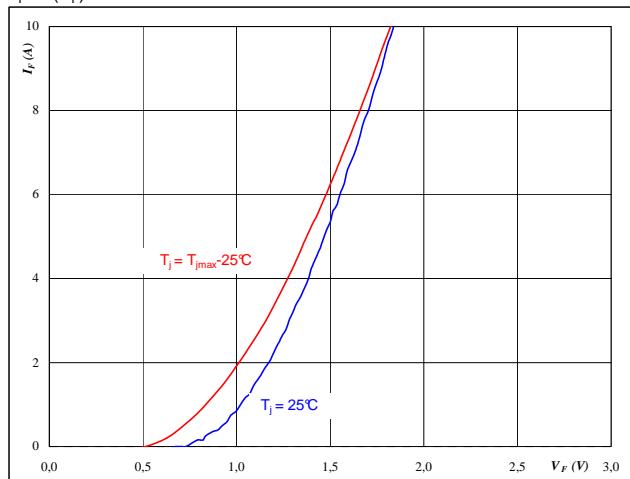
At  
 $t_p = 250 \mu s$   
 $T_j = 150^\circ C$   
 $V_{GE}$  from 7 V to 17 V in steps of 1 V

**Figure 3**  
Typical transfer characteristics  
 $I_C = f(V_{GE})$



At  
 $t_p = 250 \mu s$   
 $V_{CE} = 10 V$

**Figure 4**  
Typical diode forward current as a function of forward voltage  
 $I_F = f(V_F)$



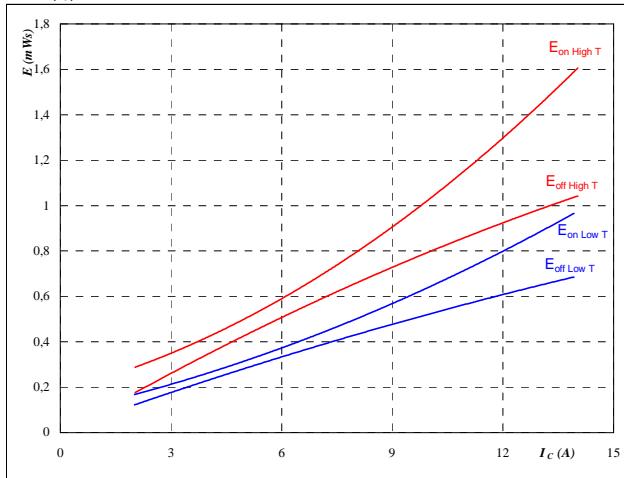
At  
 $t_p = 250 \mu s$

## Output Inverter

**Figure 5**

**Typical switching energy losses  
as a function of collector current**

$$E = f(I_C)$$



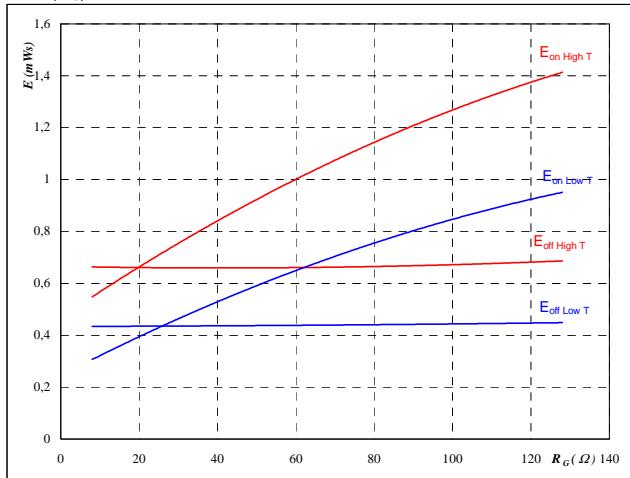
With an inductive load at

$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ R_{gon} &= 32 \quad \Omega \\ R_{goff} &= 32 \quad \Omega \end{aligned}$$

**Figure 6**

**Typical switching energy losses  
as a function of gate resistor**

$$E = f(R_G)$$



With an inductive load at

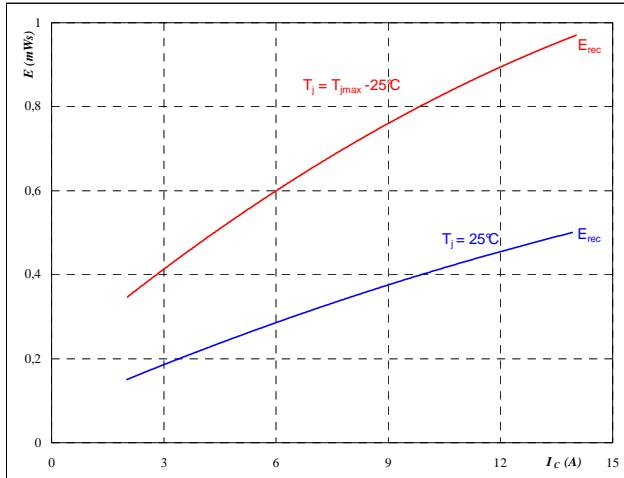
$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ I_C &= 8 \quad \text{A} \\ R_{gon} &= 32 \quad \Omega \\ R_{goff} &= 32 \quad \Omega \end{aligned}$$

**Figure 7**

**Output inverter FWD**

**Typical reverse recovery energy loss  
as a function of collector current**

$$E_{rec} = f(I_C)$$



With an inductive load at

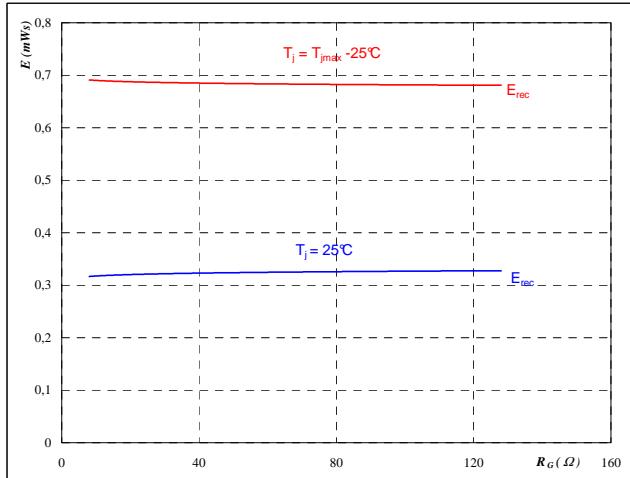
$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ R_{gon} &= 32 \quad \Omega \\ R_{goff} &= 32 \quad \Omega \end{aligned}$$

**Figure 8**

**Output inverter FWD**

**Typical reverse recovery energy loss  
as a function of gate resistor**

$$E_{rec} = f(R_G)$$



With an inductive load at

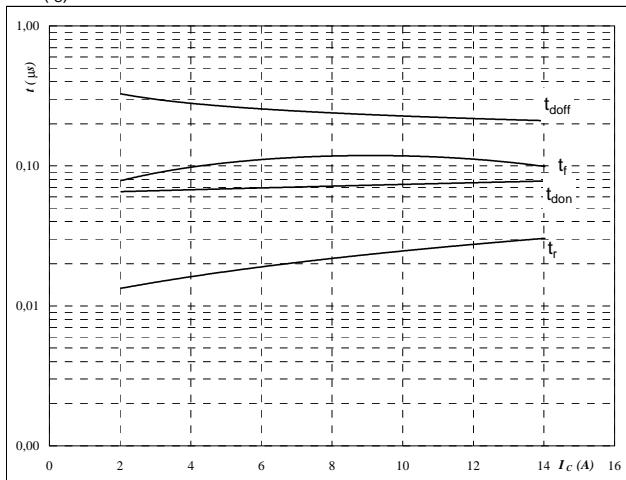
$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ I_C &= 8 \quad \text{A} \\ R_{gon} &= 32 \quad \Omega \\ R_{goff} &= 32 \quad \Omega \end{aligned}$$

## Output Inverter

**Figure 9**

Typical switching times as a function of collector current

$$t = f(I_C)$$



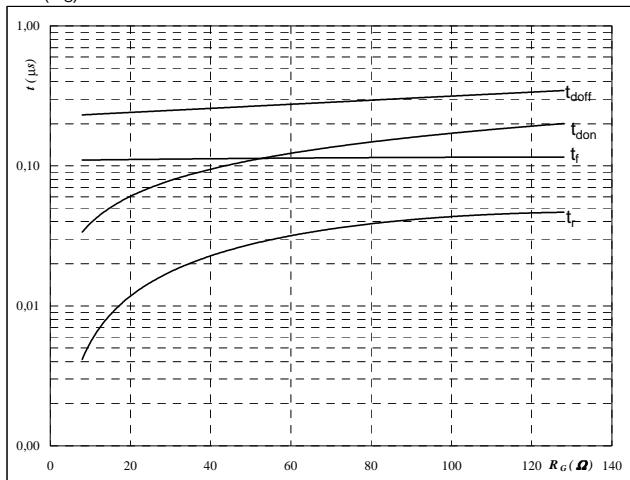
With an inductive load at

T <sub>j</sub> =	150	°C
V <sub>CE</sub> =	600	V
V <sub>GE</sub> =	±15	V
R <sub>gon</sub> =	32	Ω
R <sub>goff</sub> =	32	Ω

**Output inverter IGBT**
**Figure 10**

Typical switching times as a function of gate resistor

$$t = f(R_G)$$



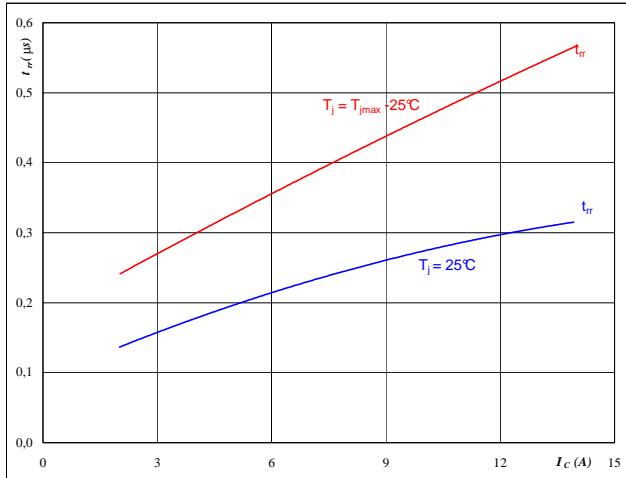
With an inductive load at

T <sub>j</sub> =	150	°C
V <sub>CE</sub> =	600	V
V <sub>GE</sub> =	±15	V
I <sub>C</sub> =	8	A

**Figure 11**
**Output inverter FWD**

Typical reverse recovery time as a function of collector current

$$t_{rr} = f(I_C)$$



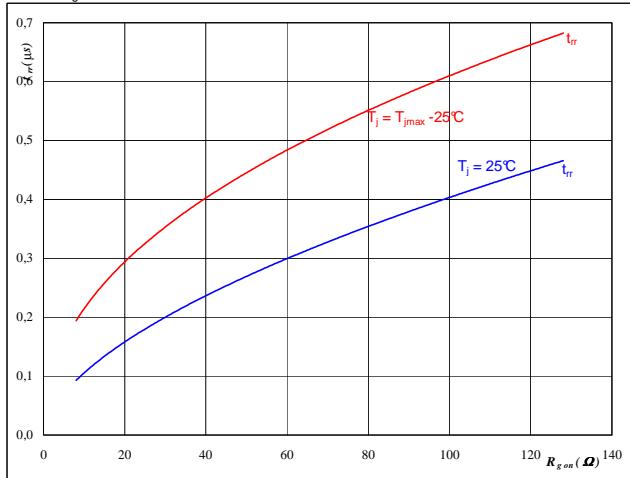
At

T <sub>j</sub> =	25/150	°C
V <sub>CE</sub> =	600	V
V <sub>GE</sub> =	±15	V
R <sub>gon</sub> =	32	Ω

**Figure 12**
**Output inverter FWD**

Typical reverse recovery time as a function of IGBT turn on gate resistor

$$t_{rr} = f(R_{gon})$$



At

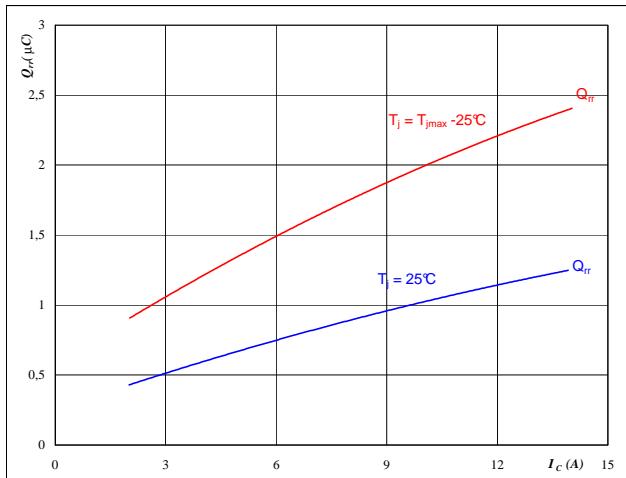
T <sub>j</sub> =	25/150	°C
V <sub>R</sub> =	600	V
I <sub>F</sub> =	8	A
V <sub>GE</sub> =	±15	V

## Output Inverter

**Figure 13**

Typical reverse recovery charge as a function of collector current

$$Q_{rr} = f(I_C)$$

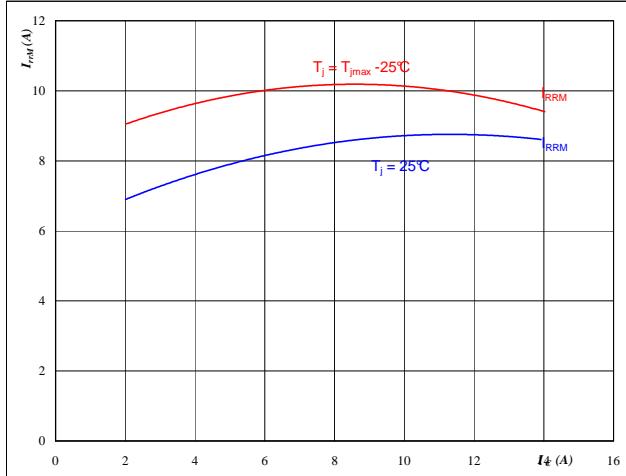

**At**

$$\begin{aligned} T_j &= 25/150 \quad \text{°C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ R_{gon} &= 32 \quad \Omega \end{aligned}$$

**Figure 15**

Typical reverse recovery current as a function of collector current

$$I_{RRM} = f(I_C)$$

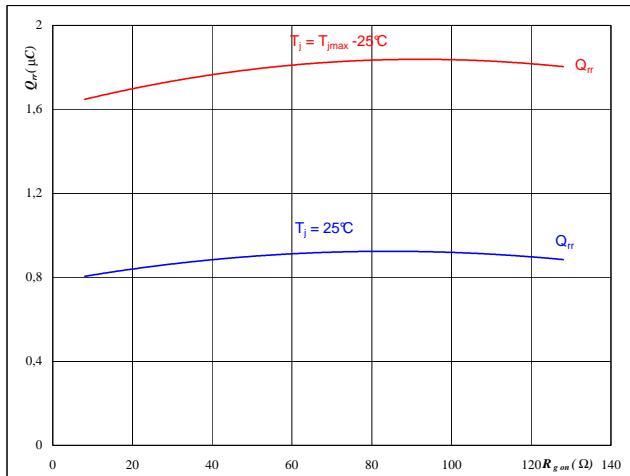

**At**

$$\begin{aligned} T_j &= 25/150 \quad \text{°C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ R_{gon} &= 32 \quad \Omega \end{aligned}$$

**Figure 14**

Typical reverse recovery charge as a function of IGBT turn on gate resistor

$$Q_{rr} = f(R_{gon})$$

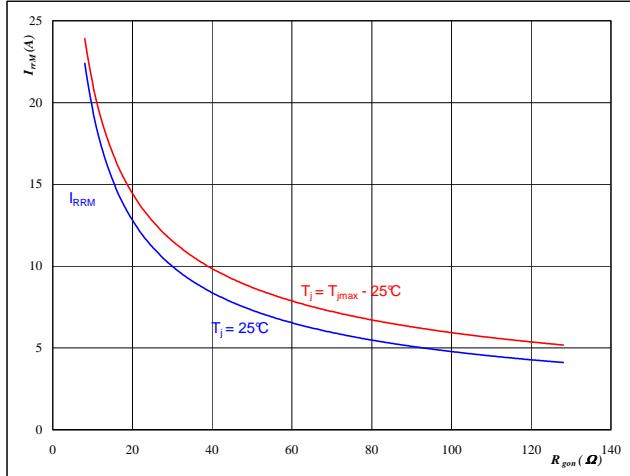

**At**

$$\begin{aligned} T_j &= 25/150 \quad \text{°C} \\ V_R &= 600 \quad \text{V} \\ I_F &= 8 \quad \text{A} \\ V_{GE} &= \pm 15 \quad \text{V} \end{aligned}$$

**Figure 16**

Typical reverse recovery current as a function of IGBT turn on gate resistor

$$I_{RRM} = f(R_{gon})$$

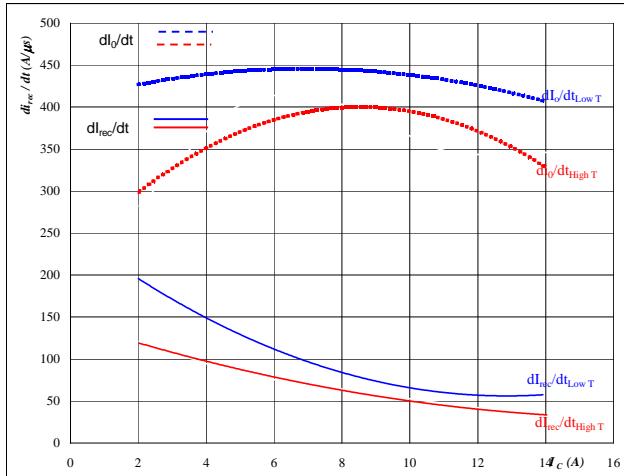

**At**

$$\begin{aligned} T_j &= 25/150 \quad \text{°C} \\ V_R &= 600 \quad \text{V} \\ I_F &= 8 \quad \text{A} \\ V_{GE} &= \pm 15 \quad \text{V} \end{aligned}$$

## Output Inverter

**Figure 17**

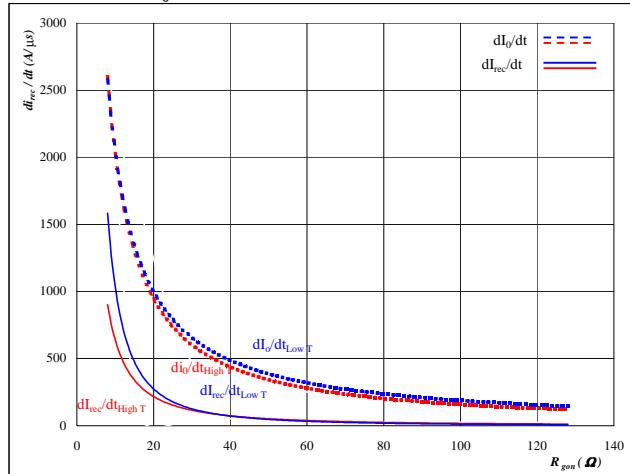
Typical rate of fall of forward  
and reverse recovery current as a  
function of collector current  
 $dI_0/dt, dI_{rec}/dt = f(I_C)$


**At**

$T_j = 25/150$  °C  
 $V_{CE} = 600$  V  
 $V_{GE} = \pm 15$  V  
 $R_{gon} = 32$  Ω

**Output inverter FWD**
**Figure 18**

Typical rate of fall of forward  
and reverse recovery current as a  
function of IGBT turn on gate resistor  
 $dI_0/dt, dI_{rec}/dt = f(R_{gon})$

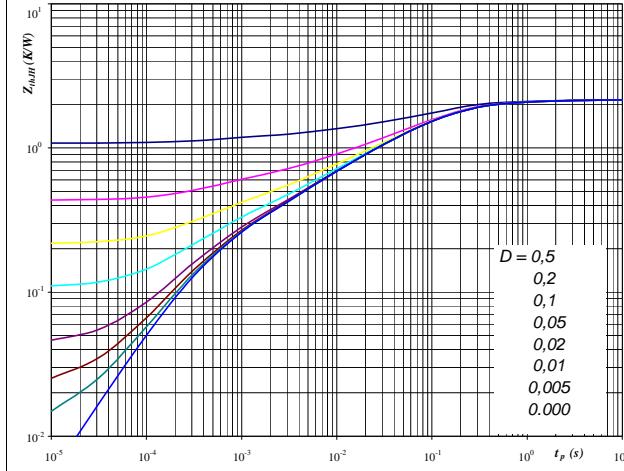

**At**

$T_j = 25/150$  °C  
 $V_R = 600$  V  
 $I_F = 8$  A  
 $V_{GE} = \pm 15$  V

**Figure 19**

IGBT transient thermal impedance  
as a function of pulse width

$$Z_{thJH} = f(t_p)$$


**At**

$D = t_p / T$   
Thermal grease Phase change material

$R_{thJH} = 2,16$  K/W       $R_{thJH} = 1,87$  K/W

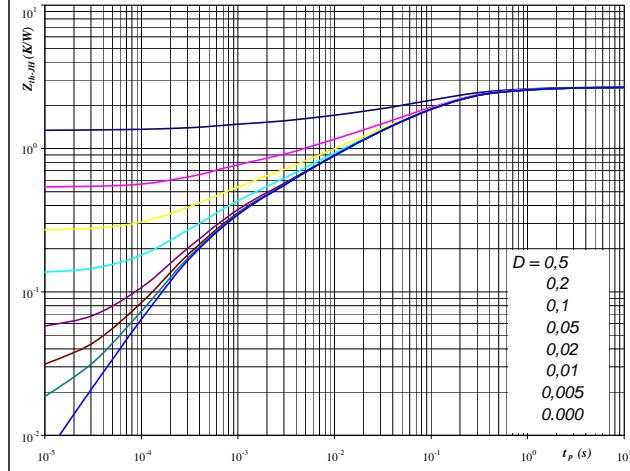
IGBT thermal model values

Thermal grease		Phase change material	
R (C/W)	Tau (s)	R (C/W)	Tau (s)
0,05	4,1E+00	0,04	4,1E+00
0,25	5,5E-01	0,22	5,5E-01
0,99	1,0E-01	0,85	1,0E-01
0,45	1,9E-02	0,39	1,9E-02
0,24	3,3E-03	0,21	3,3E-03
0,18	4,0E-04	0,16	4,0E-04

**Output inverter IGBT**
**Figure 20**

FWD transient thermal impedance  
as a function of pulse width

$$Z_{thJH} = f(t_p)$$


**At**

$D = t_p / T$   
Thermal grease Phase change material

$R_{thJH} = 2,68$  K/W       $R_{thJH} = 2,37$  K/W

FWD thermal model values

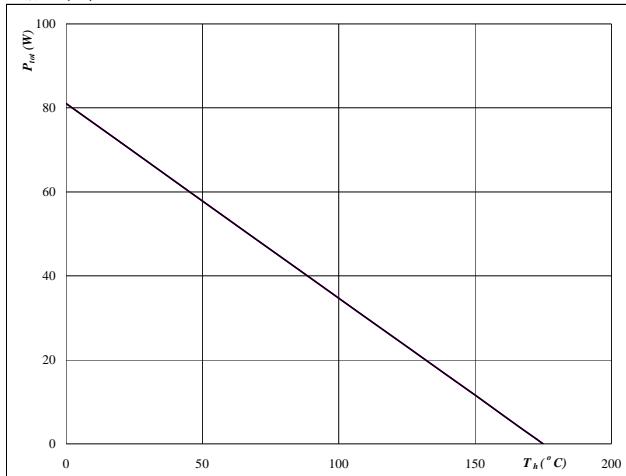
Thermal grease		Phase change material	
R (C/W)	Tau (s)	R (C/W)	Tau (s)
0,05	7,9E+00	0,04	7,9E+00
0,27	7,3E-01	0,24	7,3E-01
1,07	1,3E-01	0,94	1,3E-01
0,69	2,5E-02	0,61	2,5E-02
0,36	3,6E-03	0,32	3,6E-03
0,25	4,3E-04	0,22	4,3E-04

## Output Inverter

**Figure 21**

**Power dissipation as a function of heatsink temperature**

$$P_{\text{tot}} = f(T_h)$$

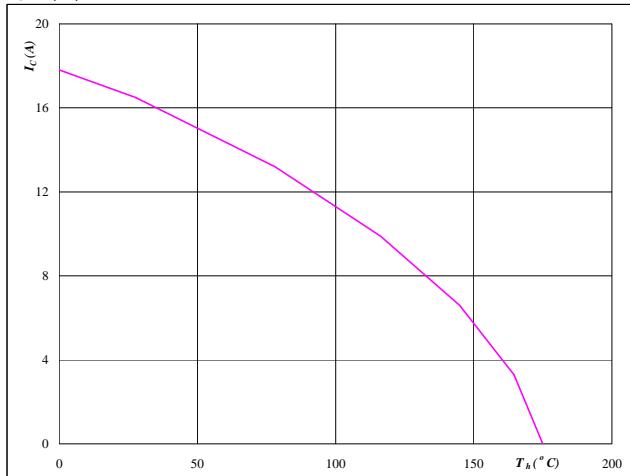

**At**

$$T_j = 175 \quad ^\circ\text{C}$$

**Output inverter IGBT**
**Figure 22**

**Collector current as a function of heatsink temperature**

$$I_C = f(T_h)$$

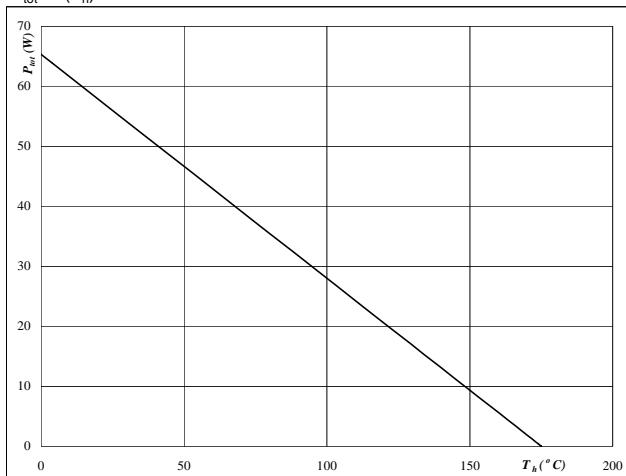

**At**

$$T_j = 175 \quad ^\circ\text{C}$$

**Output inverter IGBT**
**Figure 23**
**Output inverter FWD**

**Power dissipation as a function of heatsink temperature**

$$P_{\text{tot}} = f(T_h)$$

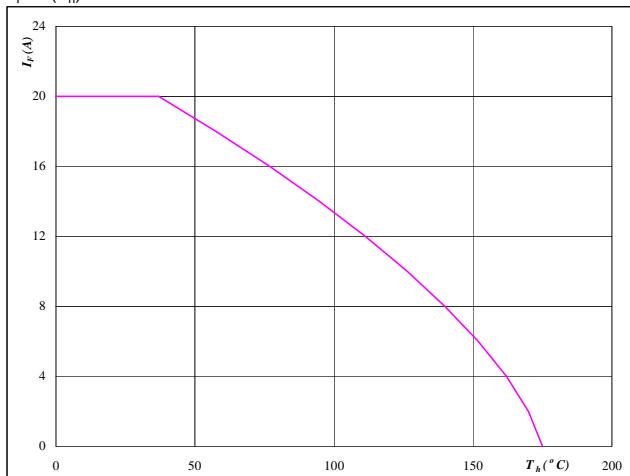

**At**

$$T_j = 175 \quad ^\circ\text{C}$$

**Figure 24**
**Output inverter FWD**

**Forward current as a function of heatsink temperature**

$$I_F = f(T_h)$$


**At**

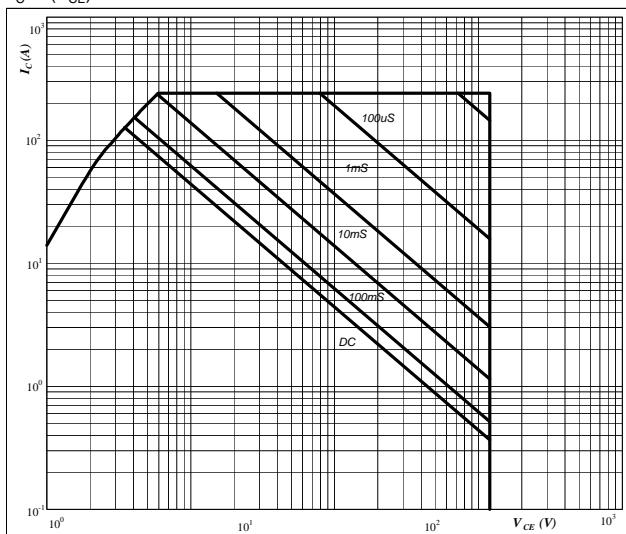
$$T_j = 175 \quad ^\circ\text{C}$$

## Output Inverter

**Figure 25**

**Safe operating area as a function of collector-emitter voltage**

$$I_C = f(V_{CE})$$


**At**

D = single pulse

T<sub>h</sub> = 80 °C

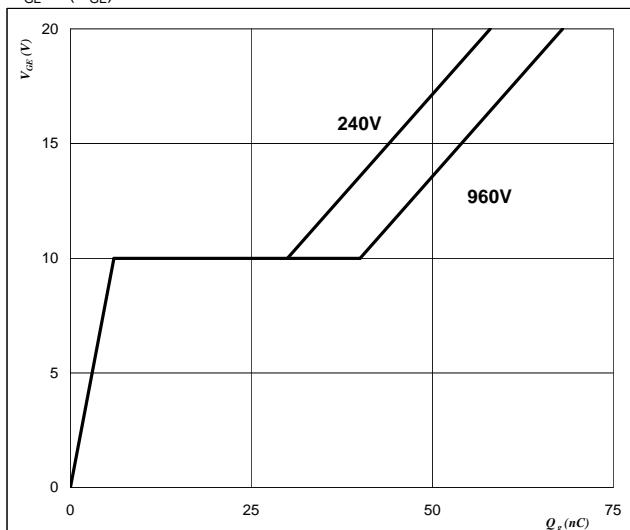
V<sub>GE</sub> = ±15 V

T<sub>j</sub> = T<sub>jmax</sub> °C

**Output inverter IGBT**
**Figure 26**

**Gate voltage vs Gate charge**

$$V_{GE} = f(Q_{GE})$$

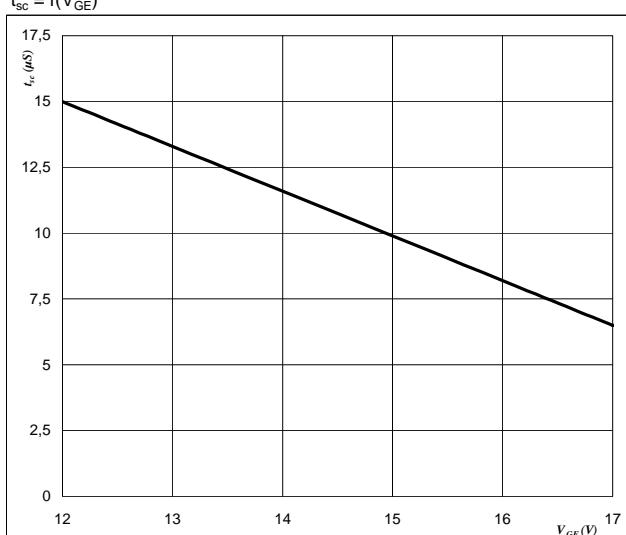

**At**

$$I_C = 8 \text{ A}$$

**Figure 27**
**Output inverter IGBT**

**Short circuit withstand time as a function of gate-emitter voltage**

$$t_{sc} = f(V_{GE})$$


**At**

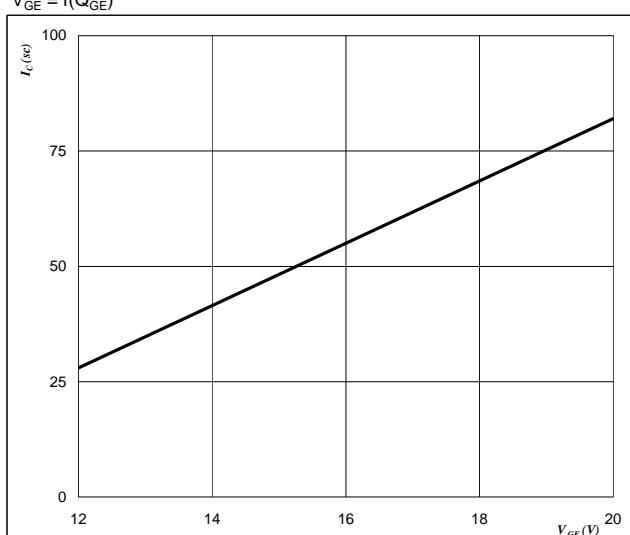
V<sub>CE</sub> = 1200 V

T<sub>j</sub> ≤ 175 °C

**Figure 28**
**Output inverter IGBT**

**Typical short circuit collector current as a function of gate-emitter voltage**

$$V_{GE} = f(Q_{GE})$$

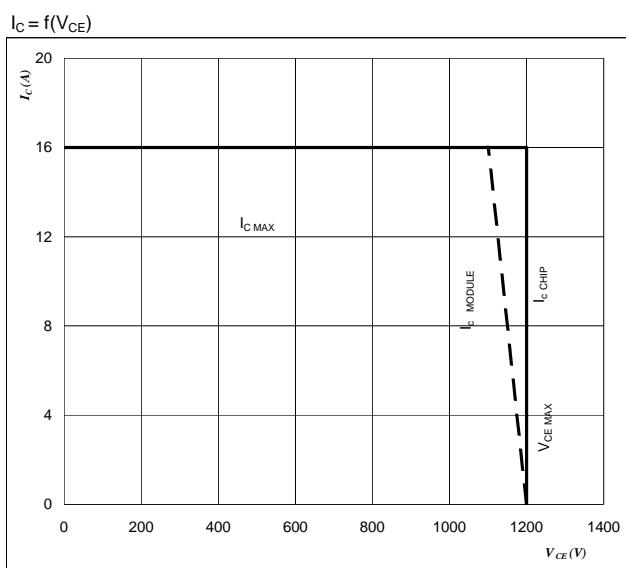

**At**

V<sub>CE</sub> ≤ 1200 V

T<sub>j</sub> = 175 °C

**Figure 29**  
**Reverse bias safe operating area**

IGBT



At

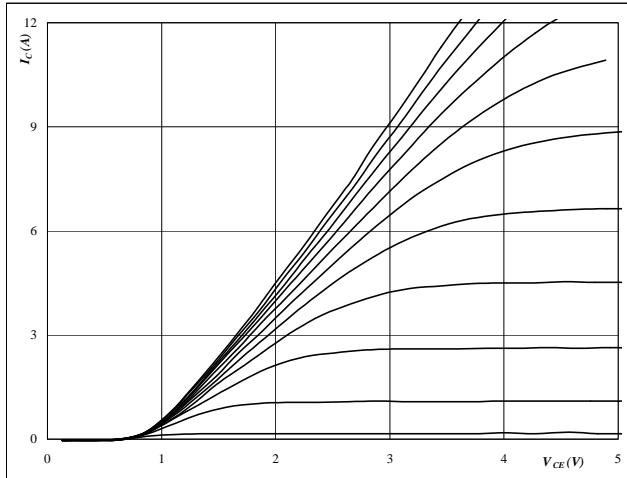
$$T_j = T_{j\max} - 25 \quad ^\circ\text{C}$$

$$U_{ccminus} = U_{ccplus}$$

Switching mode : 3 level switching

## Brake

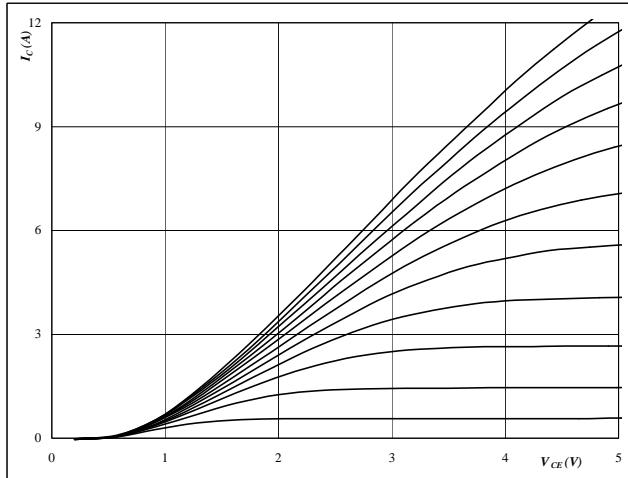
**Figure 1**  
**Typical output characteristics**  
 $I_C = f(V_{CE})$



**At**  
 $t_p = 250 \mu s$   
 $T_j = 25^\circ C$   
 $V_{GE}$  from 7 V to 17 V in steps of 1 V

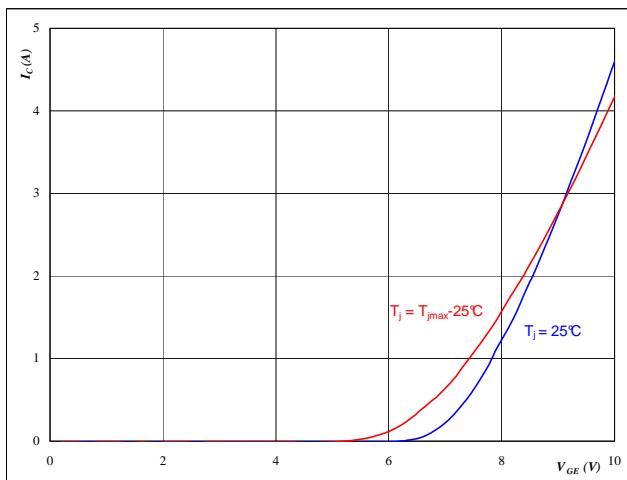
Brake IGBT

**Figure 2**  
**Typical output characteristics**  
 $I_C = f(V_{CE})$



**At**  
 $t_p = 250 \mu s$   
 $T_j = 150^\circ C$   
 $V_{GE}$  from 7 V to 17 V in steps of 1 V

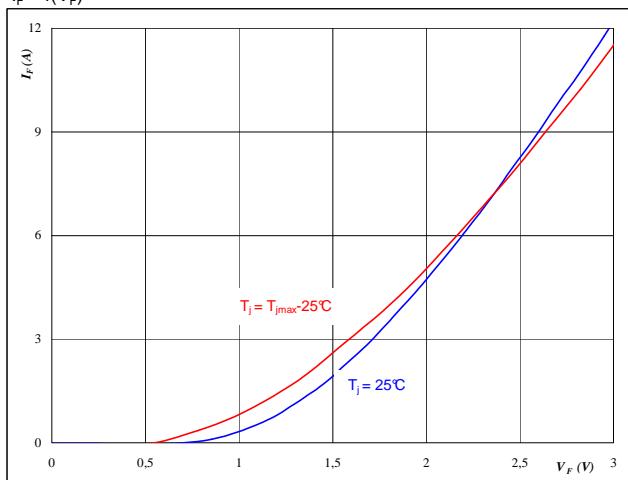
**Figure 3**  
**Typical transfer characteristics**  
 $I_C = f(V_{GE})$



**At**  
 $t_p = 250 \mu s$   
 $V_{CE} = 10 V$

Brake IGBT

**Figure 4**  
**Typical diode forward current as a function of forward voltage**  
 $I_F = f(V_F)$



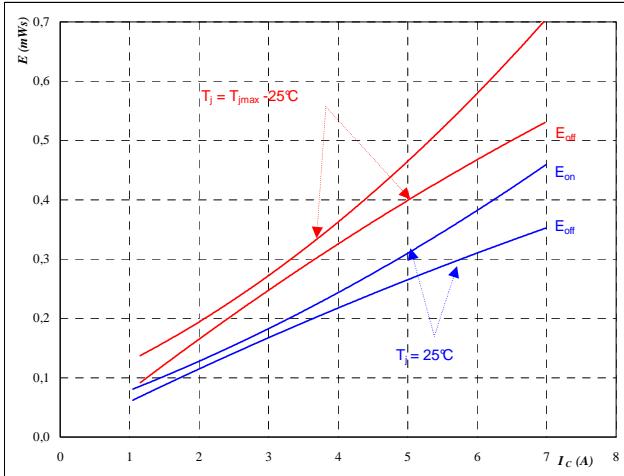
**At**  
 $t_p = 250 \mu s$

## Brake

**Figure 5**

**Typical switching energy losses  
as a function of collector current**

$$E = f(I_C)$$



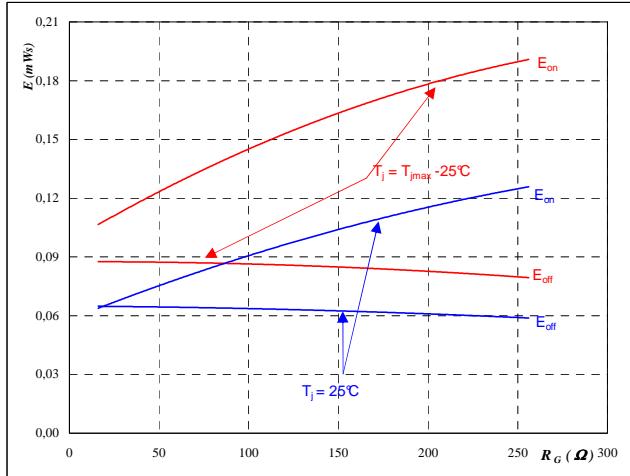
With an inductive load at

$$\begin{aligned} T_j &= 25/150 \quad ^\circ C \\ V_{CE} &= 600 \quad V \\ V_{GE} &= \pm 15 \quad V \\ R_{gon} &= 64 \quad \Omega \\ R_{goff} &= 64 \quad \Omega \end{aligned}$$

**Brake IGBT**
**Figure 6**

**Typical switching energy losses  
as a function of gate resistor**

$$E = f(R_G)$$



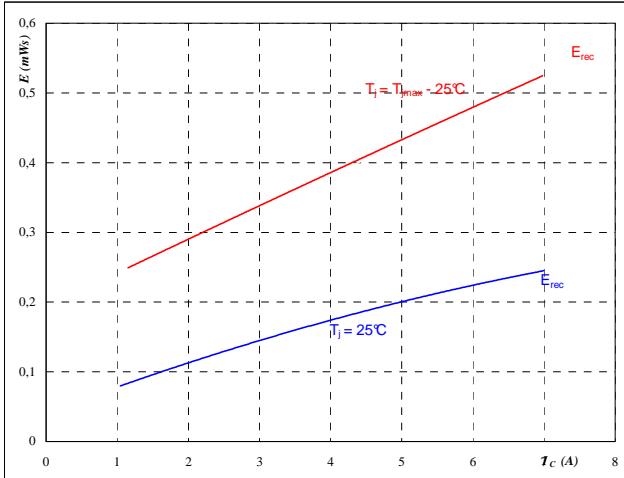
With an inductive load at

$$\begin{aligned} T_j &= 25/150 \quad ^\circ C \\ V_{CE} &= 600 \quad V \\ V_{GE} &= \pm 15 \quad V \\ I_C &= 1 \quad A \end{aligned}$$

**Figure 7**

**Typical reverse recovery energy loss  
as a function of collector current**

$$E_{rec} = f(I_C)$$



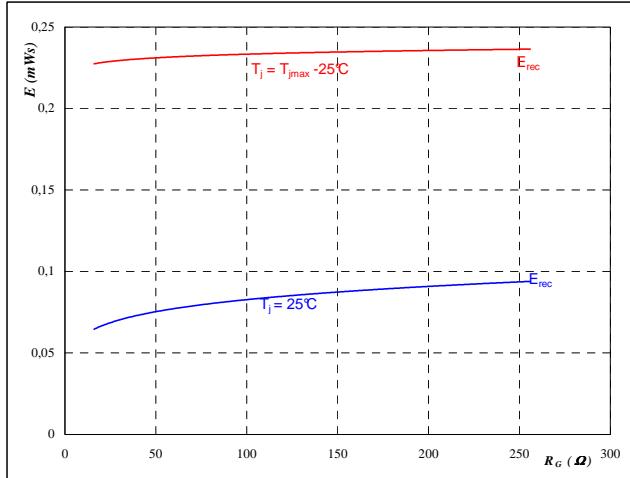
With an inductive load at

$$\begin{aligned} T_j &= 25/150 \quad ^\circ C \\ V_{CE} &= 600 \quad V \\ V_{GE} &= \pm 15 \quad V \\ R_{gon} &= 64 \quad \Omega \end{aligned}$$

**Brake FWD**
**Figure 8**

**Typical reverse recovery energy loss  
as a function of gate resistor**

$$E_{rec} = f(R_G)$$



With an inductive load at

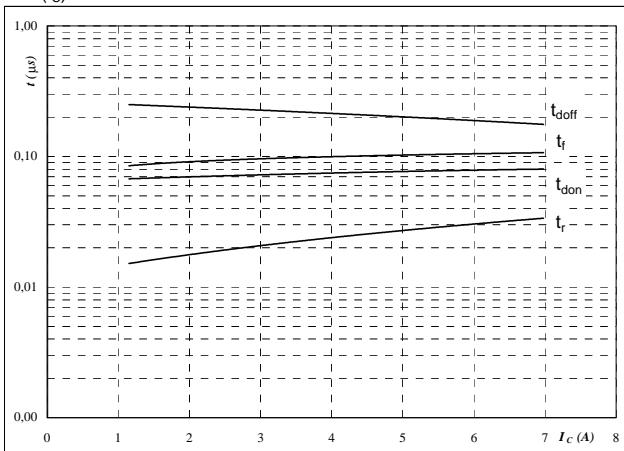
$$\begin{aligned} T_j &= 25/150 \quad ^\circ C \\ V_{CE} &= 600 \quad V \\ V_{GE} &= \pm 15 \quad V \\ I_C &= 1 \quad A \end{aligned}$$

## Brake

**Figure 9**

**Typical switching times as a function of collector current**

$$t = f(I_C)$$



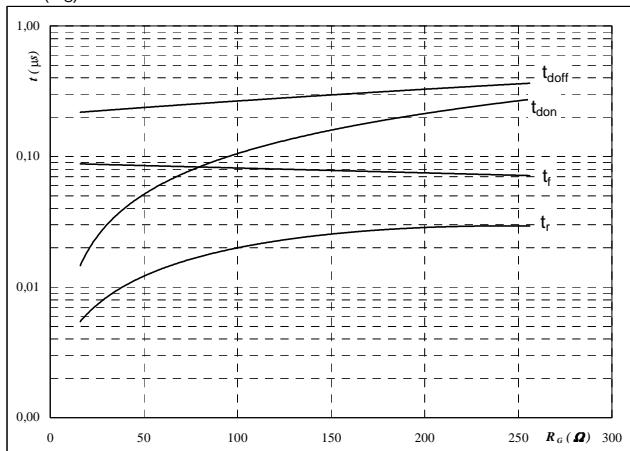
With an inductive load at

T <sub>j</sub> =	150	°C
V <sub>CE</sub> =	600	V
V <sub>GE</sub> =	±15	V
R <sub>gon</sub> =	64	Ω
R <sub>goff</sub> =	64	Ω

**Brake IGBT**
**Figure 10**

**Typical switching times as a function of gate resistor**

$$t = f(R_G)$$



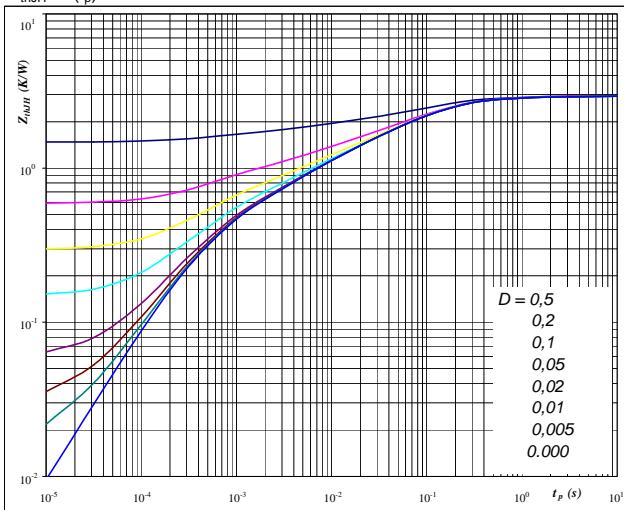
With an inductive load at

T <sub>j</sub> =	150	°C
V <sub>CE</sub> =	600	V
V <sub>GE</sub> =	±15	V
I <sub>C</sub> =	1	A

**Figure 11**
**Brake IGBT**

**IGBT transient thermal impedance as a function of pulse width**

$$Z_{thJH} = f(t_p)$$



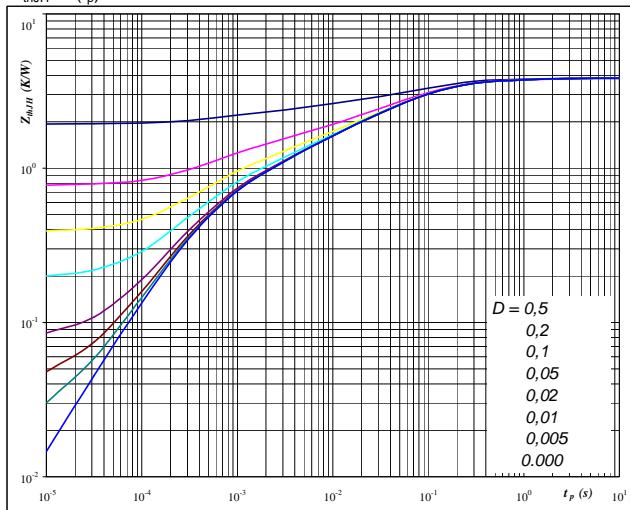
**At**  
Thermal grease  
 $R_{thJH} = 2,95$  K/W

**D =**  
tp / T  
Phase change material  
 $R_{thJH} = 2,55$  K/W

**Figure 12**
**Brake FWD**

**FWD transient thermal impedance as a function of pulse width**

$$Z_{thJH} = f(t_p)$$



**At**  
Thermal grease  
 $R_{thJH} = 3,86$  K/W

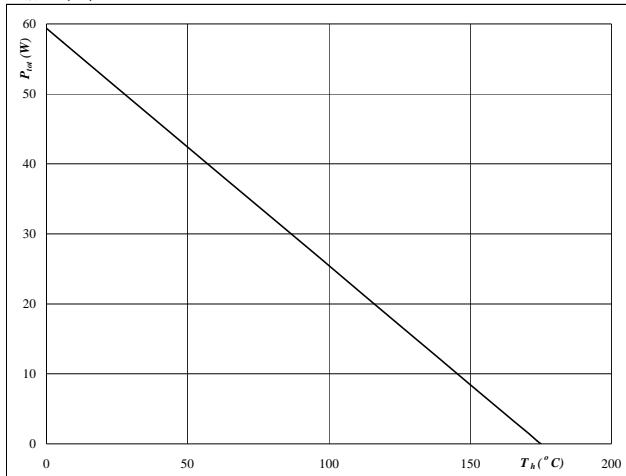
**D =**  
tp / T  
Phase change material  
 $R_{thJH} = 3,33$  K/W

## Brake

**Figure 13**

**Power dissipation as a function of heatsink temperature**

$$P_{\text{tot}} = f(T_h)$$

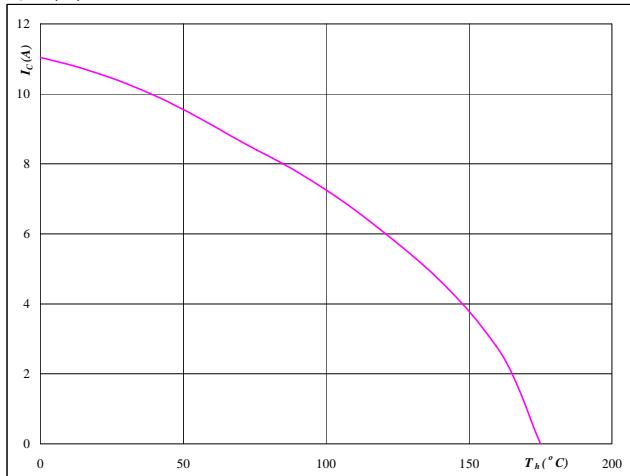

**At**

$$T_j = 175 \quad {}^\circ\text{C}$$

**Brake IGBT**
**Figure 14**

**Collector current as a function of heatsink temperature**

$$I_C = f(T_h)$$


**At**

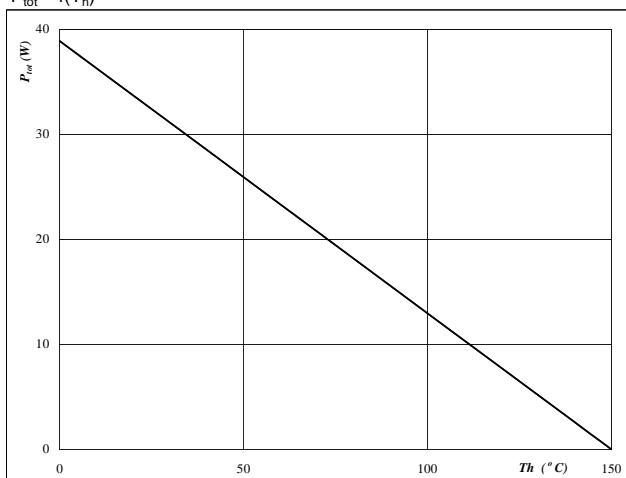
$$T_j = 175 \quad {}^\circ\text{C}$$

$$V_{GE} = 15 \quad \text{V}$$

**Figure 15**

**Power dissipation as a function of heatsink temperature**

$$P_{\text{tot}} = f(T_h)$$

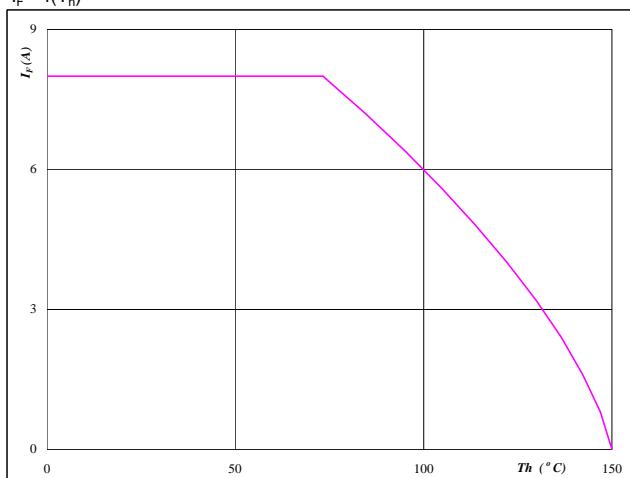

**At**

$$T_j = 150 \quad {}^\circ\text{C}$$

**Brake FWD**
**Figure 16**

**Forward current as a function of heatsink temperature**

$$I_F = f(T_h)$$


**At**

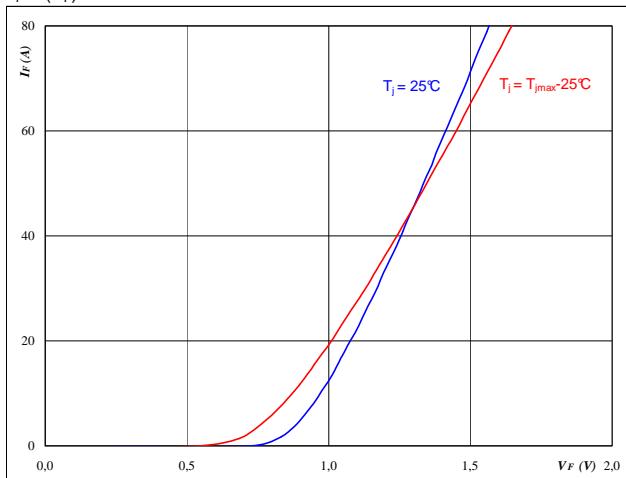
$$T_j = 150 \quad {}^\circ\text{C}$$

## Input Rectifier Bridge

**Figure 1**

**Typical diode forward current as a function of forward voltage**

$$I_F = f(V_F)$$

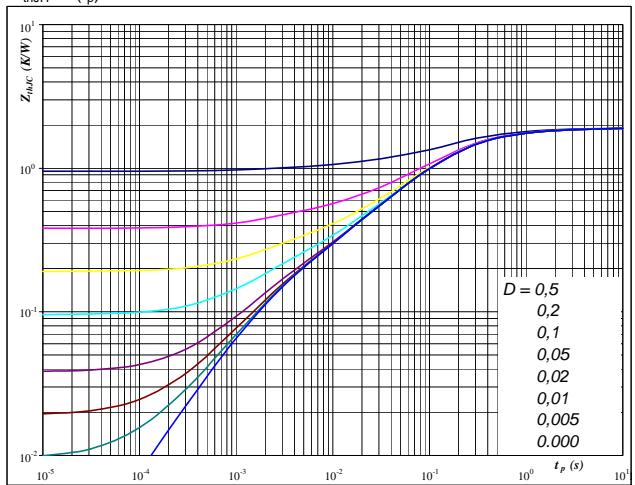

**At**

$$t_p = 250 \mu\text{s}$$

**Rectifier diode**
**Figure 2**

**Diode transient thermal impedance as a function of pulse width**

$$Z_{thJH} = f(t_p)$$

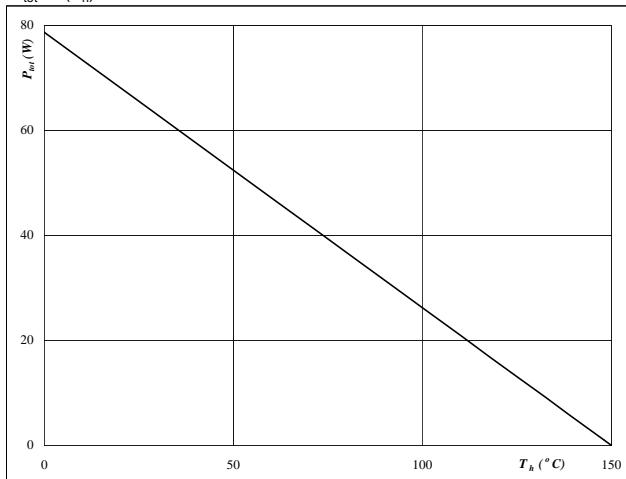

**At**

$$D = \frac{t_p / T}{\text{Thermal grease}} \quad R_{thJH} = 1.89 \text{ K/W} \quad D = \frac{t_p / T}{\text{Phase change material}} \quad R_{thJH} = 1.68 \text{ K/W}$$

**Figure 3**

**Power dissipation as a function of heatsink temperature**

$$P_{tot} = f(T_h)$$

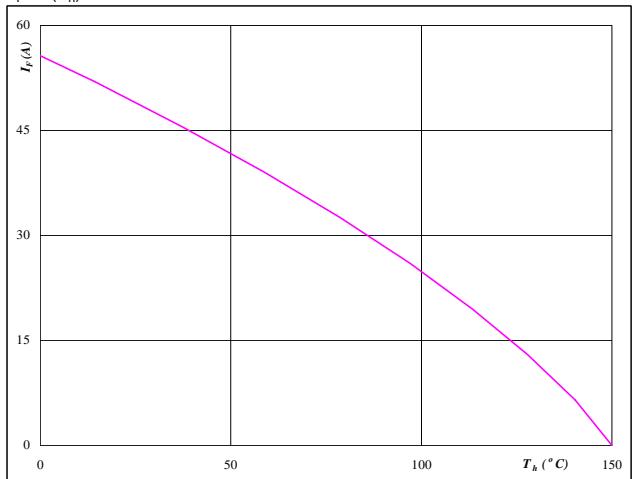

**At**

$$T_j = 150^\circ\text{C}$$

**Rectifier diode**
**Figure 4**

**Forward current as a function of heatsink temperature**

$$I_F = f(T_h)$$


**At**

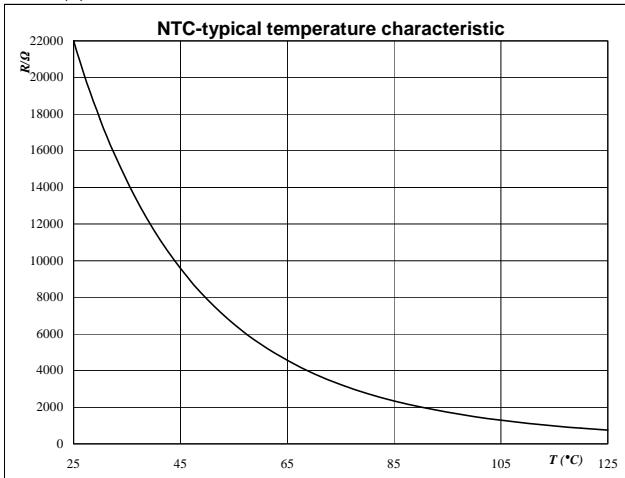
$$T_j = 150^\circ\text{C}$$

## Thermistor

**Figure 1**

**Typical NTC characteristic  
as a function of temperature**

$$R_T = f(T)$$


**Thermistor**
**Figure 2**

**Typical NTC resistance values**

$$R(T) = R_{25} \cdot e^{\left( B_{25/100} \left( \frac{1}{T} - \frac{1}{T_{25}} \right) \right)} \quad [\Omega]$$

T [°C]	R <sub>nom</sub> [Ω]	R <sub>min</sub> [Ω]	R <sub>max</sub> [Ω]	△R/R [±%]
-55	2089434,5	1506495,4	2672373,6	27,9
0	71804,2	59724,4	83884	16,8
10	43780,4	37094,4	50466,5	15,3
20	27484,6	23684,6	31284,7	13,8
25	22000	19109,3	24890,7	13,1
30	17723,3	15512,2	19934,4	12,5
60	5467,9	4980,6	5955,1	8,9
70	3848,6	3546	4151,1	7,9
80	2757,7	2568,2	2947,1	6,9
90	2008,9	1889,7	2128,2	5,9
<b>100</b>	<b>1486,1</b>	<b>1411,8</b>	<b>1560,4</b>	<b>5</b>
150	400,2	364,8	435,7	8,8

## Switching Definitions Output Inverter

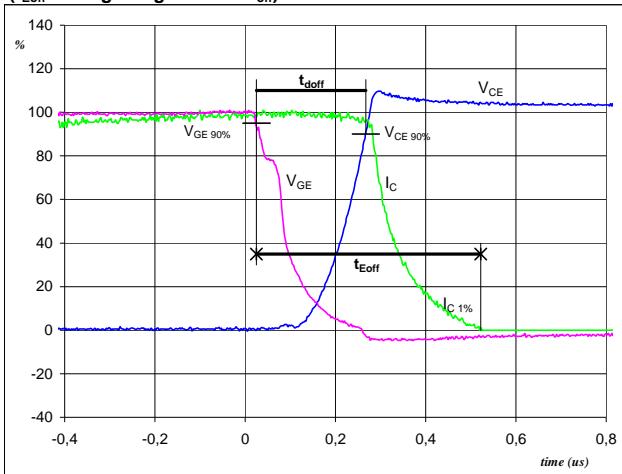
### General conditions

$T_j$	=	125 °C
$R_{gon}$	=	32 Ω
$R_{goff}$	=	32 Ω

**Figure 1**

Output inverter IGBT  
Turn-off Switching Waveforms & definition of  $t_{doff}$ ,  $t_{Eoff}$

( $t_{Eoff}$  = integrating time for  $E_{off}$ )



$V_{GE}(0\%) = -15 \text{ V}$

$V_{GE}(100\%) = 15 \text{ V}$

$V_C(100\%) = 600 \text{ V}$

$I_C(100\%) = 8 \text{ A}$

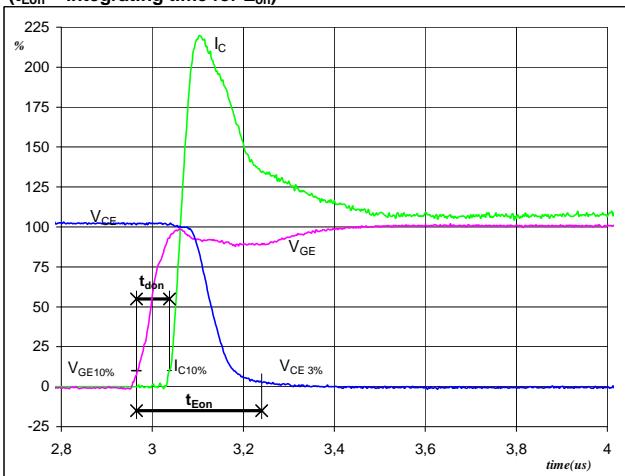
$t_{doff} = 0,24 \mu\text{s}$

$t_{Eoff} = 0,50 \mu\text{s}$

**Figure 2**

Output inverter IGBT  
Turn-on Switching Waveforms & definition of  $t_{don}$ ,  $t_{Eon}$

( $t_{Eon}$  = integrating time for  $E_{on}$ )



$V_{GE}(0\%) = -15 \text{ V}$

$V_{GE}(100\%) = 15 \text{ V}$

$V_C(100\%) = 600 \text{ V}$

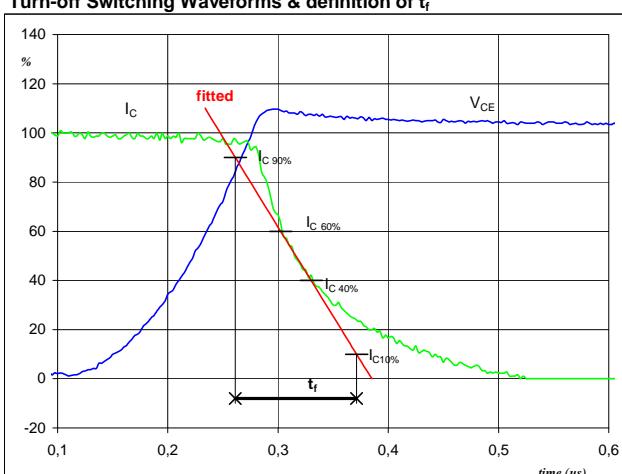
$I_C(100\%) = 8 \text{ A}$

$t_{don} = 0,07 \mu\text{s}$

$t_{Eon} = 0,27 \mu\text{s}$

**Figure 3**

Output inverter IGBT  
Turn-off Switching Waveforms & definition of  $t_f$



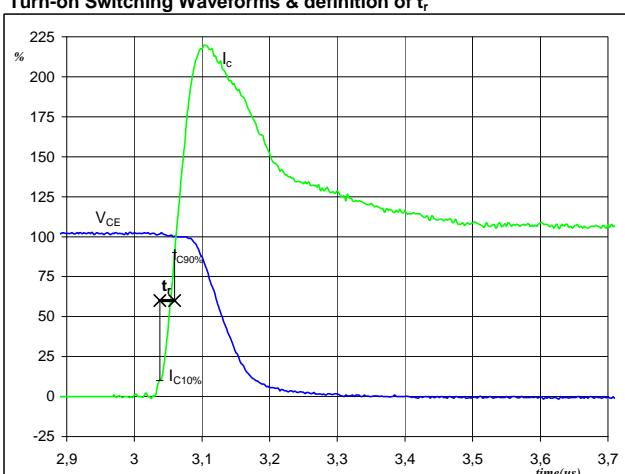
$V_C(100\%) = 600 \text{ V}$

$I_C(100\%) = 8 \text{ A}$

$t_f = 0,11 \mu\text{s}$

**Figure 4**

Output inverter IGBT  
Turn-on Switching Waveforms & definition of  $t_r$



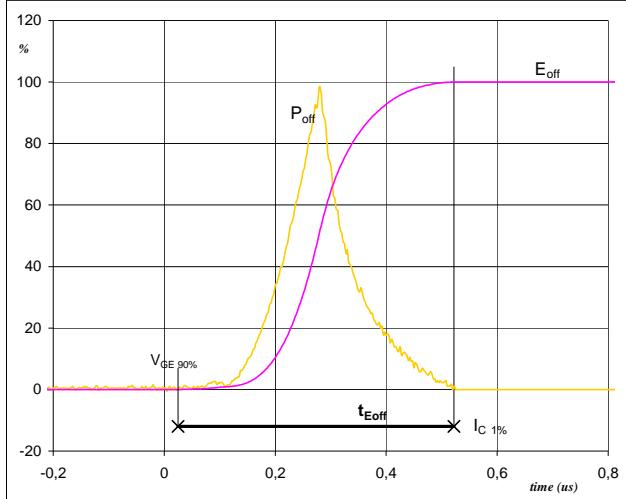
$V_C(100\%) = 600 \text{ V}$

$I_C(100\%) = 8 \text{ A}$

$t_r = 0,02 \mu\text{s}$

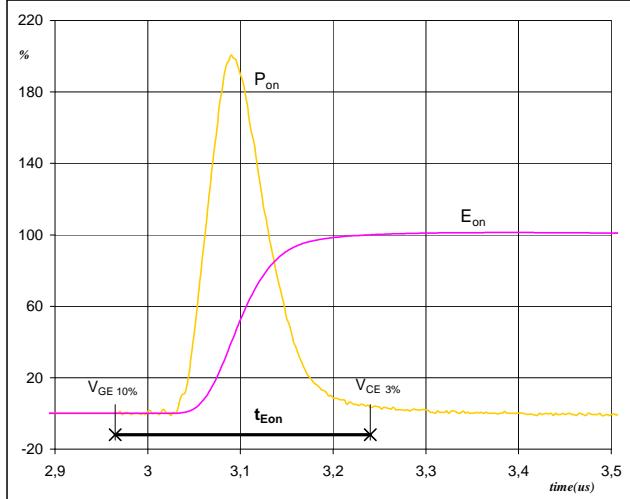
## Switching Definitions Output Inverter

**Figure 5** Output inverter IGBT  
**Turn-off Switching Waveforms & definition of  $t_{Eoff}$**



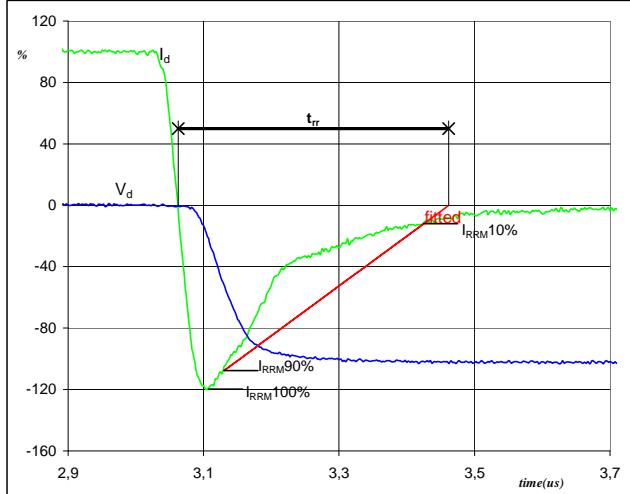
$P_{off} (100\%) =$  4,93 kW  
 $E_{off} (100\%) =$  0,62 mJ  
 $t_{Eoff} =$  0,50  $\mu s$

**Figure 6** Output inverter IGBT  
**Turn-on Switching Waveforms & definition of  $t_{Eon}$**



$P_{on} (100\%) =$  4,93 kW  
 $E_{on} (100\%) =$  0,75 mJ  
 $t_{Eon} =$  0,27  $\mu s$

**Figure 7** Output inverter IGBT  
**Turn-off Switching Waveforms & definition of  $t_{rr}$**



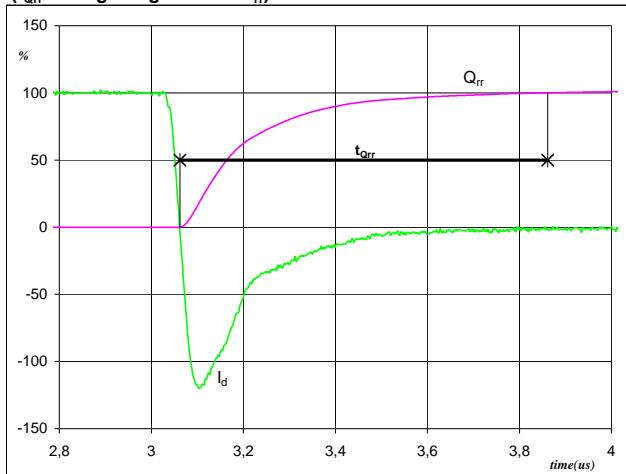
$V_d (100\%) =$  600 V  
 $I_d (100\%) =$  8 A  
 $I_{RRM} (100\%) =$  -10 A  
 $t_{rr} =$  0,38  $\mu s$

## Switching Definitions Output Inverter

**Figure 8**

Output inverter FWD

**Turn-on Switching Waveforms & definition of  $t_{Qrr}$**   
 $(t_{Qrr} = \text{integrating time for } Q_{rr})$

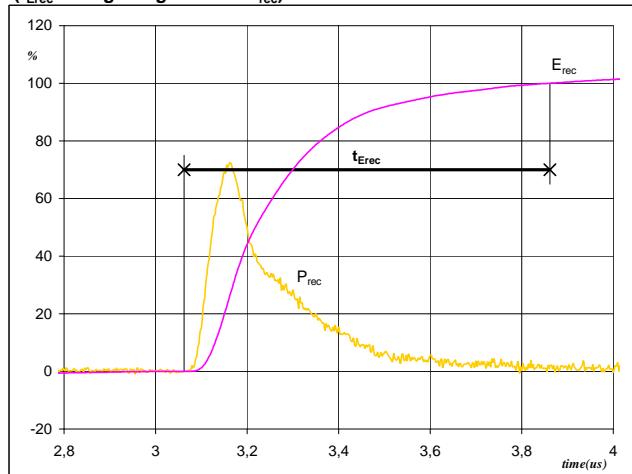


$$\begin{aligned} I_d(100\%) &= 8 \quad \text{A} \\ Q_{rr}(100\%) &= 1,57 \quad \mu\text{C} \\ t_{Qrr} &= 0,80 \quad \mu\text{s} \end{aligned}$$

**Figure 10**

Output inverter FWD

**Turn-on Switching Waveforms & definition of  $t_{Erec}$**   
 $(t_{Erec} = \text{integrating time for } E_{rec})$



$$\begin{aligned} P_{rec}(100\%) &= 4,93 \quad \text{kW} \\ E_{rec}(100\%) &= 0,63 \quad \text{mJ} \\ t_{Erec} &= 0,80 \quad \mu\text{s} \end{aligned}$$

## Ordering Code and Marking - Outline - Pinout

## Ordering Code &amp; Marking

Version	Ordering Code	in DataMatrix as	in packaging barcode as
Without thermal paste 17mm housing	V23990-P588-A88-PM	P588-A88	P588-A88
Without thermal paste 17mm housing	V23990-P588-C88-PM	P588-C88	P588-C88

## Features

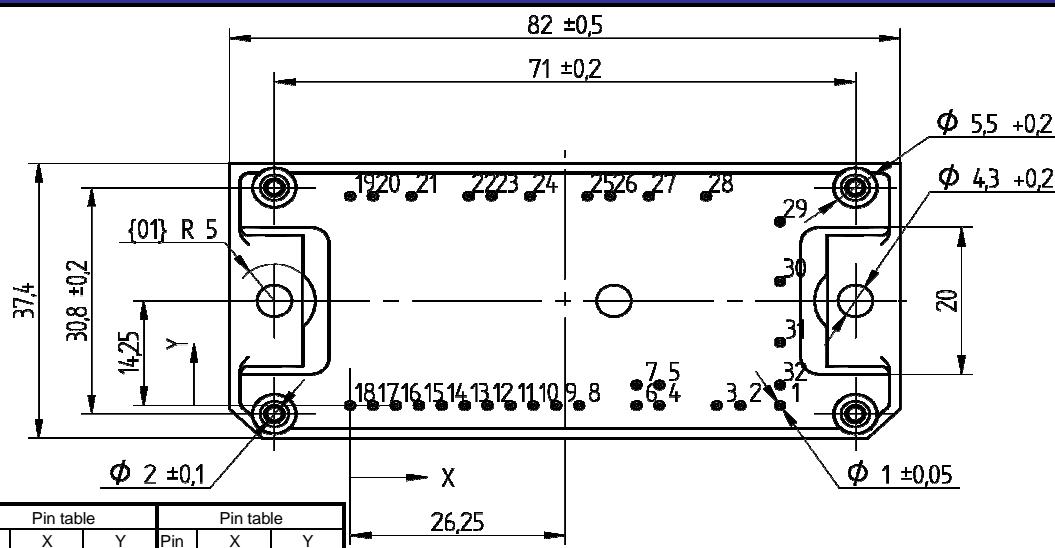
	A version	C version
Rectifier	3-leg	3-leg
Break IGBT	✓	w/o pin 1,31,32
Break FWD	✓	
Inverter IGBT	✓	✓
Inverter FWD	✓	✓

## Outline

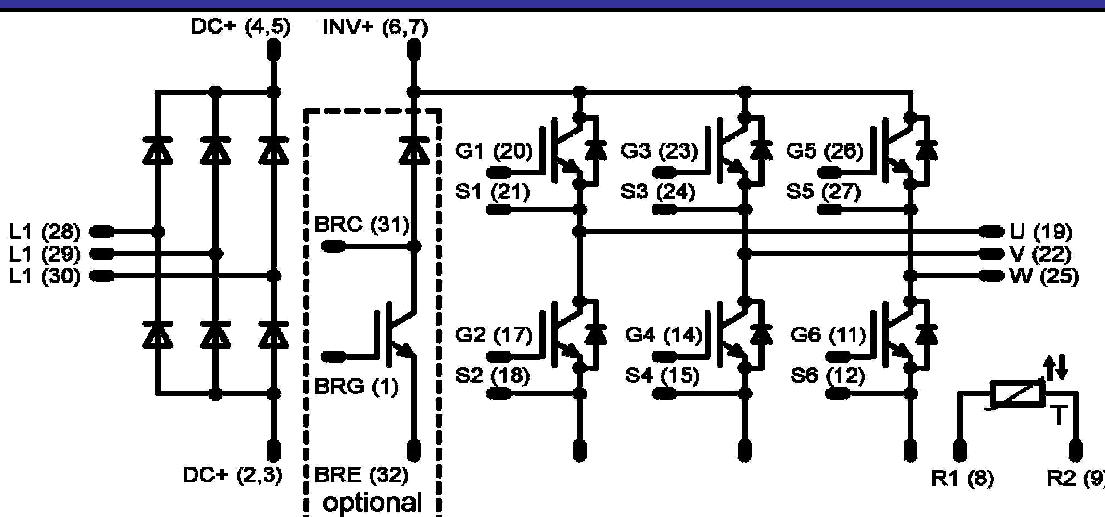
Pin table		
Pin	X	Y
1	52,55	0
2	47,7	0
3	44,8	0
4	37,8	0
5	37,8	2,8
6	35	0
7	35	2,8
8	28	0
9	25,2	0
10	22,4	0
11	19,6	0
12	16,8	0
13	14	0
14	11,2	0
15	8,4	0
16	5,6	0
17	2,8	0
18	0	0
19	0	28,5
20	2,8	28,5
21	7,5	28,5
22	14,5	28,5
23	17,3	28,5
24	22	28,5

Pin table			Pin table		
Pin	X	Y	Pin	X	Y
25	29	28,5	29	52,55	25
26	31,8	28,5	30	52,55	16,9
27	36,5	28,5	31	52,55	8,6
28	43,5	28,5	32	52,55	2,8



## Pinout



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2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.