MOSFETs Silicon N-channel MOS (U-MOS IV)

# TK8S06K3L

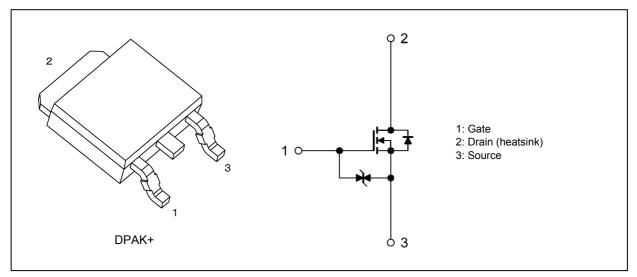
### 1. Applications

- Automotive
- Motor Drivers
- DC-DC Converters
- Switching Voltage Regulators

### 2. Features

- (1) Low drain-source on-resistance:  $R_{DS(ON)} = 43 \text{ m}\Omega$  (typ.) (V<sub>GS</sub> = 10 V)
- (2) Low leakage current:  $I_{DSS} = 10 \ \mu A \ (max) \ (V_{DS} = 60 \ V)$
- (3) Enhancement mode:  $V_{\rm th}$  = 2.0 to 3.0 V (V\_{\rm DS} = 10 V,  $I_{\rm D}$  = 1 mA)

### 3. Packaging and Internal Circuit



### 4. Absolute Maximum Ratings (Note) ( $T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics			Symbol	Rating	Unit
Drain-source voltage			V <sub>DSS</sub>	60	V
Gate-source voltage			V <sub>GSS</sub>	±20	
Drain current (DC)		(Note 1)	I <sub>D</sub>	8	A
Drain current (pulsed)		(Note 1)	I <sub>DP</sub>	16	
Power dissipation	(T <sub>c</sub> = 25°C)		PD	25	W
Single-pulse avalanche energy		(Note 2)	E <sub>AS</sub>	16	mJ
Avalanche current			I <sub>AR</sub>	8	Α
Channel temperature		(Note 3)	T <sub>ch</sub>	175	°C
Storage temperature		(Note 3)	T <sub>stg</sub>	-55 to 175	]

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### 5. Thermal Characteristics

Characteristics	Symbol	Max	Unit
Channel-to-case thermal resistance	R <sub>th(ch-c)</sub>	6	°C/W

Note 1: Ensure that the channel temperature does not exceed 175°C.

Note 2: V<sub>DD</sub> = 25 V, T<sub>ch</sub> = 25°C (initial), L = 331  $\mu$ H, R<sub>G</sub> = 1  $\Omega$ , I<sub>AR</sub> = 8 A

Note 3: The definitions of the absolute maximum channel and storage temperatures are qualified per AEC-Q101.

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.

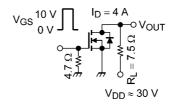
#### 6. Electrical Characteristics

### 6.1. Static Characteristics (T<sub>a</sub> = 25°C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I <sub>GSS</sub>	$V_{GS}$ = ±16 V, $V_{DS}$ = 0 V	—		±10	μA
Drain cut-off current	I <sub>DSS</sub>	V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0 V	—	_	10	
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	60		_	V
	V <sub>(BR)DSX</sub>	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = -20 V	40	_	_	
Gate threshold voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	_	3.0	
Drain-source on-resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 6 V, I <sub>D</sub> = 4 A	_	51	80	mΩ
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 4 A	—	43	54	

### 6.2. Dynamic Characteristics ( $T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	400	_	pF
Reverse transfer capacitance	C <sub>rss</sub>		_	40	—	
Output capacitance	C <sub>oss</sub>		_	80	_	
Switching time (rise time)	t <sub>r</sub>	See Figure 6.2.1.	_	7	_	ns
Switching time (turn-on time)	t <sub>on</sub>			14	_	
Switching time (fall time)	t <sub>f</sub>		_	4	_	
Switching time (turn-off time)	t <sub>off</sub>		_	18	_	



Duty  $\leq$  1 %,  $t_W =$  10  $\mu s$ 

Fig. 6.2.1 Switching Time Test Circuit

### 6.3. Gate Charge Characteristics ( $T_a = 25^{\circ}C$ unless otherwise specified)

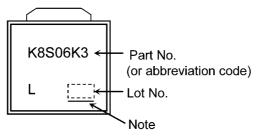
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus gate-drain)	Qg	$V_{DD} \approx 48 \text{ V}, \text{ V}_{GS}$ = 10 V, I <sub>D</sub> = 8 A	—	10	—	nC
Gate-source charge	Q <sub>gs</sub>			7	_	
Gate-drain charge	Q <sub>gd</sub>		_	3	_	

### 6.4. Source-Drain Characteristics (T<sub>a</sub> = 25°C unless otherwise specified)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Reverse drain current (DC)	(Note 4)	I <sub>DR</sub>	—	_	_	8	A
Reverse drain current (pulsed)	(Note 4)	I <sub>DRP</sub>	—	_	—	16	
Diode forward voltage		V <sub>DSF</sub>	I <sub>DR</sub> = 8 A, V <sub>GS</sub> = 0 V	_	_	-1.2	V
Reverse recovery time		t <sub>rr</sub>	I <sub>DR</sub> = 8 A, V <sub>GS</sub> = 0 V		32		ns
Reverse recovery charge		Q <sub>rr</sub>	-dI <sub>DR</sub> /dt = 50 A/μs	-	18	_	nC

Note 4: Ensure that the channel temperature does not exceed 175°C.

### 7. Marking (Note)



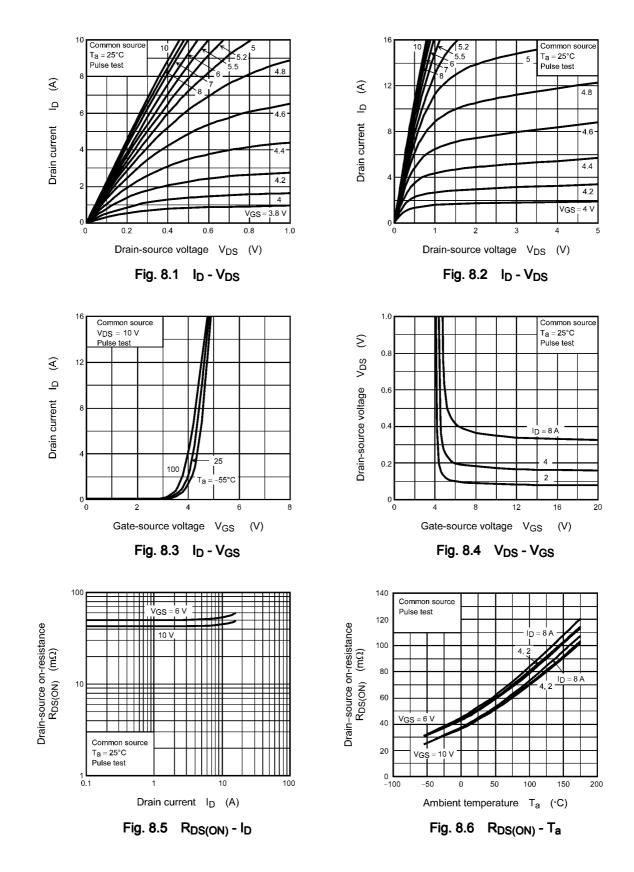
#### Fig. 7.1 Marking

Note: A line under a Lot No. identifies the indication of product Labels. Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

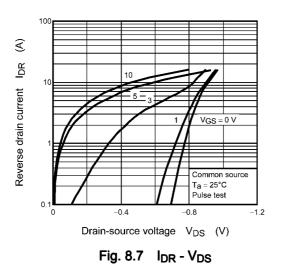
Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.

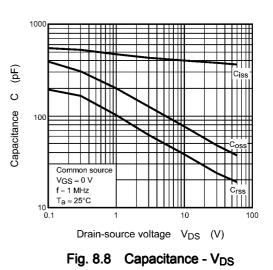
The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

### 8. Characteristics Curves (Note)



S





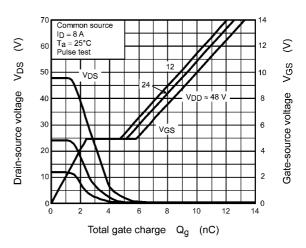
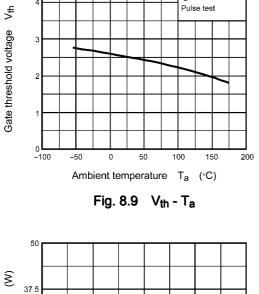
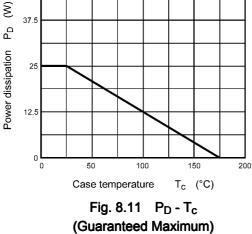


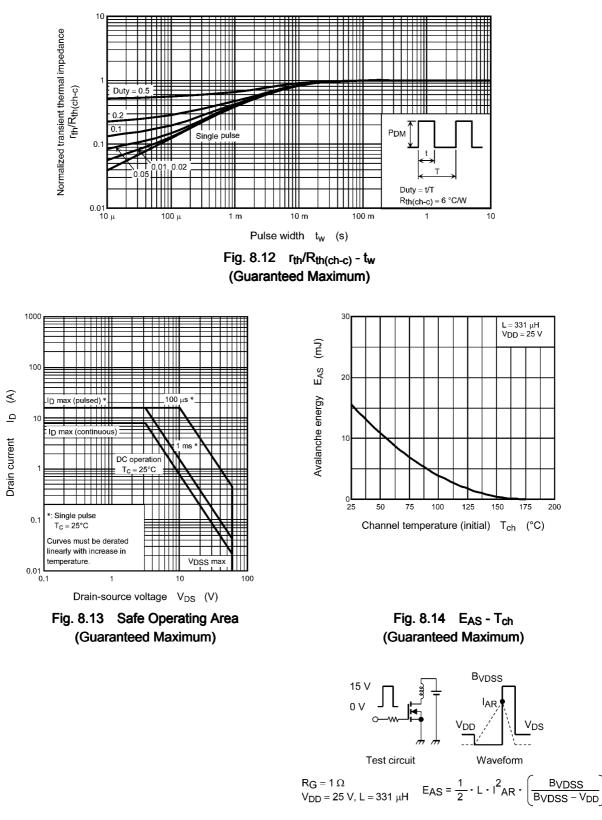
Fig. 8.10 Dynamic Input/Output Characteristics

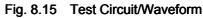


Common source  $V_{DS} = 10 V$  $I_{D} = 1 mA$ 

Pulse test





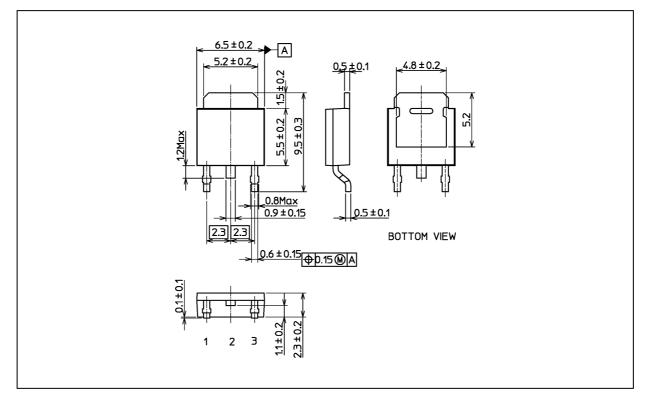


Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

### TK8S06K3L

### Package Dimensions

Unit: mm



#### Weight: 0.36 g (typ.)

	Package Name(s)	
TOSHIBA: 2-7M1A		
Nickname: DPAK+		

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