

3-phase brushless motor pre-driver



BD63000MUV

●General Description

BD63000MUV is pre-driver of 3-phase brushless motor. It generates a driving signal from the Hall sensor and drives PWM through the input control signal. Since there is a built-in booster circuit, Nch-Nch MOS transistors can be used on the external power transistor. In addition, the power supply can use 12V or 24V and it has various controls and protection functions built-in, making it useful for a variety of purposes. Because it adopts small packages, it can also be used on small diameter motors.

●Features

- Built-in 120° commutation logic circuit.
- Driving with Nch-Nch MOS transistors.
- PWM control mode (low side arm switching).
- Built-in power-saving circuit.
- CW/CCW function.
- Short brake function.
- FG output (Open drain).
- Built-in protection circuit for current limiting, overheating, under voltage, over voltage, motor lock.

●Applications

- OA machines.
- Other general civil equipments.

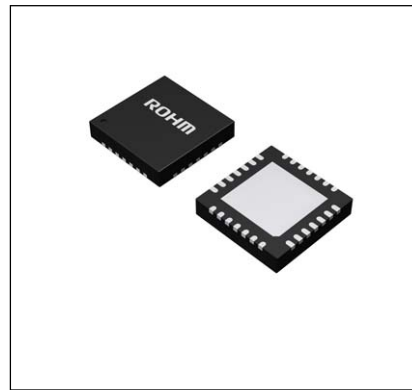
●Key Specifications

- Power supply voltage rating: 30V
- Operating temperature range: -40°C to +85°C
- Stand-by current: 1.2mA(Max.)
- Range of in-phase input voltage for hall input: $V_{REG}-1.7V(\text{Max.})$
- Current limit detect voltage: $0.2V \pm 10\%$
- UVLO Lock out voltage: 6.0V(Typ.)
- OVLO Lock out voltage 1: 16.0V(Typ.)
- OVLO Lock out voltage 2: 28.5V(Typ.)

●Package

VQFN028V5050

W(Typ.) x D(Typ.) x H(Max.)
5.00mm x 5.00mm x 1.00mm



VQFN028V5050

●Typical Application Circuit

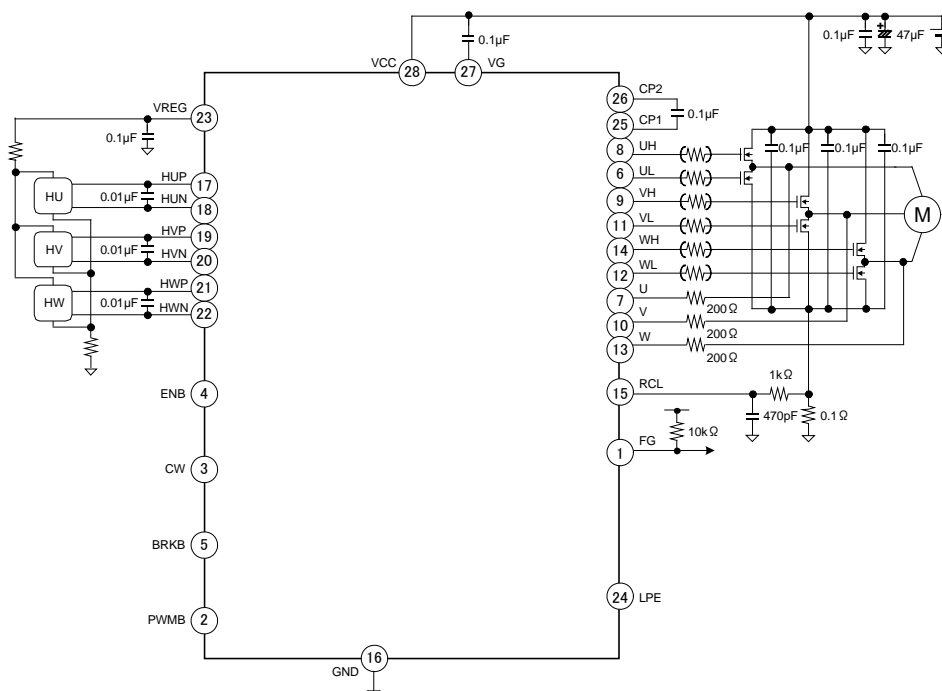


Fig.1 Application circuit

○Product structure : Silicon monolithic integrated circuit ○This product is not designed protection against radioactive rays

● Pin Configuration

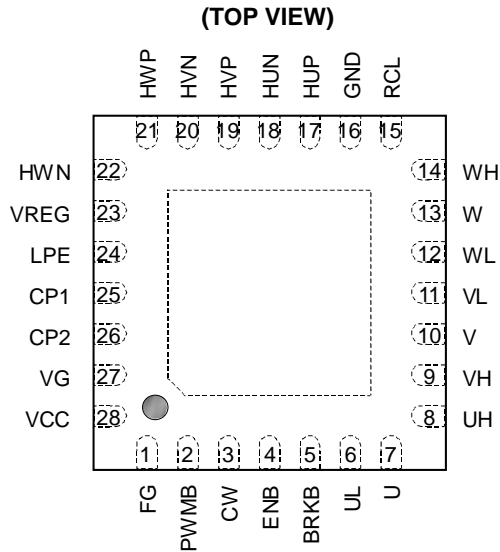


Fig.2 Pin configuration

● Block Diagram

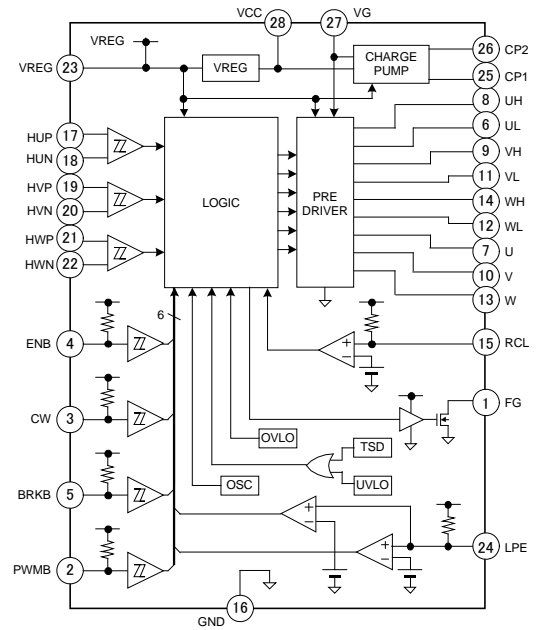


Fig.3 Block diagram

● Pin Description

Pin No.	Pin name	Function	Pin No.	Pin name	Function
1	FG	FG output	15	RCL	Detect voltage input for over current
2	PWMB	PWM input (negative logic)	16	GND	GND
3	CW	CW/CCW input (H: CW, L: CCW)	17	HUP	U phase Hall input +
4	ENB	Enable (negative logic)	18	HUN	U phase Hall input -
5	BRKB	Brake input (negative logic)	19	HVP	V phase Hall input +
6	UL	U phase lower output	20	HVN	V phase Hall input -
7	U	U phase output feedback	21	HWP	W phase Hall input +
8	UH	U phase upper output	22	HWN	W phase Hall input -
9	VH	V phase upper output	23	VREG	VREG output (turn off at stand-by)
10	V	V phase output feedback	24	LPE	Setting about motor lock protection and OLVO (H/M/L input)
11	VL	V phase lower output	25	CP1	Charge pump setting 1
12	WL	W phase lower output	26	CP2	Charge pump setting 2
13	W	W phase output feedback	27	VG	Charge pump output
14	WH	W phase upper output	28	VCC	Power supply

●Description of operation

1) Commutation logic (120° commutation)

Truth table

HU	HV	HW	CW (CW=H or open)						CCW (CW=L)						FG
			UH	UL	VH	VL	WH	WL	UH	UL	VH	VL	WH	WL	
H	L	H	$\overline{\text{PWM}}$	PWM*	H	L	L	L	H	L	$\overline{\text{PWM}}$	PWM*	L	L	L
H	L	L	$\overline{\text{PWM}}$	PWM*	L	L	H	L	H	L	L	L	$\overline{\text{PWM}}$	PWM*	L
H	H	L	L	L	$\overline{\text{PWM}}$	PWM*	H	L	L	L	H	L	$\overline{\text{PWM}}$	PWM*	L
L	H	L	H	L	$\overline{\text{PWM}}$	PWM*	L	L	$\overline{\text{PWM}}$	PWM*	H	L	L	L	Hi-z
L	H	H	H	L	L	L	$\overline{\text{PWM}}$	PWM*	$\overline{\text{PWM}}$	PWM*	L	L	H	L	Hi-z
L	L	H	L	L	H	L	$\overline{\text{PWM}}$	PWM*	L	L	$\overline{\text{PWM}}$	PWM*	H	L	Hi-z

* When PWMB=L, PWM="H", When PWMB=H, PWM="L".

2) Enable input terminal (ENB)

Output of each phase can be set to ON/OFF (negative logic) through ENB terminal. When applied voltage is below V_{ENA} , the motor is driven (enable), when applied voltage is V_{STBY} , the motor suspends (stand-by). Stand-by mode has precedence to other control input signal, VREG output will be OFF. In addition, ENB terminal is pulled up by internal power supply through a resistance of $100\text{k}\Omega(\text{Typ.})\pm 30\text{k}\Omega$.

ENB	Operation
H or open	Stand-by
L	Enable

3) PWM input terminal (PWMB)

Speed can be controlled by inputting PWM signal into PWMB terminal (negative logic). Synchronous rectifier PWM can be realized through lower switching. When PWMB="L", lower output that belongs to Hall input logic is "H". When PWMB="H" or open, lower output is "L". When PWMB="H" or OPEN status is detected $104\mu\text{s}(\text{Typ.})$, the synchronous rectifier will be OFF. Synchronous rectifier will be ON through rising and falling edges of subsequent PWMB. Additionally, PWMB terminal is pulled up by VREG through a resistance of $100\text{k}\Omega(\text{Typ.})\pm 30\text{k}\Omega$.

PWMB	Low side output
H or open	L
L	H

4) Brake input terminal (BRKB)

Motor rotation can be quickly stopped by BRKB terminal (negative logic). When BRKB="L", lower output will short brake all "H". When BRKB="H" or OPEN, then short brake action will be released. What's more, BRKB terminal is pulled up by VREG through a resistance of $100\text{k}\Omega(\text{Typ.})\pm 30\text{k}\Omega$.

BRKB	Operation
H or open	Normal
L	Short brake

5) CW/CCW input terminal (CW)

Rotation direction can be switched with CW terminal. When CW="H" or OPEN, the direction is CW, when CW="L", the direction will be CCW. Though we do not recommend switching rotation direction when motor is rotating, if rotation direction is switched when rotating, the rotation speed will become hall frequency, which will be up to less than 40Hz and it will be switched to set rotation direction after the action short brake. And the CW terminal is pulled up by VREG through a resistance of $100\text{k}\Omega(\text{Typ.})\pm 30\text{k}\Omega$.

CW	Direction
H or open	CW
L	CCW

- 6) FG output
FG output is reshaped Hall U phase signal and output through FG terminal. It is not output in stand-by mode. In addition, because FG terminal is output from open drain, please use resistance of about 10kΩ ~ 100kΩ to pulled up from outside.
- 7) Hall input
Hall input amplifier is designed with hysteresis ($\pm 15\text{mV(Typ.)}$) in order to prevent incorrect action due to noise inside. So please set bias current for Hall element to make amplitude of Hall input voltage over minimum input voltage (V_{HALLMIN}). Here, we recommend you to connect the ceramic capacitor with about 100pF ~ 0.01μF between difference input terminals of Hall amplifier. What's more, because the in-phase input voltage range (V_{HALLCM} , 0V ~ VREG-1.7V(Typ.)) is designed for Hall input amplifier, so when bias to Hall element, please set within this range. When Hall inputs all become "H" or "L", detect circuit through Hall input abnormalities to make driver output all "L".
- 8) Booster circuit
There is built-in booster circuit used to drive upper Nch MOS transistor. VG terminals can produce a boost voltage (from the VCC voltage + $2 \times V_F$ voltage (7V(Typ.)) reduced by internal regulator) through connecting capacitors between CP1-CP2 and between VG-VCC. We recommend connected condensers to be over 0.1μF. In addition, because there is built-in protection circuit for insufficient booster, when VG voltage is below V_{GUVON} (VCC+4V (Typ.)), driver outputs will all be "L".
- 9) Current limit circuit (CL circuit)
Output current limit (Current limit: CL) circuit can be formed by connecting a low resistor used for testing current between RCL terminal & GND terminal. When RCL voltage is detected over 0.2V(Typ.), lower output will all become "L". It (32μs(Typ.)) resets automatically after a set amount of time. This action does not synchronize with the action that PWM signal is input into PWMB terminal.
- 10) Thermal Shut Down circuit (TSD circuit)
When chip temperature of driver IC rises and exceeds the set temperature (175°C(Typ.)), the thermal shut down circuit (Thermal Shut Down: TSD) begins working. At this time, the driver outputs all become "L". In addition, the TSD circuit is designed with hysteresis (25°C(Typ.)), therefore, when the chip temperature drops, it will return to normal working condition. Moreover, the purpose of the TSD circuit is to protect driver IC from thermal breakdown, therefore, temperature of this circuit will be over working temperature when it is started up. Thus, thermal design should have sufficient margin, so do not take continuous use and action of the circuit as precondition.
- 11) Under voltage lock out circuit (UVLO circuit)
There is a built-in under voltage lock out circuit (Under Voltage Lock Out: UVLO circuit) used to ensure the lowest power supply voltage for drive IC to work and to prevent error action of IC. When VCC voltage declined to V_{UVL} (6V (Typ.)), all of the driver outputs should be "L". At the same time, UVLO circuit is designed with hysteresis (1V(Typ.)), so when VCC voltage reaches more than V_{UVH} (7V(Typ.)), it will enter normal working condition.
- 12) Over voltage lock out circuit (OVLO circuit)
There is built-in over voltage lock out circuit (Over Voltage Lock Out: OVLO circuit) used to restrain rise of VCC voltage when motor decelerating. When LPE terminal is at "M" and VCC voltage is over V_{OVH1} (16V(Typ.)), and when LPE terminal is at "H" or "M" and VCC voltage of is over V_{OVH2} (28.5V(Typ.)), a certain time (4ms(Typ.)) of short brake action will be conducted. What's more, because OVLO circuit is designed with hysteresis (1V(Typ.)), therefore, when V_{OVH1} is below V_{OVL1} (15V(Typ.)) and when V_{OVH2} is below V_{OVL2} (27.5V(Typ.)), it can return to normal working condition after a certain time of short brake action.
- 13) Motor Lock Protection circuit (MLP circuit)
There is built-in motor lock protection circuit (Motor Lock Protection: MLP), ON/OFF of MLP circuit and OVLO threshold can be set from LPE terminal. Monitor Hall signals, when the LPE = "H" or "M", if Hall signal logic does not change to 1.1sec(Typ.), driver outputs will all be locked as "L". Latch can be released via standby status or through switching BRKB/CW logic. Moreover, when PWMB = "H" or open state is detected for about 15ms, latch can be released by rising and falling edges of subsequent PWMB. However, when LPE = "L", when short brake action (including switching rotation direction) enables or TSD circuit works, MLP circuit doesn't work. And LPE terminal is pulled up by VREG through a resistance of 100kΩ(Typ.) $\pm 30\text{k}\Omega$.

LPE	Monitoring time	OVLO threshold
H or open	1.1sec(Typ.) $\pm 30\%$	$V_{\text{OVH2}}, V_{\text{OVL2}}$
M	1.1sec(Typ.) $\pm 30\%$	$V_{\text{OVH1}}, V_{\text{OVL1}}$
L	Disable	$V_{\text{OVH2}}, V_{\text{OVL2}}$

14) Pre-driver output

Driving signal is output to external output power transistor through drive signal generated from internal logic output drive signal is output for external power supply transistor. Driving voltage of upper gate is VG voltage (Vcc+7V(Typ.)) and driving voltage of lower gate is VREG voltage (5V(Typ.)). In addition, a dead time (1µs ~ 2µs(Typ.)) is designed between driving signals of upper gate and lower gate in order to prevent upper and lower FET from being set to ON synchronously when PWM is rectified synchronously. Due to the influence of the motor's counter electromotive force, the output feedback terminal might swing under GND potential. When excessive current occurs (about over 30mA), incorrect action or even damage might be caused. Therefore, please insert a resistor of about 100Ω ~ 510 Ω before putting into use. Moreover, we recommend you to use output feedback terminal with a slow rate of over 50ns.

15) Control signal sequence

Though we recommend you input control signals of ENB, CW, PWMB, BRKB and LPE terminals after inputting VCC, if input control signals before inputting VCC, there won't be any problem. If LPE terminal is set to "H" or "M" when being started, please be informed that if motor rotation cannot be detected within the set time (edge of FG signal cannot be input), then the MLP circuit starts and motor fails to start. Moreover, the order of priority is set to control signal and IC internal signal. Please refer to the following table.

Priority of control signal

Priority	Input / Internal signals
1 st	ENB, UVLO
2 nd	BRKB↑↓, CW↑↓, PWMB ↓
3 rd	TSD, MLP, HALLERR
4 th	OVLO
5 th	VG_UVLO
6 th	BRKB
7 th	CL
8 th	PWMB, CW

Note) ↑↓ means rising and falling edges of signal.
For signal name, please see state transition diagram.

● Absolute Maximum Ratings (Ta=25°C)

Item	Symbol	Limit	Unit
Power supply voltage	V _{CC}	-0.3 to +30.0* ¹	V
VG voltage	V _G	-0.3 to +38.0* ¹	V
Output voltage of pre-driver	V _(UH,VH,WH)	-0.3 to +38.0	V
	V _(UL,VL,WL)	-0.3 to +7.0	V
FG terminal voltage	V _{FG}	-0.3 to +7.0	V
Voltage of other output terminals	V _{IO}	-0.3 to +5.5	V
Output current of pre-driver	I _{OMAX(OUT)}	±15* ¹	mA
FG output current	I _{OMAX(FG)}	5* ¹	mA
VREG output current	I _{OMAX(VREG)}	-30* ¹	mA
Operating temperature range	T _{OPR}	-40 to +85	°C
Storage temperature range	T _{STG}	-55 to +150	°C
Power dissipation	P _d	0.38* ²	W
Junction temperature	T _{Jmax}	150	°C

*1 Do not exceed Pd.

*2 IC only. Derating in done at 3.04mW/°C for operating above Ta=25°C.

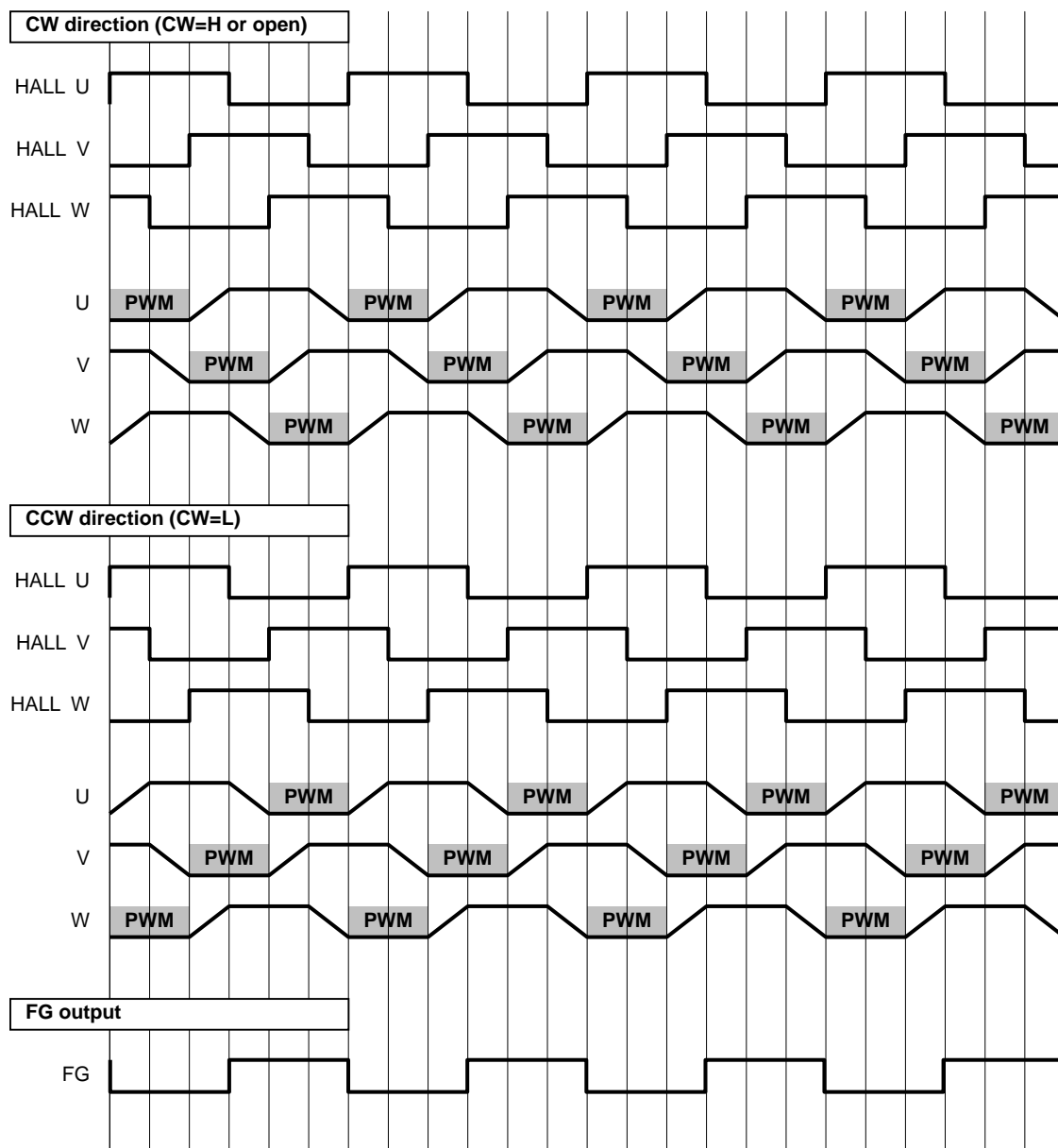
● Recommended Operating Range (Ta=25°C)

Item	Symbol	Range	Unit
Power supply voltage	V _{CC}	8.0 to 26.4	V

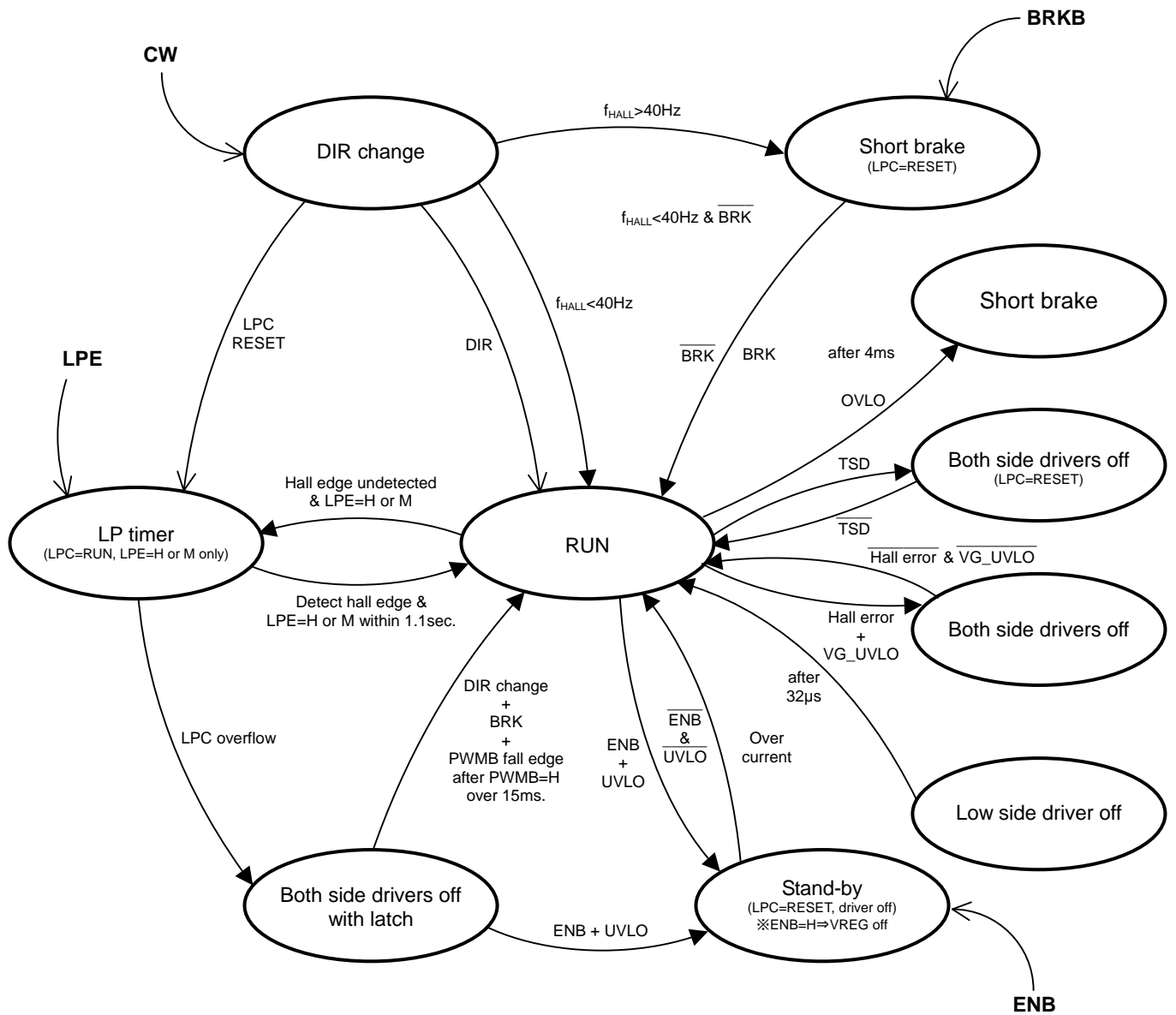
●Electrical Characteristic (Unless otherwise specified Ta=25°C, V_{CC}=24V)

Item	Symbol	Limit			Unit	Condition
		Min.	Typ.	Max.		
[Whole]						
Circuit current	I _{CC}	-	2.5	5.0	mA	V _{ENB} =0V
Stand-by current	I _{STBY}	-	0.6	1.2	mA	ENB=OPEN
VREG voltage	V _{REG}	4.5	5.0	5.5	V	I _O =-10mA
[Booster circuit]						
VG voltage	V _G	V _{CC} +6	V _{CC} +7	V _{CC} +8	V	
VG UVLO voltage	V _{GUVON}	V _{CC} +3	V _{CC} +4	V _{CC} +5	V	
[Driver output]						
High side output voltage H	V _{OHH}	V _G -0.6	V _G -0.2	V _G	V	I _O =-5mA
High side output voltage L	V _{OHL}	0	0.2	0.6	V	I _O =5mA
Low side output voltage H	V _{OLH}	4.1	4.8	5.5	V	I _O =-5mA
Low side output voltage L	V _{OLL}	0	0.2	0.6	V	I _O =5mA
[Hall input]						
Input bias current	I _{HALL}	-2.0	-0.1	2.0	μA	V _{IN} =0V
Range of in-phase input voltage	V _{HALLCM}	0	-	V _{REG} -1.7	V	
Minimum input voltage	V _{HALLMIN}	50	-	-	mV _{p-p}	
HYS level +	V _{HALLHY+}	5	15	25	mV	
HYS level -	V _{HALLHY-}	-25	-15	-5	mV	
[Input of control : ENB]						
Input current	I _{ENB}	-75	-45	-25	μA	V _{ENB} =0V
Standby voltage	V _{STBY}	2.0	-	V _{REG}	V	
Enable voltage	V _{ENA}	0	-	0.8	V	
[Input of control : PWMB, CW, BRKB]						
Input current	I _{IN}	-80	-50	-30	μA	V _{IN} =0V
Voltage input H	V _{INH}	2.0	-	V _{REG}	V	
Voltage input L	V _{INL}	0	-	0.8	V	
Minimum input pulse width	t _{PLSMIN}	1	-	-	msec	CW, BRKB
[Input of control : LPE E]						
Input current	I _{LPE}	-80	-50	-30	μA	V _{LPE} =0V
Input voltage H	V _{LPH}	0.8 × V _{REG}	-	V _{REG}	V	
Input voltage M	V _{LPM}	0.4 × V _{REG}	-	0.6 × V _{REG}	V	
Input voltage L	V _{LPL}	0	-	0.2 × V _{REG}	V	
[FG output]						
Output voltage L	V _{FGOL}	0	0.1	0.3	V	I _O =2mA
[Current limit]						
Detect voltage	V _{CL}	0.18	0.20	0.22	V	
[UVLO]						
Release voltage	V _{UVH}	6.5	7.0	7.5	V	
Lock out voltage	V _{UVL}	5.5	6.0	6.5	V	
[OVLO]						
Release voltage1	V _{OVL1}	14.0	15.0	16.0	V	LPE="M"
Lock out voltage1	V _{OVH1}	15.0	16.0	17.0	V	LPE="M"
Release voltage2	V _{OVL2}	26.5	27.5	28.5	V	LPE="H" or "L"
Lock out voltage2	V _{OVH2}	27.5	28.5	29.5	V	LPE="H" or "L"

●Timing Chart



● State transition diagram



Legend:

- DIR: motor rotational direction
- LP: motor lock protection
- LPC: internal counter for the motor lock protection (watch-dog timer)
- f_{HALL} : hall signal frequency
- Hall error: HU=HV=HW
- &: logical "AND"
- +: logical "OR"



Note) All values are typical.

● I/O equivalence circuits

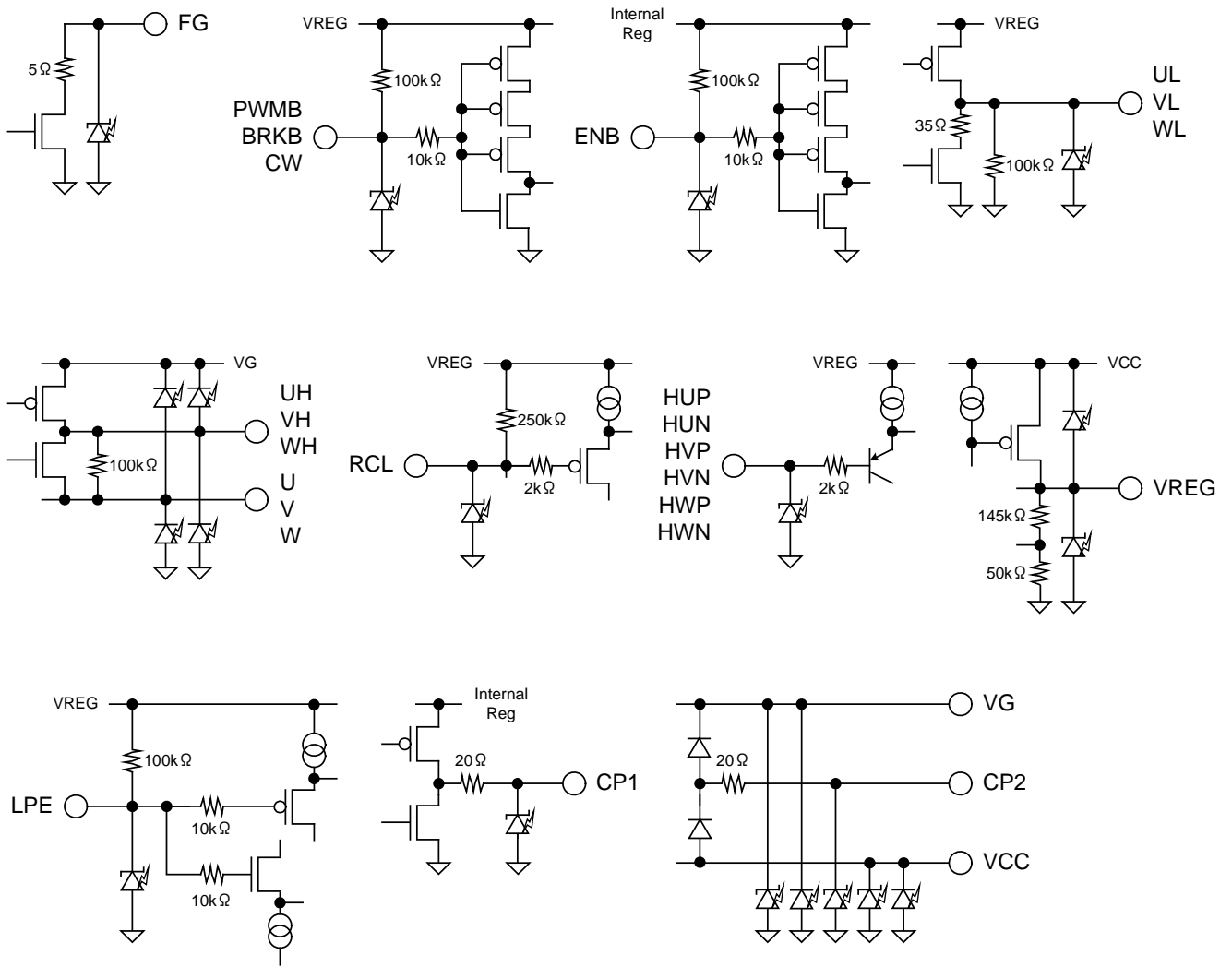


Fig.4 I/O equivalence circuits

● Power Dissipation

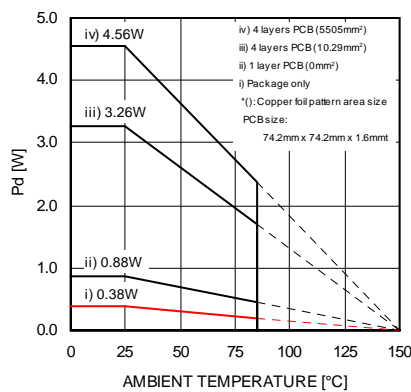


Fig.5 Derating curve (VQFN028V5050)

Package thermal resistor

Board	θ_{j-a} [°C/W]
Board (4)	27.4
Board (3)	38.3
Board (2)	142.0
Board (1)	328.9

Note) Values about heat reducing curve and packaged thermal resistor are tested values.

●Operational Notes

- 1) Absolute maximum ratings
An excess in the absolute maximum ratings, such as supply voltage, temperature range of operating conditions, etc., can break down the devices, thus making impossible to identify breaking mode, such as a short circuit or an open circuit. If any over rated values will expect to exceed the absolute maximum ratings, consider adding circuit protection devices, such as fuses.
- 2) Connecting the power supply connector backward
Connecting of the power supply in reverse polarity can damage IC. Take precautions when connecting the power supply lines. An external direction diode can be added.
- 3) Power supply Lines
Design PCB layout pattern to provide low impedance GND and supply lines. To obtain a low noise ground and supply line, separate the ground section and supply lines of the digital and analog blocks. Furthermore, for all power supply terminals to ICs, connect a capacitor between the power supply and the GND terminal. When applying electrolytic capacitors in the circuit, not that capacitance characteristic values are reduced at low temperatures.
- 4) GND
The potential of GND pin must be minimum potential in all operating conditions. When using both small signal and large current GND patterns, it is recommended to isolate the two ground patterns, placing a single ground point at the application's reference point so that the pattern wiring resistance and voltage variations caused by large currents do not cause variations in the small signal ground voltage. Be careful not to change the GND wiring pattern potential of any external components, either.
- 5) Metal on the backside (Define the side where product markings are printed as front)
The metal on the backside is shorted with the backside of IC chip therefore it should be connected to GND. Be aware that there is a possibility of malfunction or destruction if it is shorted with any potential other than GND.
- 6) Thermal design
Use a thermal design that allows for a sufficient margin in light of the power dissipation (Pd) in actual operating conditions.
- 7) Inter-pin shorts and mounting errors
When attaching to a printed circuit board, pay close attention to the direction of the IC and displacement. Improper attachment may lead to destruction of the IC. There is also possibility of destruction from short circuits which can be caused by foreign matter entering between outputs or an output and the power supply or GND.
- 8) Operation in a strong electric field
Use caution when using the IC in the presence of a strong electromagnetic field as doing so may cause the IC to malfunction.
- 9) Thermal shutdown circuit
The IC has a built-in thermal shutdown circuit (TSD circuit). If the chip temperature becomes $T_{jmax}=150^{\circ}\text{C}$, and higher, coil output to the motor will be open. The TSD circuit is designed only to shut the IC off to prevent runaway thermal operation. It is not designed to protect or indemnify peripheral equipment. Do not use the TSD function to protect peripheral equipment.

TSD ON temperature [°C] (Typ.)	Hysteresis temperature [°C] (Typ.)
175	25

- 10) Inspection of the application board
During inspection of the application board, if a capacitor is connected to a pin with low impedance there is a possibility that it could cause stress to the IC, therefore an electrical discharge should be performed after each process. Also, as a measure again electrostatic discharge, it should be earthed during the assembly process and special care should be taken during transport or storage. Furthermore, when connecting to the jig during the inspection process, the power supply should first be turned off and then removed before the inspection.

11) Input terminal of IC

This IC is a monolithic IC, and between each element there is a P+ isolation for element partition and a P substrate. This P layer and each element's N layer make up the P-N junction, and various parasitic elements are made up. For example, when the resistance and transistor are connected to the terminal as shown in figure 6,

○When GND > (Pin A) at the resistance and GND > (Pin B) at the transistor (NPN),

the P-N junction operates as a parasitic diode.

○Also, when GND > (Pin B) at the transistor (NPN)

The parasitic NPN transistor operates with the N layers of other elements close to the aforementioned parasitic diode.

Because of the IC's structure, the creation of parasitic elements is inevitable from the electrical potential relationship. The operation of parasitic elements causes interference in circuit operation, and can lead to malfunction and destruction. Therefore, be careful not to use it in a way which causes the parasitic elements to operate, such as by applying voltage that is lower than the GND (P substrate) to the input terminal.

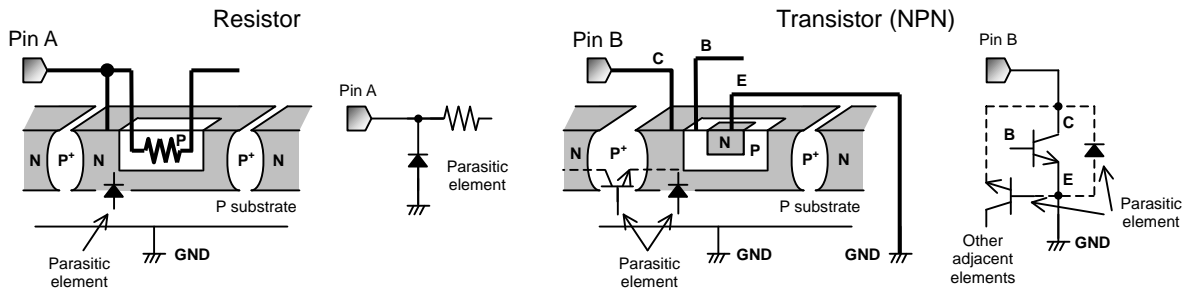


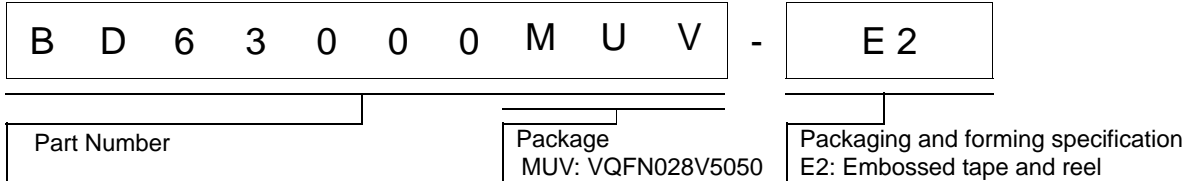
Fig.6 Pattern Diagram of Parasitic Element

Status of this document

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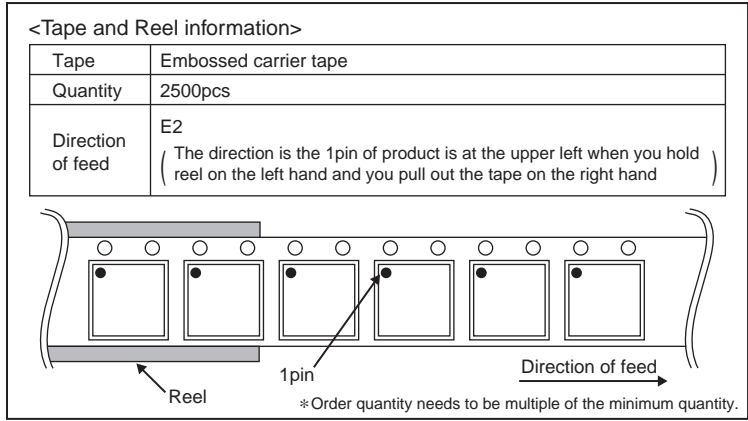
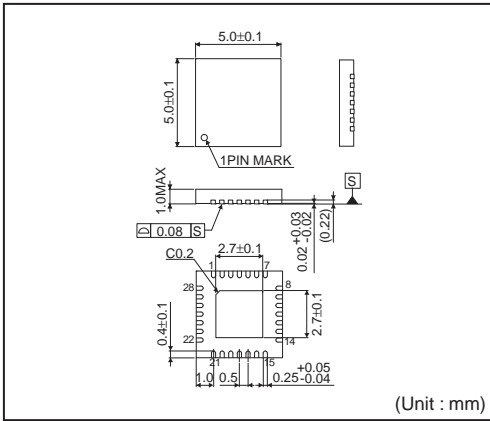
If there are any differences in translation version of this document formal version takes priority

●Ordering Information



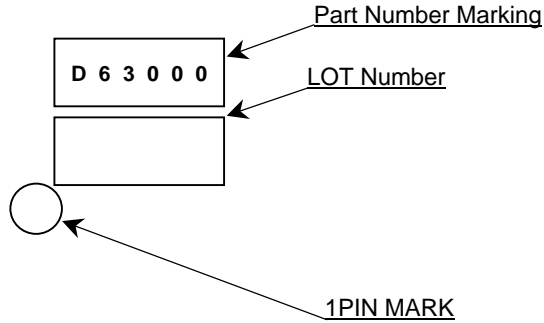
●Physical Dimension Tape and Reel Information

VQFN028V5050



●Marking Diagram

VQFN028V5050 (TOP VIEW)



●Revision History

Date	Revision	Changes
11.May.2012	001	New Release

Notice

Precaution on using ROHM Products

- Our Products are designed and manufactured for application in ordinary electronic equipments (such as AV equipment, OA equipment, telecommunication equipment, home electronic appliances, amusement equipment, etc.). If you intend to use our Products in devices requiring extremely high reliability (such as medical equipment ^(Note 1), transport equipment, traffic equipment, aircraft/spacecraft, nuclear power controllers, fuel controllers, car equipment including car accessories, safety devices, etc.) and whose malfunction or failure may cause loss of human life, bodily injury or serious damage to property ("Specific Applications"), please consult with the ROHM sales representative in advance. Unless otherwise agreed in writing by ROHM in advance, ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of any ROHM's Products for Specific Applications.

(Note1) Medical Equipment Classification of the Specific Applications

JAPAN	USA	EU	CHINA
CLASS III	CLASS III	CLASS II b	CLASS III
CLASS IV		CLASS III	

- ROHM designs and manufactures its Products subject to strict quality control system. However, semiconductor products can fail or malfunction at a certain rate. Please be sure to implement, at your own responsibilities, adequate safety measures including but not limited to fail-safe design against the physical injury, damage to any property, which a failure or malfunction of our Products may cause. The following are examples of safety measures:
 - Installation of protection circuits or other protective devices to improve system safety
 - Installation of redundant circuits to reduce the impact of single or multiple circuit failure
- Our Products are designed and manufactured for use under standard conditions and not under any special or extraordinary environments or conditions, as exemplified below. Accordingly, ROHM shall not be in any way responsible or liable for any damages, expenses or losses arising from the use of any ROHM's Products under any special or extraordinary environments or conditions. If you intend to use our Products under any special or extraordinary environments or conditions (as exemplified below), your independent verification and confirmation of product performance, reliability, etc. prior to use, must be necessary:
 - Use of our Products in any types of liquid, including water, oils, chemicals, and organic solvents
 - Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
 - Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - Sealing or coating our Products with resin or other coating materials
 - Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - Use of the Products in places subject to dew condensation
- The Products are not subject to radiation-proof design.
- Please verify and confirm characteristics of the final or mounted products in using the Products.
- In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- Confirm that operation temperature is within the specified range described in the product specification.
- ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- In principle, the reflow soldering method must be used; if flow soldering method is preferred, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

Precautions Regarding Application Examples and External Circuits

1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
2. You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of ionizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

Precaution for Product Label

QR code printed on ROHM Products label is for ROHM's internal use only.

Precaution for Disposition

When disposing Products please dispose them properly using an authorized industry waste company.

Precaution for Foreign Exchange and Foreign Trade act

Since our Products might fall under controlled goods prescribed by the applicable foreign exchange and foreign trade act, please consult with ROHM representative in case of export.

Precaution Regarding Intellectual Property Rights

1. All information and data including but not limited to application example contained in this document is for reference only. ROHM does not warrant that foregoing information or data will not infringe any intellectual property rights or any other rights of any third party regarding such information or data. ROHM shall not be in any way responsible or liable for infringement of any intellectual property rights or other damages arising from use of such information or data.:
2. No license, expressly or implied, is granted hereby under any intellectual property rights or other rights of ROHM or any third parties with respect to the information contained in this document.

Other Precaution

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3. In no event shall you use in any way whatsoever the Products and the related technical information contained in the Products or this document for any military purposes, including but not limited to, the development of mass-destruction weapons.
4. The proper names of companies or products described in this document are trademarks or registered trademarks of ROHM, its affiliated companies or third parties.

General Precaution

1. Before you use our Products, you are requested to carefully read this document and fully understand its contents. ROHM shall not be in any way responsible or liable for failure, malfunction or accident arising from the use of any ROHM's Products against warning, caution or note contained in this document.
2. All information contained in this document is current as of the issuing date and subject to change without any prior notice. Before purchasing or using ROHM's Products, please confirm the latest information with a ROHM sales representative.
3. The information contained in this document is provided on an "as is" basis and ROHM does not warrant that all information contained in this document is accurate and/or error-free. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties resulting from inaccuracy or errors of or concerning such information.