## SI-8000GL Series

Compact, Separate Excitation Step-down Switching Mode

## Features

- DIP 8 pin package
- Output current: 1.5A
- High efficiency: $86 \%$ (at $\mathrm{Vin}=20 \mathrm{~V}$, lo $=1 \mathrm{~A}$, Vo $=5 \mathrm{~V}$ )
- Capable of downsize a choke-coil due to IC's high switching frequency ( 250 kHz ). (Compared with conventional Sanken devices)
- The output-voltage-variable type can vary its output voltage from 1 V to 14 V because of its low reference voltage (Vref) of 1 V .
- Wide Input Voltage Range (8 to 50V)
- Output ON/OFF available
- Built-in overcurrent protection and thermal protection circuits


## Applications

- Onboard local power supplies
- OA equipment
- For stabilization of the secondary-side output voltage of switching power supplies

■Recommended Operating Conditions

| Parameter | Symbol | Ratings | Unit |
| :---: | :---: | :---: | :---: |
|  |  | SI-8010GL |  |
| DC Input Voltage Range | Vin | ( 8 or $\mathrm{Vo}+3$ ) ${ }^{\text {¹ }}$ to 50 | V |
| Output Voltage Range | Vo | 1 to 14 | V |
| Output Current Range ${ }^{2}$ | 10 | 0.02 to $1.5^{*}$ | A |
| Operating Junction Temperature Range | Tiop | -30 to +125 | ${ }^{\circ} \mathrm{C}$ |
| Operating Temperature Range | Top | -30 to +125 | ${ }^{\circ} \mathrm{C}$ |

*1: The minimum value of an input voltage range is the higher of either 8 V or $\mathrm{Vo}+3 \mathrm{~V}$.
*2: Please be sure to let the output current run more than 20 mA . When using by less than 20 mA , there is a possibility that the output voltage becomes unstable.

## Electrical Characteristics

| Parameter |  | Symbol | Ratings |  |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | SI-8010GL (Variable type) |  |
|  |  | min. | typ. | max. |  |
| Reference Voltage |  |  | Vref | 0.97 | 1.00 | 1.03 | V |
|  |  | Conditions | V IN=12V, $\mathrm{lo}=1 \mathrm{~A}$ |  |  |  |  |
| Efficiency |  |  |  | V IN $=20 \mathrm{~V}, \mathrm{lo}=1 \mathrm{~A}, \mathrm{VO}=5 \mathrm{~V}$ |  |  | \% |
|  |  | Conditions |  |  |  |  |  |
| Oscillation Frequency |  | Fosc |  | 250 |  | kHz |  |
|  |  | Conditions | V IN=12V, $\mathrm{lo}=1 \mathrm{~A}$ |  |  |  |  |
| Line Regulation |  | $\triangle$ Voline |  | 20 | 40 | mV |  |
|  |  | Conditions | $\mathrm{VIN}=10$ to $30 \mathrm{~V}, \mathrm{lo}=1 \mathrm{~A}$ |  |  |  |  |
| Load Regulation |  | $\Delta$ Voload |  | 10 | 30 | mV |  |
|  |  | Conditions | $\mathrm{V} / \mathrm{N}=12 \mathrm{~V}, \mathrm{lo}=0.1$ to 1.5 A |  |  |  |  |
| Temperature Coefficient of Reference Voltage |  | $\Delta \mathrm{V}_{\text {ref }} / \Delta \mathrm{Ta}_{\text {a }}$ |  | $\pm 0.5$ |  | $\mathrm{mV} /{ }^{\circ} \mathrm{C}$ |  |
| Overcurrent Protection Starting Current |  | Is | 1.6 |  |  | A |  |
|  |  | Conditions | V IN=12V |  |  | A |  |
| Quiescent Circuit Current |  | 1 l | VIN=12V, $\mathrm{lo}=0 \mathrm{~A}$ |  |  | mA |  |
|  |  | Conditions |  |  |  |  |  |  |  |  |
| Circuit Current at Output OFF |  | la(off) |  |  | 400 | $\mu \mathrm{A}$ |  |
|  |  | Conditions | VIN=12V, Von/off=0.3V |  |  |  |  |
| CE/SS* <br> Terminal | Low Level Voltage | VssL |  |  | 0.5 | V |  |
|  | Terminal Outflow Current at Low Voltage | IssL |  |  | 50 | $\mu \mathrm{A}$ |  |
|  |  | Conditions | VssL=0V |  |  |  |  |

*: Pin 2 is the CE/SS pin. Soft start at power on can be performed with a capacitor connected to this pin.The output can also be turned ON/OFF with this pin. The output is stopped by setting the voltage of this pin to VssL or lower. CE/SS-pin voltage can be changed with an open-collector drive circuit of a transistor.
When using both the soft-start and ON/OFF functions together, the discharge current from $\mathrm{C}_{4}$ flows into the ON/OFF control transistor. Therefore, limit the current securely to protect the transistor if $\mathrm{C}_{3}$ capacitance is large. The $\mathrm{CE} / \mathrm{SS}$ pin is pulled up to the power supply in the IC, so applying the external voltage is prohibited.


Vo. ON/OFF


Soft start


Soft start +Vo. ON/OFF


Pin Assignment

1. GND
. CE/SS
2. Reg
3. SWout
4. Vin
5. B.S
6. Comp
. Vref

Plastic Mold Package Type
Flammability: UL 94V-0
Product Mass: Approx. 0.49 g

## ■Block Diagram

SI-8010GL


## Ta-Pd Characteristics



Note 1: The efficiency depends on the input voltage and the output current. Therefore, obtain the value from the efficiency graph and substitute the percentage in the formula above.
Note 2: Thermal design for D1 must be considered separately.

Vo : Output voltage
VIn : Input voltage
Io : Output current
$\eta \chi$ : Efficiency
$V_{F}$ : Diode $\mathrm{D}_{1}$ forward voltage RK16 $\cdots 0.4 \mathrm{~V}(\mathrm{lo}=1 \mathrm{~A})$

## ■Typical Connection Diagram



