

# SILICON TRANSISTOR 2SC1623A

## NPN SILICON EPITAXIAL TRANSISTOR MINI MOLD

#### **FEATURES**

• High DC Current Gain:

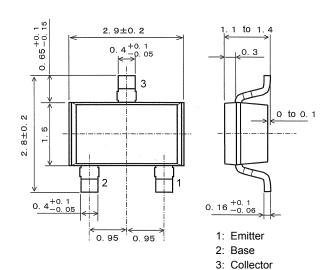
 $h_{FE} = 200 \text{ TYP.}$  (Vce = 6.0 V, Ic = 1.0 mA)

• High Voltage: VCEO = 50 V

#### ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Collector to Base Voltage	Vсво	60	V
Collector to Emitter Voltage	VCEO	50	V
Emitter to Base Voltage	$V_{EBO}$	5.0	V
Collector Current (DC)	Ic	100	mΑ
Total Power Dissipation	Рт	200	mW
Junction Temperature	Tj	150	°C
Storage Temperature Range	Tsta	-55 to +150	°C

#### PACKAGE DRAWING (Unit: mm)



### **ELECTRICAL CHARACTERISTICS (TA = 25°C)**

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Collector Cut-off Current	Ісво			0.1	μΑ	V <sub>CB</sub> = 60 V, I <sub>E</sub> = 0 A
Emitter Cut-off Current	ІЕВО			0.1	μΑ	V <sub>EB</sub> = 5.0 V, I <sub>C</sub> = 0 A
DC Current Gain	hfe	90	200	600		V <sub>CE</sub> = 6.0 V, I <sub>C</sub> = 1.0 mA <sup>Note</sup>
Collector Saturation Voltage	V <sub>CE(sat)</sub>		0.15	0.3	V	Ic = 100 mA, I <sub>B</sub> = 10 mA <sup>Note</sup>
Base to Saturation Voltage	V <sub>BE(sat)</sub>		0.86	1.0	V	Ic = 100 mA, I <sub>B</sub> = 10 mA <sup>Note</sup>
Base to Emitter voltage	VBE	0.55	0.62	0.65	V	V <sub>CE</sub> = 6.0 V, I <sub>C</sub> = 1.0 mA <sup>Note</sup>
Gain Bandwidth Product	f⊤		250		MHz	VcE = 6.0 V, IE = -10 mA
Output Capacitance	Cob		3.0		pF	V <sub>CB</sub> = 6.0 V, I <sub>E</sub> = 0 A, f = 1.0 MHz

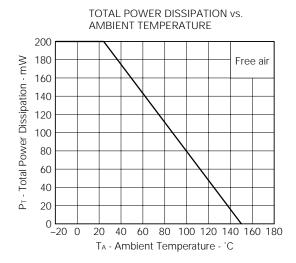
**Note** Pulsed: PW  $\leq$  350  $\mu$ s, Duty Cycle  $\leq$  2%

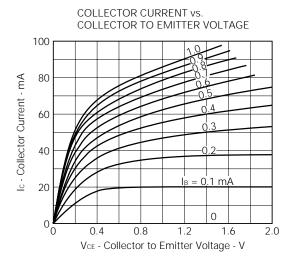
#### **hfe CLASSIFICATION**

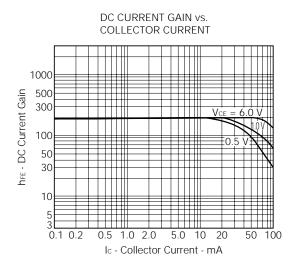
Marking	L4	L5	L6	L7
hfe	90 to 180	135 to 270	200 to 400	300 to 600

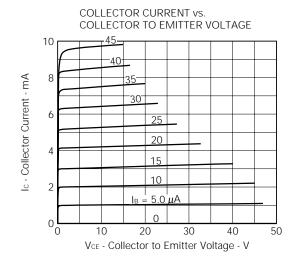
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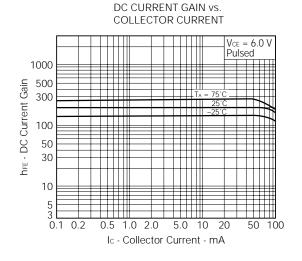
#### <R> TYPICAL CHARACTERISTICS (TA = 25°C)



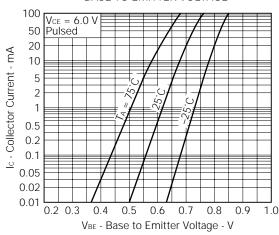




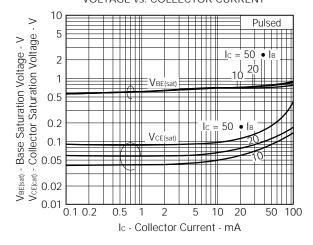




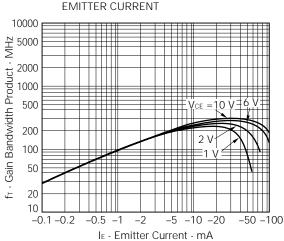
### COLLECTOR CURRENT vs. BASE TO EMITTER VOLTAGE



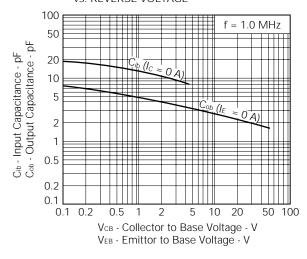
### COLLECTOR AND BASE SATURATION VOLTAGE vs. COLLECTOR CURRENT



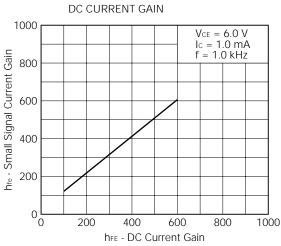
### GAIN BANDWIDTH PRODUCT vs.



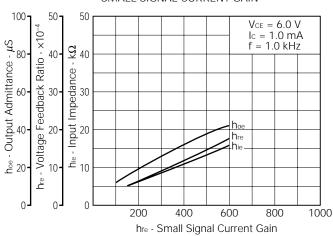
### INPUT AND OUTPUT CAPACITANCE vs. REVERSE VOLTAGE



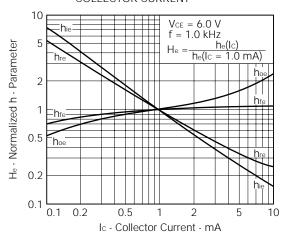
### SMALL SIGNAL CURRENT GAIN vs.



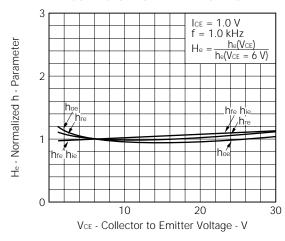
#### INPUT IMPEDANCE VOLTAGE FEEDBACK RATIO AND OUTPUT ADMITTANCE vs. SMALL SIGNAL CURRENT GAIN



### NORMALIZED h-PARAMETER vs. COLLECTOR CURRENT



### NORMALIZED h-PARAMETER vs. COLLECTOR TO EMITTER VOLTAGE



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